

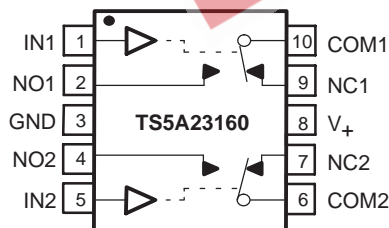
## Description

The TS5A23160 is a dual single-pole double-throw (SPDT) analog switch that is designed to operate from 1.65 V to 5.5 V. The device offers a low ON-state resistance and an excellent channel-to-channel ON-resistance matching. The device has excellent total harmonic distortion (THD) performance and consumes very low power. These features make this device suitable for portable audio applications.

## Applications

- Cell Phones
- PDAs
- Portable Instrumentation
- Audio and Video Signal Routing
- Low-Voltage Data Acquisition Systems
- Communication Circuits
- Modems
- Hard Drives
- Computer Peripherals
- Wireless Terminals and Peripherals

**VSSOP PACKAGE**  
**(TOP VIEW)**



**FUNCTION TABLE**

IN	NC TO COM, COM TO NC	NO TO COM, COM TO NO
L	ON	OFF
H	OFF	ON

## Features

- Specified Make-Before-Break Switching
- Low ON-State Resistance (1 Ω)
- Control Inputs Are 5.5-V Tolerant
- Low Charge Injection
- Excellent ON-State Resistance Matching
- Low Total Harmonic Distortion (THD)
- 1.65-V to 5.5-V Single-Supply Operation
- Latch-Up Performance Exceeds 100 mA Per JESD 78, Class II
- ESD Performance Tested Per JESD 22
  - 2000-V Human-Body Model (A114-B, Class II)
  - 1000-V Charged-Device Model (C101)

## Summary of Characteristics

$V_+ = 5\text{ V}$ ,  $T_A = 25^\circ\text{C}$

Configuration	Dual 2:1 Multiplexer/ Demultiplexer (2 × SPDT)
Number of channels	2
ON-state resistance ( $r_{ON}$ )	0.9 Ω
ON-state resistance match ( $\Delta r_{ON}$ )	0.1 Ω
ON-state resistance flatness ( $r_{ON}(\text{flat})$ )	0.15 Ω
Turn-on/turn-off time ( $t_{ON}/t_{OFF}$ )	2.5 ns/6 ns
Make-before-break time ( $t_{MBB}$ )	5.5 ns
Charge injection ( $Q_C$ )	1 pC
Bandwidth (BW)	95 MHz
OFF isolation ( $O_{ISO}$ )	–64 dB at 1 MHz
Crosstalk ( $X_{TALK}$ )	–64 dB at 1 MHz
Total harmonic distortion (THD)	0.004%
Leakage current ( $I_{NC}(\text{OFF})$ )	±20 nA
Power-supply current ( $I_+$ )	0.1 μA
Package option	10-pin VSSOP



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

**TS5A23160**  
**0.9-Ω DUAL SPDT ANALOG SWITCH**  
**5-V/3.3-V 2-CHANNEL 2:1 MULTIPLEXER/DEMULTIPLEXER**



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**ORDERING INFORMATION**

TA	PACKAGE(1)		ORDERABLE PART NUMBER	TOP-SIDE MARKING
–40°C to 85°C	VSSOP – DGS (MSOP)	Tape and reel	TS5A23160DGSR	PREVIEW

(1) Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at [www.ti.com/sc/package](http://www.ti.com/sc/package).

**Absolute Minimum and Maximum Ratings(1)(2)**

over operating free-air temperature range (unless otherwise noted)

			MIN	MAX	UNIT
V <sub>+</sub>	Supply voltage range(3)		–0.5	6.5	V
V <sub>NC</sub> V <sub>NO</sub> V <sub>COM</sub>	Analog voltage range(3)(4)(5)		–0.5	V <sub>+</sub> + 0.5	V
I <sub>K</sub>	Analog port diode current	V <sub>NC</sub> , V <sub>NO</sub> , V <sub>COM</sub> < 0 or V <sub>NC</sub> , V <sub>NO</sub> , V <sub>COM</sub> > V <sub>+</sub>	–50	50	mA
I <sub>NC</sub> I <sub>NO</sub> I <sub>COM</sub>	On-state switch current	V <sub>NC</sub> , V <sub>NO</sub> , V <sub>COM</sub> = 0 to V <sub>+</sub>	–200	200	mA
	On-state peak switch current(6)		–400	400	
V <sub>I</sub>	Digital input voltage range(3)(4)		–0.5	6.5	V
I <sub>I<sub>K</sub></sub>	Digital input clamp current	V <sub>I</sub> < 0	–50		mA
I <sub>+</sub>	Continuous current through V <sub>+</sub>			100	mA
I <sub>GND</sub>	Continuous current through GND		–100	100	mA
θ <sub>JA</sub>	Package thermal impedance(7)			165	°C/W
T <sub>stg</sub>	Storage temperature range		–65	150	°C

(1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those specified is not implied.

(2) The algebraic convention, whereby the most negative value is a minimum and the most positive value is a maximum.

(3) All voltages are with respect to ground, unless otherwise specified.

(4) The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

(5) This value is limited to 5.5 V maximum.

(6) Pulse at 1-ms duration < 10% duty cycle.

(7) The package thermal impedance is calculated in accordance with JESD 51-7.

## Electrical Characteristics for 5-V Supply<sup>(1)</sup>

$V_+ = 4.5 \text{ V to } 5.5 \text{ V}$ ,  $T_A = -40^\circ\text{C to } 85^\circ\text{C}$  (unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS	$T_A$	$V_+$	MIN	TYP	MAX	UNIT
<b>Analog Switch</b>								
Analog signal range	$V_{COM}, V_{NO}, V_{NC}$				0		$V_+$	V
Peak ON resistance	$r_{peak}$	$0 \leq (V_{NO} \text{ or } V_{NC}) \leq V_+$ , $I_{COM} = -100 \text{ mA}$ , Switch ON, See Figure 13	25°C Full	4.5 V		0.8	1.1 1.5	Ω
ON-state resistance	$r_{on}$	$V_{NO} \text{ or } V_{NC} = 2.5 \text{ V}$ , $I_{COM} = -100 \text{ mA}$ , Switch ON, See Figure 13	25°C Full	4.5 V		0.7	0.9 1.1	Ω
ON-state resistance match between channels	$\Delta r_{on}$	$V_{NO} \text{ or } V_{NC} = 2.5 \text{ V}$ , $I_{COM} = -100 \text{ mA}$ , Switch ON, See Figure 13	25°C Full	4.5 V		0.05	0.1 0.1	Ω
ON-state resistance flatness	$r_{on(flat)}$	$0 \leq (V_{NO} \text{ or } V_{NC}) \leq V_+$ , $I_{COM} = -100 \text{ mA}$ , Switch ON, See Figure 13	25°C Full	4.5 V		0.15 0.1	0.25 0.25	Ω
NC, NO OFF leakage current	$I_{NC(OFF)}, I_{NO(OFF)}$	$V_{NC} \text{ or } V_{NO} = 1 \text{ V}$ , $V_{COM} = 4.5 \text{ V}$ , or $V_{NC} \text{ or } V_{NO} = 4.5 \text{ V}$ , $V_{COM} = 1 \text{ V}$ , Switch OFF, See Figure 14	25°C Full	5.5 V	-20 -150	2	20 150	nA
	$I_{NC(PWROFF)}, I_{NO(PWROFF)}$	$V_{NC} \text{ or } V_{NO} = 0 \text{ to } 5.5 \text{ V}$ , $V_{COM} = 5.5 \text{ V to } 0$ , Switch OFF, See Figure 14	25°C Full	0 V	-1 -20		1 20	μA
NC, NO ON leakage current	$I_{NC(ON)}, I_{NO(ON)}$	$V_{NC} \text{ or } V_{NO} = 1 \text{ V}$ , $V_{COM} = \text{Open}$ , or $V_{NC} \text{ or } V_{NO} = 4.5 \text{ V}$ , $V_{COM} = \text{Open}$ , Switch ON, See Figure 15	25°C Full	5.5 V	-150 -20	2	-150 20	nA
	$I_{COM(PWROFF)}$	$V_{NC} \text{ or } V_{NO} = 0 \text{ to } 5.5 \text{ V}$ , $V_{COM} = 5.5 \text{ V to } 0$ , Switch OFF, See Figure 14	25°C Full	0 V	-1 -20	0.1	1 20	μA
COM ON leakage current	$I_{COM(ON)}$	$V_{NC} \text{ or } V_{NO} = \text{Open}$ , $V_{COM} = 1 \text{ V}$ , or $V_{NC} \text{ or } V_{NO} = \text{Open}$ , $V_{COM} = 4.5 \text{ V}$ , Switch ON, See Figure 15	25°C Full	5.5 V	-20 -150	2	20 150	nA
<b>Digital Control Inputs (IN1, IN2)<sup>(2)</sup></b>								
Input logic high	$V_{IH}$		Full		2.4		5.5	V
Input logic low	$V_{IL}$		Full		0		0.8	V
Input leakage current	$I_{IH}, I_{IL}$	$V_I = 5.5 \text{ V or } 0$	25°C	5.5 V	-2		2	nA
			Full		-1		1	μA

(1) The algebraic convention, whereby the most negative value is a minimum and the most positive value is a maximum

(2) All unused digital inputs of the device must be held at  $V_+$  or GND to ensure proper device operation. Refer to the TI application report, *Implications of Slow or Floating CMOS Inputs*, literature number SCBA004.

**TS5A23160**  
**0.9-Ω DUAL SPDT ANALOG SWITCH**  
**5-V/3.3-V 2-CHANNEL 2:1 MULTIPLEXER/DEMULTIPLEXER**



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**Electrical Characteristics for 5-V Supply<sup>(1)</sup> (continued)**

$V_+ = 4.5\text{ V to }5.5\text{ V}$ ,  $T_A = -40^\circ\text{C to }85^\circ\text{C}$  (unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS	$T_A$	$V_+$	MIN	TYP	MAX	UNIT
<b>Dynamic</b>								
Turn-on time	$t_{ON}$	$V_{COM} = V_+$ , $R_L = 50\ \Omega$ , $C_L = 35\text{ pF}$ , See Figure 17	25°C	5 V	1	2.5	5.5	ns
			Full	4.5 V to 5.5 V	0.5		6.5	
Turn-off time	$t_{OFF}$	$V_{COM} = V_+$ , $R_L = 50\ \Omega$ , $C_L = 35\text{ pF}$ , See Figure 17	25°C	5 V	2	6	10	ns
			Full	4.5 V to 5.5 V	0.5		13.5	
Make-before-break time	$t_{MBB}$	$V_{COM} = V_+$ , $R_L = 50\ \Omega$ , $C_L = 35\text{ pF}$ , See Figure 18	25°C	5 V		5.5		ns
			Full	4.5 V to 5.5 V	2		9.5	
Charge injection	$Q_C$	$V_{GEN} = 0$ , $R_{GEN} = 0$ , $C_L = 1\text{ nF}$ , See Figure 22	25°C	5 V		1		pC
NC, NO OFF capacitance	$C_{NC(OFF)}$ , $C_{NO(OFF)}$	$V_{NC}$ or $V_{NO} = V_+$ or GND, Switch OFF, See Figure 16	25°C	5 V		18		pF
NC, NO ON capacitance	$C_{NC(ON)}$ , $C_{NO(ON)}$	$V_{NC}$ or $V_{NO} = V_+$ or GND, Switch ON, See Figure 16	25°C	5 V		55		pF
COM ON capacitance	$C_{COM(ON)}$	$V_{COM} = V_+$ or GND, Switch ON, See Figure 16	25°C	5 V		55		pF
Digital input capacitance	$C_I$	$V_I = V_+$ or GND, See Figure 16	25°C	5 V		2		pF
Bandwidth	BW	$R_L = 50\ \Omega$ , Switch ON, See Figure 19	25°C	5 V		95		MHz
OFF isolation	OISO	$R_L = 50\ \Omega$ , $f = 1\text{ MHz}$ , Switch OFF, See Figure 20	25°C	5 V		-64		dB
Crosstalk	XTALK	$R_L = 50\ \Omega$ , $f = 1\text{ MHz}$ , Switch ON, See Figure 21	25°C	5 V		-64		dB
Total harmonic distortion	THD	$R_L = 600\ \Omega$ , $C_L = 50\text{ pF}$ , $f = 20\text{ Hz to }20\text{ kHz}$ , See Figure 23	25°C	5 V		0.004		%
<b>Supply</b>								
Positive supply current	$I_+$	$V_I = V_+$ or GND, Switch ON or OFF	25°C	5.5 V		10		nA
			Full			0.5		μA

(1) The algebraic convention, whereby the most negative value is a minimum and the most positive value is a maximum

### Electrical Characteristics for 3.3-V Supply<sup>(1)</sup>

$V_+ = 3\text{ V to }3.6\text{ V}$ ,  $T_A = -40^\circ\text{C to }85^\circ\text{C}$  (unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS		T <sub>A</sub>	V <sub>+</sub>	MIN	TYP	MAX	UNIT
Analog Switch									
Analog signal range	V <sub>COM</sub> , V <sub>NO</sub> , V <sub>NC</sub>					0		V <sub>+</sub>	V
Peak ON resistance	r <sub>peak</sub>	0 ≤ (V <sub>NO</sub> or V <sub>NC</sub> ) ≤ V <sub>+</sub> , I <sub>COM</sub> = −100 mA,	Switch ON, See Figure 13	25 °C Full	3 V	1.3	1.6	2	Ω
ON-state resistance	r <sub>on</sub>	V <sub>NO</sub> or V <sub>NC</sub> = 2 V, I <sub>COM</sub> = −100 mA,	Switch ON, See Figure 13	25°C Full	3 V	1.2	1.5	1.7	Ω
ON-state resistance match between channels	Δr <sub>on</sub>	V <sub>NO</sub> or V <sub>NC</sub> = 2 V, 0.8 V, I <sub>COM</sub> = −100 mA,	Switch ON, See Figure 13	25°C Full	3 V	0.1	0.15	0.15	Ω
ON-state resistance flatness	r <sub>on(flat)</sub>	0 ≤ (V <sub>NO</sub> or V <sub>NC</sub> ) ≤ V <sub>+</sub> , I <sub>COM</sub> = −100 mA,	Switch ON, See Figure 13	25°C	3 V	0.2			Ω
		V <sub>NO</sub> or V <sub>NC</sub> = 2 V, 0.8 V, I <sub>COM</sub> = −100 mA,	Switch ON, See Figure 13	25°C		0.15	0.3		
			Full			0.3			
NC, NO OFF leakage current	I <sub>NC(OFF)</sub> , I <sub>NO(OFF)</sub>	V <sub>NC</sub> or V <sub>NO</sub> = 1 V, V <sub>COM</sub> = 3 V, or V <sub>NC</sub> or V <sub>NO</sub> = 3 V, V <sub>COM</sub> = 1 V,	Switch OFF, See Figure 14	25°C Full	3.6 V	−20	2	20	nA
						−50	50		
	I <sub>NC(PWROFF)</sub> , I <sub>NO(PWROFF)</sub>	V <sub>NC</sub> or V <sub>NO</sub> = 0 to 3.6 V, V <sub>COM</sub> = 3.6 V to 0,	Switch OFF, See Figure 14	25°C Full	0 V	−1	0.2	1	μA
NC, NO ON leakage current	I <sub>NC(ON)</sub> , I <sub>NO(ON)</sub>	V <sub>NC</sub> or V <sub>NO</sub> = 1 V, V <sub>COM</sub> = Open, or V <sub>NC</sub> or V <sub>NO</sub> = 3 V, V <sub>COM</sub> = Open,	Switch ON, See Figure 15	25°C Full	3.6 V	−20	2	20	nA
						−20	20		
COM OFF leakage current	I <sub>COMPWROFF</sub>	V <sub>NC</sub> or V <sub>NO</sub> = 3.6 V to 0, V <sub>COM</sub> = 0 to 3.6 V,	Switch OFF, See Figure 14	25°C Full	0 V	−1	0.2	1	μA
						−15	15		
COM ON leakage current	I <sub>COM(ON)</sub>	V <sub>NC</sub> or V <sub>NO</sub> = Open, V <sub>COM</sub> = 1 V, or V <sub>NC</sub> or V <sub>NO</sub> = Open, V <sub>COM</sub> = 3 V,	Switch ON, See Figure 15	25°C Full	3.6 V	−20	2	20	nA
						−20	20		
Digital Control Inputs (IN1, IN2)(2)									
Input logic high	V <sub>IH</sub>			Full		2		5.5	V
Input logic low	V <sub>IL</sub>			Full		0		0.8	V
Input leakage current	I <sub>IH</sub> , I <sub>IL</sub>	V <sub>I</sub> = 5.5 V or 0		25°C	3.6 V	−2		2	nA
				Full		20	20		

(1) The algebraic convention, whereby the most negative value is a minimum and the most positive value is a maximum

(2) All unused digital inputs of the device must be held at  $V_+$  or GND to ensure proper device operation. Refer to the TI application report, *Implications of Slow or Floating CMOS Inputs*, literature number SCBA004.

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**0.9-Ω DUAL SPDT ANALOG SWITCH**  
**5-V/3.3-V 2-CHANNEL 2:1 MULTIPLEXER/DEMULTIPLEXER**



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**Electrical Characteristics for 3.3-V Supply<sup>(1)</sup> (continued)**

$V_+ = 3\text{ V to }3.6\text{ V}$ ,  $T_A = -40^\circ\text{C to }85^\circ\text{C}$  (unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS	$T_A$	$V_+$	MIN	TYP	MAX	UNIT
<b>Dynamic</b>								
Turn-on time	$t_{ON}$	$V_{COM} = V_+$ , $R_L = 50\ \Omega$ , $C_L = 35\text{ pF}$ , See Figure 17	25°C	3.3 V	1.5	3.5	6.5	ns
			Full	3 V to 3.6 V	0.5		8	
Turn-off time	$t_{OFF}$	$V_{COM} = V_+$ , $R_L = 50\ \Omega$ , $C_L = 35\text{ pF}$ , See Figure 17	25°C	3.3 V	2.5	7	11.5	ns
			Full	3 V to 3.6 V	1		14.5	
Make-before-break time	$t_{MBB}$	$V_{COM} = V_+$ , $R_L = 50\ \Omega$ , $C_L = 35\text{ pF}$ , See Figure 18	25°C	3.3 V		5.5		ns
			Full	3 V to 3.6 V	2		9.5	
Charge injection	$Q_C$	$V_{GEN} = 0$ , $R_{GEN} = 0$ , $C_L = 1\text{ nF}$ , See Figure 22	25°C	3.3 V		3		pC
NC, NO OFF capacitance	$C_{NC(OFF)}$ , $C_{NO(OFF)}$	$V_{NC}$ or $V_{NO} = V_+$ or GND, Switch OFF, See Figure 16	25°C	3.3 V		18		pF
NC, NO ON capacitance	$C_{NC(ON)}$ , $C_{NO(ON)}$	$V_{NC}$ or $V_{NO} = V_+$ or GND, Switch ON, See Figure 16	25°C	3.3 V		56		pF
COM ON capacitance	$C_{COM(ON)}$	$V_{COM} = V_+$ or GND, Switch ON, See Figure 16	25°C	3.3 V		56		pF
Digital input capacitance	$C_I$	$V_I = V_+$ or GND, See Figure 16	25°C	3.3 V		2		pF
Bandwidth	BW	$R_L = 50\ \Omega$ , Switch ON, See Figure 19	25°C	3.3 V		95		MHz
OFF isolation	$O_{ISO}$	$R_L = 50\ \Omega$ , $f = 1\text{ MHz}$ , Switch OFF, See Figure 20	25°C	3.3 V		-64		dB
Crosstalk	$X_{TALK}$	$R_L = 50\ \Omega$ , $f = 1\text{ MHz}$ , Switch ON, See Figure 21	25°C	3.3 V		-64		dB
Total harmonic distortion	THD	$R_L = 600\ \Omega$ , $C_L = 50\text{ pF}$ , $f = 20\text{ Hz to }20\text{ kHz}$ , See Figure 23	25°C	3.3 V		0.01		%
<b>Supply</b>								
Positive supply current	$I_+$	$V_I = V_+$ or GND, Switch ON or OFF	25°C	3.6 V		10		nA
			Full				100	

(1) The algebraic convention, whereby the most negative value is a minimum and the most positive value is a maximum

### Electrical Characteristics for 2.5-V Supply<sup>(1)</sup>

$V_+ = 2.3 \text{ V to } 2.7 \text{ V}$ ,  $T_A = -40^\circ\text{C to } 85^\circ\text{C}$  (unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS	$T_A$	$V_+$	MIN	TYP	MAX	UNIT
<b>Analog Switch</b>								
Analog signal range	$V_{COM}, V_{NO}, V_{NC}$				0		$V_+$	V
Peak ON resistance	$r_{peak}$	$0 \leq (V_{NO} \text{ or } V_{NC}) \leq V_+$ , $I_{COM} = -8 \text{ mA}$ , Switch ON, See Figure 13	25°C Full	2.3 V		1.8 2.5	2.7	Ω
ON-state resistance	$r_{on}$	$V_{NO} \text{ or } V_{NC} = 1.8 \text{ V}$ , $I_{COM} = -8 \text{ mA}$ , Switch ON, See Figure 13	25°C Full	2.3 V		1.5 2	2.4	Ω
ON-state resistance match between channels	$\Delta r_{on}$	$V_{NO} \text{ or } V_{NC} = 1.8 \text{ V}$ , 0.8 V, $I_{COM} = -8 \text{ mA}$ , Switch ON, See Figure 13	25°C Full	2.3 V		0.15 0.2	0.2	Ω
ON-state resistance flatness	$r_{on(flat)}$	$0 \leq (V_{NO} \text{ or } V_{NC}) \leq V_+$ , $I_{COM} = -8 \text{ mA}$ , Switch ON, See Figure 13	25°C Full	2.3 V		0.6 0.6	1 1	Ω
NC, NO OFF leakage current	$I_{NC(OFF)}, I_{NO(OFF)}$	$V_{NC} \text{ or } V_{NO} = 0.5 \text{ V}$ , $V_{COM} = 2.3 \text{ V}$ , or $V_{NC} \text{ or } V_{NO} = 2.3 \text{ V}$ , $V_{COM} = 0.5 \text{ V}$ , Switch OFF, See Figure 14	25°C Full	2.3 V	-20 -50	2	20 50	nA
	$I_{NC(PWROFF)}, I_{NO(PWROFF)}$	$V_{NC} \text{ or } V_{NO} = 0 \text{ to } 2.7 \text{ V}$ , $V_{COM} = 2.7 \text{ V to } 0$ , Switch OFF, See Figure 14	25°C Full	0 V	-1 -10	0.1	1 10	μA
COM OFF leakage current	$I_{COM(PWROFF)}$	$V_{NC} \text{ or } V_{NO} = 2.7 \text{ V to } 0$ , $V_{COM} = 0 \text{ to } 2.7 \text{ V}$ , Switch OFF, See Figure 14	25°C Full	0 V	-1 -10	0.1	1 10	nA
NC, NO ON leakage current	$I_{NC(ON)}, I_{NO(ON)}$	$V_{NC} \text{ or } V_{NO} = 0.5 \text{ V}$ , $V_{COM} = \text{Open}$ , or $V_{NC} \text{ or } V_{NO} = 2.3 \text{ V}$ , $V_{COM} = \text{Open}$ , Switch ON, See Figure 15	25°C Full	2.7 V	-20 -20	2	20 20	nA
COM ON leakage current	$I_{COM(ON)}$	$V_{NC} \text{ or } V_{NO} = \text{Open}$ , $V_{COM} = 0.5 \text{ V}$ , or $V_{NC} \text{ or } V_{NO} = \text{Open}$ , $V_{COM} = 2.3 \text{ V}$ , Switch ON, See Figure 15	25°C Full	2.7 V	-20 -20	2	20 20	nA
<b>Digital Control Inputs (IN1, IN2)<sup>(2)</sup></b>								
Input logic high	$V_{IH}$		Full		1.8		5.5	V
Input logic low	$V_{IL}$		Full		0		0.6	V
Input leakage current	$I_{IH}, I_{IL}$	$V_I = 5.5 \text{ V or } 0$	25°C	2.7 V	-2		2	nA
		$V_I = 5.5 \text{ V or } 0$	Full		-20		20	

(1) The algebraic convention, whereby the most negative value is a minimum and the most positive value is a maximum

(2) All unused digital inputs of the device must be held at  $V_+$  or GND to ensure proper device operation. Refer to the TI application report, *Implications of Slow or Floating CMOS Inputs*, literature number SCBA004.



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**0.9-Ω DUAL SPDT ANALOG SWITCH**  
**5-V/3.3-V 2-CHANNEL 2:1 MULTIPLEXER/DEMULTIPLEXER**



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**Electrical Characteristics for 2.5-V Supply<sup>(1)</sup> (continued)**

$V_+ = 2.3 \text{ V to } 2.7 \text{ V}$ ,  $T_A = -40^\circ\text{C to } 85^\circ\text{C}$  (unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS	$T_A$	$V_+$	MIN	TYP	MAX	UNIT
<b>Dynamic</b>								
Turn-on time	$t_{ON}$	$V_{COM} = V_+$ , $R_L = 50 \Omega$ , $C_L = 35 \text{ pF}$ , See Figure 17	25°C	2.5 V	2	4.5	8.5	ns
			Full	2.3 V to 2.7 V	1		10.5	
Turn-off time	$t_{OFF}$	$V_{COM} = V_+$ , $R_L = 50 \Omega$ , $C_L = 35 \text{ pF}$ , See Figure 17	25°C	2.5 V	3.5	8.5	13.5	ns
			Full	2.3 V to 2.7 V	1.5		16.5	
Make-before-break time	$t_{MBB}$	$V_{COM} = V_+$ , $R_L = 50 \Omega$ , $C_L = 35 \text{ pF}$ , See Figure 18	25°C	2.5 V		6		ns
			Full	2.3 V to 2.7 V	8.5		10	
Charge injection	$Q_C$	$V_{GEN} = 0$ , $R_{GEN} = 0$ , $C_L = 1 \text{ nF}$ , See Figure 22	25°C	2.5 V		4.5		pC
NC, NO OFF capacitance	$C_{NC(OFF)}$ , $C_{NO(OFF)}$	$V_{NC}$ or $V_{NO} = V_+$ or GND, Switch OFF, See Figure 16	25°C	2.5 V		18.5		pF
NC, NO ON capacitance	$C_{NC(ON)}$ , $C_{NO(ON)}$	$V_{NC}$ or $V_{NO} = V_+$ or GND, Switch ON, See Figure 16	25°C	2.5 V		56.5		pF
COM ON capacitance	$C_{COM(ON)}$	$V_{COM} = V_+$ or GND, Switch ON, See Figure 16	25°C	2.5 V		56.5		pF
Digital input capacitance	$C_I$	$V_I = V_+$ or GND, See Figure 16	25°C	2.5 V		2		pF
Bandwidth	BW	$R_L = 50 \Omega$ , Switch ON, See Figure 19	25°C	2.5 V		100		MHz
OFF isolation	$O_{ISO}$	$R_L = 50 \Omega$ , $f = 1 \text{ MHz}$ , Switch OFF, See Figure 20	25°C	2.5 V		-64		dB
Crosstalk	$X_{TALK}$	$R_L = 50 \Omega$ , $f = 1 \text{ MHz}$ , Switch ON, See Figure 21	25°C	2.5 V		-64		dB
Total harmonic distortion	THD	$R_L = 600 \Omega$ , $C_L = 50 \text{ pF}$ , $f = 20 \text{ Hz to } 20 \text{ kHz}$ , See Figure 23	25°C	2.5 V		0.020		%
<b>Supply</b>								
Positive supply current	$I_+$	$V_I = V_+$ or GND, Switch ON or OFF	25°C	2.7 V		10		nA
			Full				50	

(1) The algebraic convention, whereby the most negative value is a minimum and the most positive value is a maximum



### Electrical Characteristics for 1.8-V Supply<sup>(1)</sup>

$V_+ = 1.65\text{ V to }1.95\text{ V}$ ,  $T_A = -40^\circ\text{C to }85^\circ\text{C}$  (unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS	$T_A$	$V_+$	MIN	TYP	MAX	UNIT
<b>Analog Switch</b>								
Analog signal range	$V_{COM}, V_{NO}, V_{NC}$				0		$V_+$	V
Peak ON resistance	$r_{peak}$	$0 \leq (V_{NO} \text{ or } V_{NC}) \leq V_+$ , $I_{COM} = -2\text{ mA}$ , Switch ON, See Figure 13	25°C Full	1.65 V	5		30	Ω
ON-state resistance	$r_{on}$	$V_{NO} \text{ or } V_{NC} = 1.5\text{ V}$ , $I_{COM} = -2\text{ mA}$ , Switch ON, See Figure 13	25°C Full	1.65 V	2		2.5 3.5	Ω
ON-state resistance match between channels	$\Delta r_{on}$	$V_{NO} \text{ or } V_{NC} = 1.5\text{ V}$ , $I_{COM} = -2\text{ mA}$ , Switch ON, See Figure 13	25°C Full	1.65 V	0.15		0.4 0.4	Ω
ON-state resistance flatness	$r_{on(flat)}$	$0 \leq (V_{NO} \text{ or } V_{NC}) \leq V_+$ , $I_{COM} = -2\text{ mA}$ , Switch ON, See Figure 13	25°C Full	1.65 V	5		4.5	Ω
NC, NO OFF leakage current	$I_{NC(OFF)}, I_{NO(OFF)}$	$V_{NC} \text{ or } V_{NO} = 0.3\text{ V}$ , $V_{COM} = 1.65\text{ V}$ , or $V_{NC} \text{ or } V_{NO} = 1.65\text{ V}$ , $V_{COM} = 0.3\text{ V}$ , Switch OFF, See Figure 14	25°C Full	1.95 V	-20	2	20	nA
	$I_{NC(PWROFF)}, I_{NO(PWROFF)}$	$V_{NC} \text{ or } V_{NO} = 0 \text{ to } 1.95\text{ V}$ , $V_{COM} = 1.95\text{ V to } 0$ , Switch OFF, See Figure 14	25°C Full	0 V	-1	0.1	1	μA
NC, NO ON leakage current	$I_{NC(ON)}, I_{NO(ON)}$	$V_{NC} \text{ or } V_{NO} = 0.3\text{ V}$ , $V_{COM} = \text{Open}$ , or $V_{NC} \text{ or } V_{NO} = 1.65\text{ V}$ , $V_{COM} = \text{Open}$ , Switch ON, See Figure 15	25°C Full	1.95 V	-20	2	20	nA
	$I_{COM(PWROFF)}$	$V_{NC} \text{ or } V_{NO} = 1.95\text{ V to } 0$ , $V_{COM} = 0 \text{ to } 1.95\text{ V}$ , Switch OFF, See Figure 14	25°C Full	0 V	-1	0.1	1	nA
COM ON leakage current	$I_{COM(ON)}$	$V_{NC} \text{ or } V_{NO} = \text{Open}$ , $V_{COM} = 0.3\text{ V}$ , or $V_{NC} \text{ or } V_{NO} = \text{Open}$ , $V_{COM} = 1.65\text{ V}$ , Switch ON, See Figure 15	25°C Full	1.95 V	-20	2	20	nA
<b>Digital Control Inputs (IN1, IN2)<sup>(2)</sup></b>								
Input logic high	$V_{IH}$		Full		1.5		5.5	V
Input logic low	$V_{IL}$		Full		0		0.6	V
Input leakage current	$I_{IH}, I_{IL}$	$V_I = 5.5\text{ V or } 0$	25°C	1.95 V	-2		2	nA
			Full		20		20	

(1) The algebraic convention, whereby the most negative value is a minimum and the most positive value is a maximum

(2) All unused digital inputs of the device must be held at  $V_+$  or GND to ensure proper device operation. Refer to the TI application report, *Implications of Slow or Floating CMOS Inputs*, literature number SCBA004.

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**Electrical Characteristics for 1.8-V Supply<sup>(1)</sup> (continued)**

$V_+ = 1.65\text{ V to }1.95\text{ V}$ ,  $T_A = -40^\circ\text{C to }85^\circ\text{C}$  (unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS		T <sub>A</sub>	V <sub>+</sub>	MIN	TYP	MAX	UNIT
Dynamic									
Turn-on time	t <sub>ON</sub>	V <sub>COM</sub> = V <sub>+</sub> , R <sub>L</sub> = 50 Ω,	C <sub>L</sub> = 35 pF, See Figure 17	25°C	1.8 V	2.5	10	14.5	ns
				Full	1.65 V to 1.95 V	1		17	
Turn-off time	t <sub>OFF</sub>	V <sub>COM</sub> = V <sub>+</sub> , R <sub>L</sub> = 50 Ω,	C <sub>L</sub> = 35 pF, See Figure 17	25°C	1.8 V	6.5	12.5	21.5	ns
				Full	1.65 V to 1.95 V	2		24	
Make-before-break time	t <sub>MBB</sub>	V <sub>COM</sub> = V <sub>+</sub> , R <sub>L</sub> = 50 Ω,	C <sub>L</sub> = 35 pF, See Figure 18	25°C	1.8 V		6.5		ns
				Full	1.65 V to 1.95 V	2.5		14	
Charge injection	Q <sub>C</sub>	V <sub>GEN</sub> = 0, R <sub>GEN</sub> = 0,	C <sub>L</sub> = 1 nF, See Figure 22	25°C	1.8 V		5.5		pC
NC, NO OFF capacitance	C <sub>NC(OFF)</sub> , C <sub>NO(OFF)</sub>	V <sub>NC</sub> or V <sub>NO</sub> = V <sub>+</sub> or GND, Switch OFF,	See Figure 16	25°C	1.8 V		18.5		pF
NC, NO ON capacitance	C <sub>NC(ON)</sub> , C <sub>NO(ON)</sub>	V <sub>NC</sub> or V <sub>NO</sub> = V <sub>+</sub> or GND, Switch ON,	See Figure 16	25°C	1.8 V		56.5		pF
COM ON capacitance	C <sub>COM(ON)</sub>	V <sub>COM</sub> = V <sub>+</sub> or GND, Switch ON,	See Figure 16	25°C	1.8 V		56.5		pF
Digital input capacitance	C <sub>I</sub>	V <sub>I</sub> = V <sub>+</sub> or GND,	See Figure 16	25°C	1.8 V		2		pF
Bandwidth	BW	R <sub>L</sub> = 50 Ω, Switch ON,	See Figure 19	25°C	1.8 V		100		MHz
OFF isolation	O <sub>ISO</sub>	R <sub>L</sub> = 50 Ω, f = 1 MHz,	Switch OFF, See Figure 20	25°C	1.8 V		−64		dB
Crosstalk	X <sub>TALK</sub>	R <sub>L</sub> = 50 Ω, f = 1 MHz,	Switch ON, See Figure 21	25°C	1.8 V		−64		dB
Total harmonic distortion	THD	R <sub>L</sub> = 600 Ω, C <sub>L</sub> = 50 pF,	f = 20 Hz to 20 kHz, See Figure 23	25°C	1.8 V		0.060		%
Supply									
Positive supply current	I <sub>+</sub>	V <sub>I</sub> = V <sub>+</sub> or GND,	Switch ON or OFF	25°C	1.95 V				nA
				Full		50			

(1) The algebraic convention, whereby the most negative value is a minimum and the most positive value is a maximum

TYPICAL PERFORMANCE

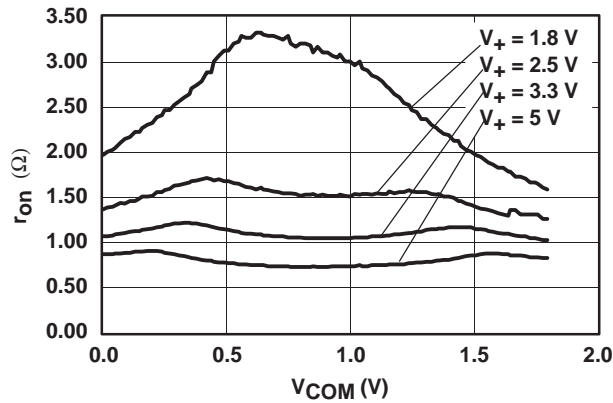


Figure 1.  $r_{on}$  vs  $V_{COM}$

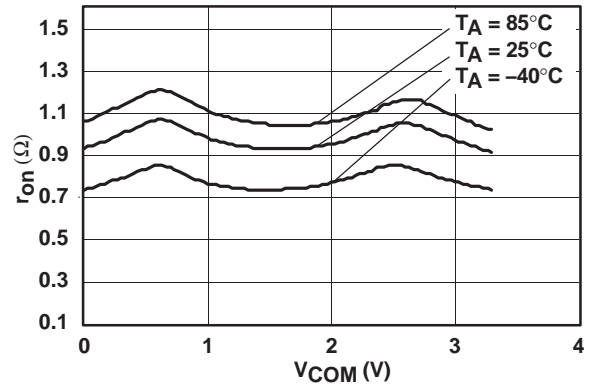


Figure 2.  $r_{on}$  vs  $V_{COM}$  ( $V_+ = 3.3$  V)

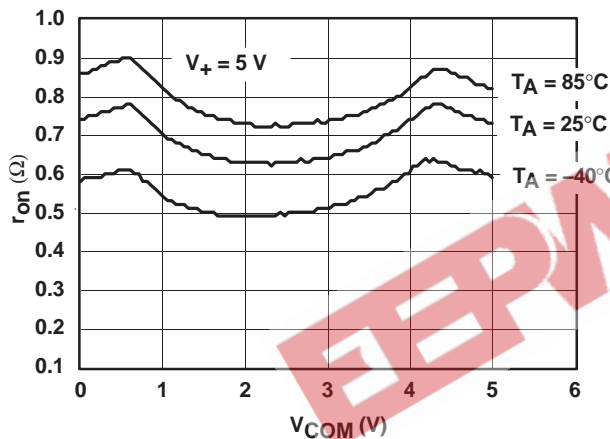


Figure 3.  $r_{on}$  vs  $V_{COM}$

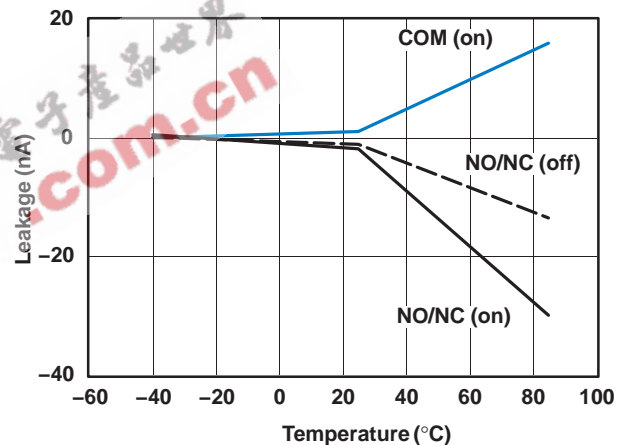


Figure 4. Leakage Current vs Temperature

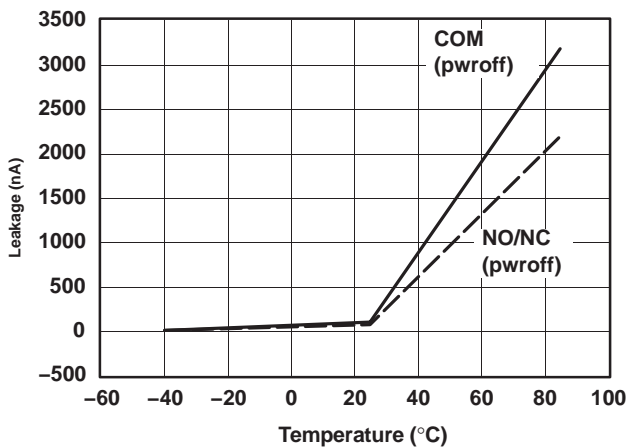


Figure 5. Leakage Current vs Temperature

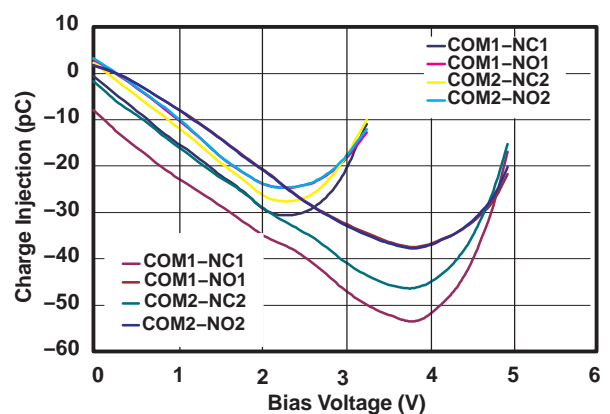


Figure 6. Charge Injection ( $Q_C$ ) vs  $V_{COM}$

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**TYPICAL PERFORMANCE**

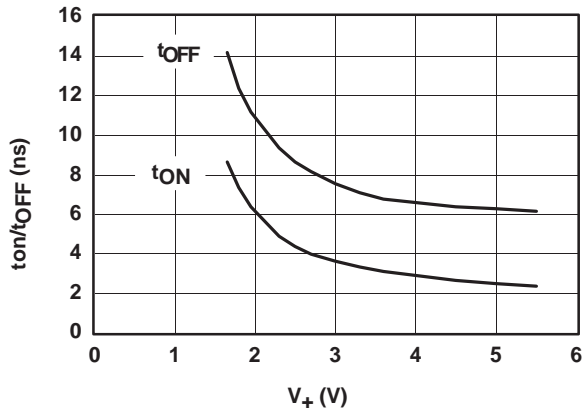


Figure 7. t<sub>ON</sub> and t<sub>OFF</sub> vs Supply Voltage

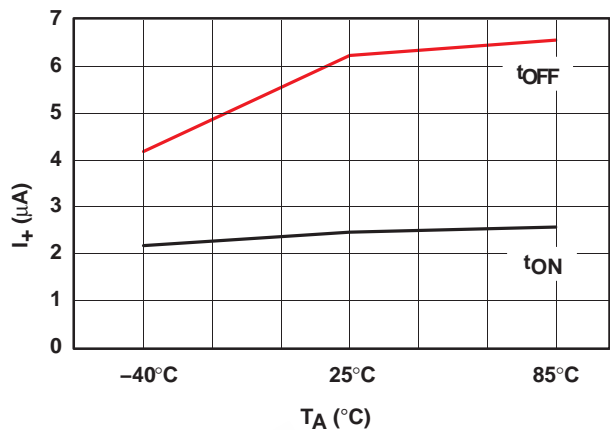


Figure 8. t<sub>ON</sub> and t<sub>OFF</sub> vs Temperature (V<sub>+</sub> = 5 V)

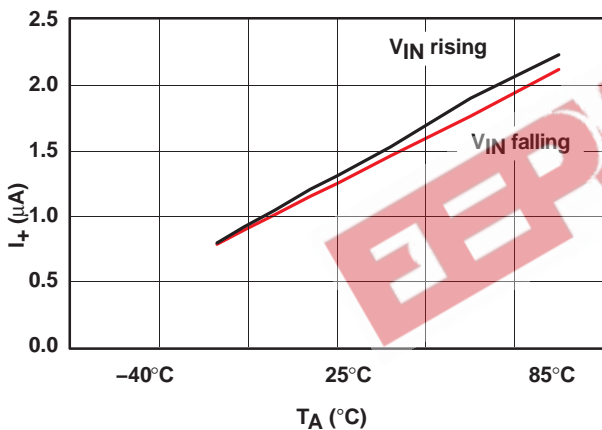


Figure 9. t<sub>ON</sub> and t<sub>OFF</sub> vs Temperature

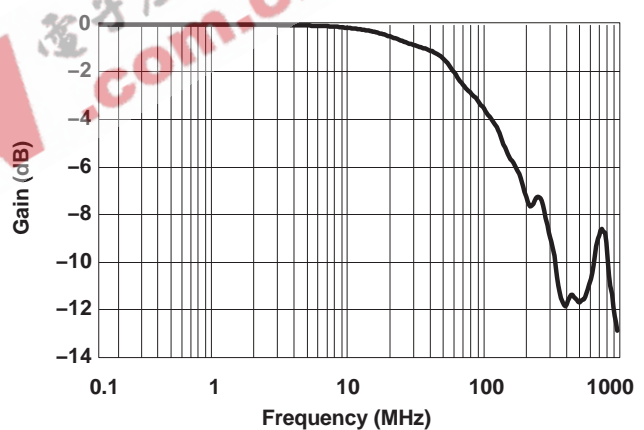


Figure 10. Bandwidth (Gain vs Frequency)  
(V<sub>+</sub> = 5 V)

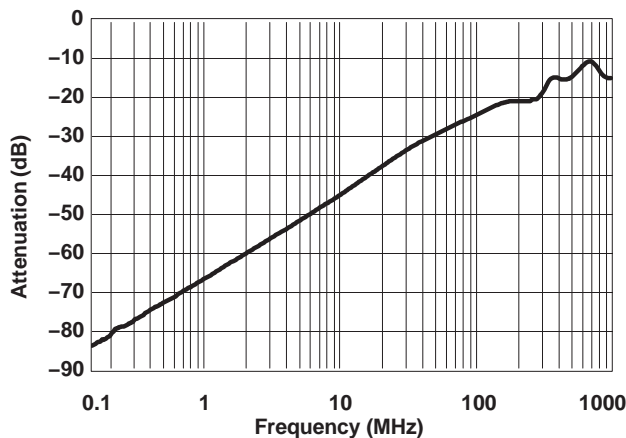


Figure 11. OFF Isolation vs Frequency

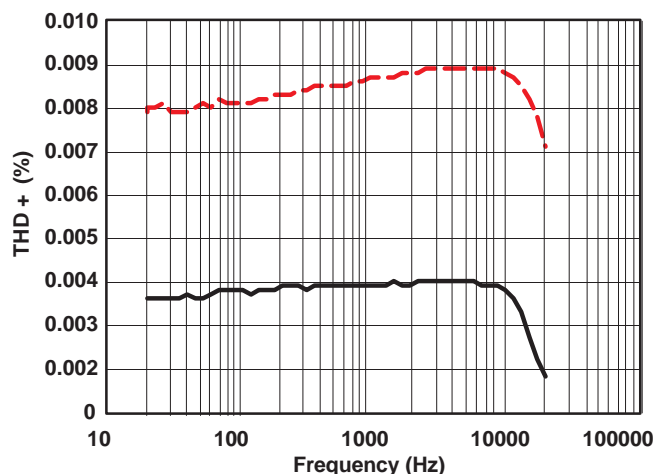


Figure 12. Total Harmonic Distortion  
vs Frequency (V<sub>+</sub> = 5 V)

## TYPICAL PERFORMANCE

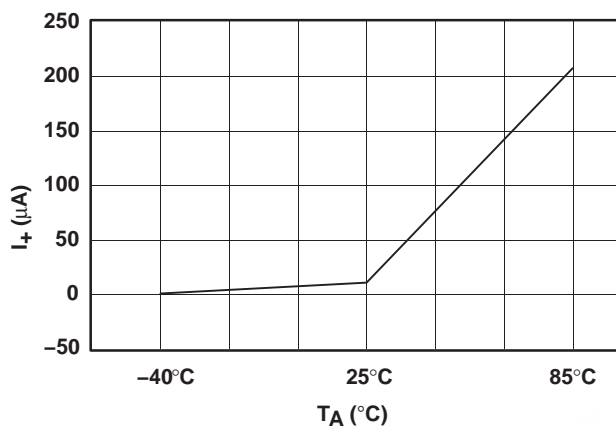


Figure 13. Power-Supply Current vs Temperature ( $V_+ = 5\text{ V}$ )

## PIN DESCRIPTION

PIN	NAME	DESCRIPTION
1	IN1	Digital control pin to connect COM to NO or NC
2	NO1	Normally open
3	GND	Digital ground
4	NO2	Normally open
5	IN2	Digital control to connect COM to NO or NC
6	COM2	Common
7	NC2	Normally closed
8	$V_+$	Power supply
9	NC1	Normally closed
10	COM1	Power supply

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**PARAMETER DESCRIPTION**

SYMBOL	DESCRIPTION
$V_{COM}$	Voltage at COM
$V_{NC}$	Voltage at NC
$V_{NO}$	Voltage at NO
$r_{on}$	Resistance between COM and NC or COM and NO ports when the channel is ON
$r_{peak}$	Peak on-state resistance over a specified voltage range
$\Delta r_{on}$	Difference of $r_{on}$ between channels in a specific device
$r_{on(flat)}$	Difference between the maximum and minimum value of $r_{on}$ in a channel over the specified range of conditions
$I_{NC(OFF)}$	Leakage current measured at the NC port, with the corresponding channel (NC to COM) in the OFF state under worst-case input and output conditions
$I_{NC(PWROFF)}$	Leakage current measured at the NC port during the power-down condition, $V_+ = 0$
$I_{NO(OFF)}$	Leakage current measured at the NO port, with the corresponding channel (NO to COM) in the OFF state under worst-case input and output conditions
$I_{NO(PWROFF)}$	Leakage current measured at the NO port during the power-down condition, $V_+ = 0$
$I_{NC(ON)}$	Leakage current measured at the NC port, with the corresponding channel (NC to COM) in the ON state and the output (COM) open
$I_{NO(ON)}$	Leakage current measured at the NO port, with the corresponding channel (NO to COM) in the ON state and the output (COM) open
$I_{COM(PWROFF)}$	Leakage current measured at the COM port during the power-down condition, $V_+ = 0$
$I_{COM(ON)}$	Leakage current measured at the COM port, with the corresponding channel (COM to NO or COM to NC) in the ON state and the output (NC or NO) open
$V_{IH}$	Minimum input voltage for logic high for the control input (IN)
$V_{IL}$	Maximum input voltage for logic low for the control input (IN)
$V_I$	Voltage at the control input (IN)
$I_{IH}, I_{IL}$	Leakage current measured at the control input (IN)
$t_{ON}$	Turn-on time for the switch. This parameter is measured under the specified range of conditions and by the propagation delay between the digital control (IN) signal and analog output (COM, NC, or NO) signal when the switch is turning ON.
$t_{OFF}$	Turn-off time for the switch. This parameter is measured under the specified range of conditions and by the propagation delay between the digital control (IN) signal and analog output (COM, NC, or NO) signal when the switch is turning OFF.
$t_{MBB}$	Make-before-break time. This parameter is measured under the specified range of conditions and by the propagation delay between the output of two adjacent analog channels (NC and NO) when the control signal changes state.
$Q_C$	Charge injection is a measurement of unwanted signal coupling from the control (IN) input to the analog (NC, NO, or COM) output. This is measured in coulomb (C) and measured by the total charge induced due to switching of the control input. Charge injection, $Q_C = C_L \times \Delta V_{COM}$ . $C_L$ is the load capacitance and $\Delta V_{COM}$ is the change in analog output voltage.

**PARAMETER DESCRIPTION (continued)**

SYMBOL	DESCRIPTION
C <sub>NC(OFF)</sub>	Capacitance at the NC port when the corresponding channel (NC to COM) is OFF
C <sub>NO(OFF)</sub>	Capacitance at the NO port when the corresponding channel (NO to COM) is OFF
C <sub>NC(ON)</sub>	Capacitance at the NC port when the corresponding channel (NC to COM) is ON
C <sub>NO(ON)</sub>	Capacitance at the NO port when the corresponding channel (NO to COM) is ON
C <sub>COM(ON)</sub>	Capacitance at the COM port when the corresponding channel (COM to NC or COM to NO) is ON
C <sub>I</sub>	Capacitance of control input (IN)
O <sub>ISO</sub>	OFF isolation of the switch is a measurement of OFF-state switch impedance. This is measured in dB in a specific frequency, with the corresponding channel (NC to COM or NO to COM) in the OFF state.
X <sub>TALK</sub>	Crosstalk is a measurement of unwanted signal coupling from an ON channel to an OFF channel (NC to NO or NO to NC). This is measured in a specific frequency and in dB.
BW	Bandwidth of the switch. This is the frequency in which the gain of an ON channel is –3 dB below the DC gain.
THD	Total harmonic distortion describes the signal distortion caused by the analog switch. This is defined as the ratio of root mean square (RMS) value of the second, third, and higher harmonic to the absolute magnitude of the fundamental harmonic.
I <sub>+</sub>	Static power-supply current with the control (IN) pin at V <sub>+</sub> or GND

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**PARAMETER MEASUREMENT INFORMATION**

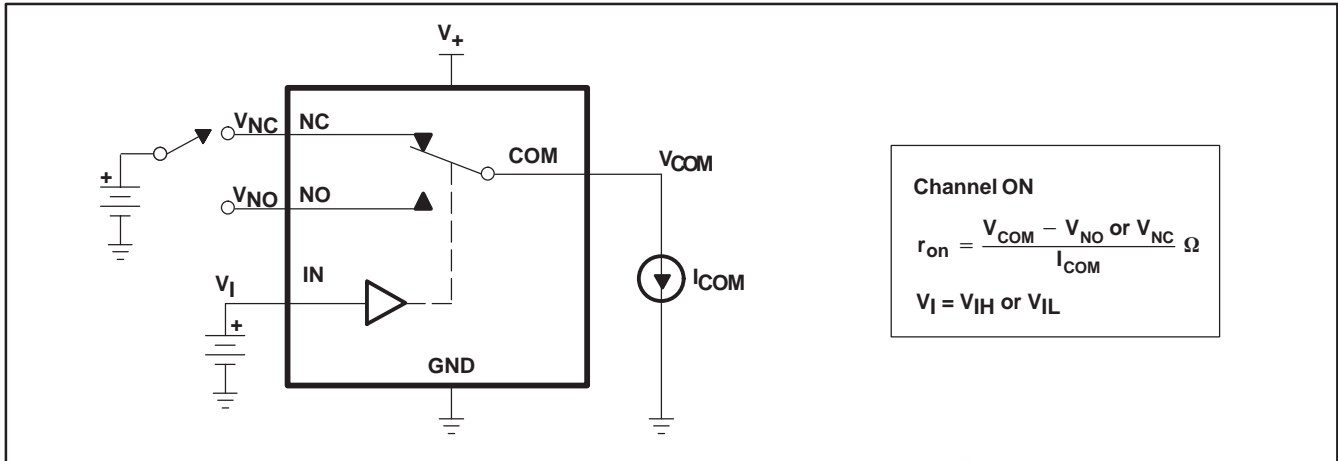


Figure 14. ON-State Resistance ( $r_{on}$ )

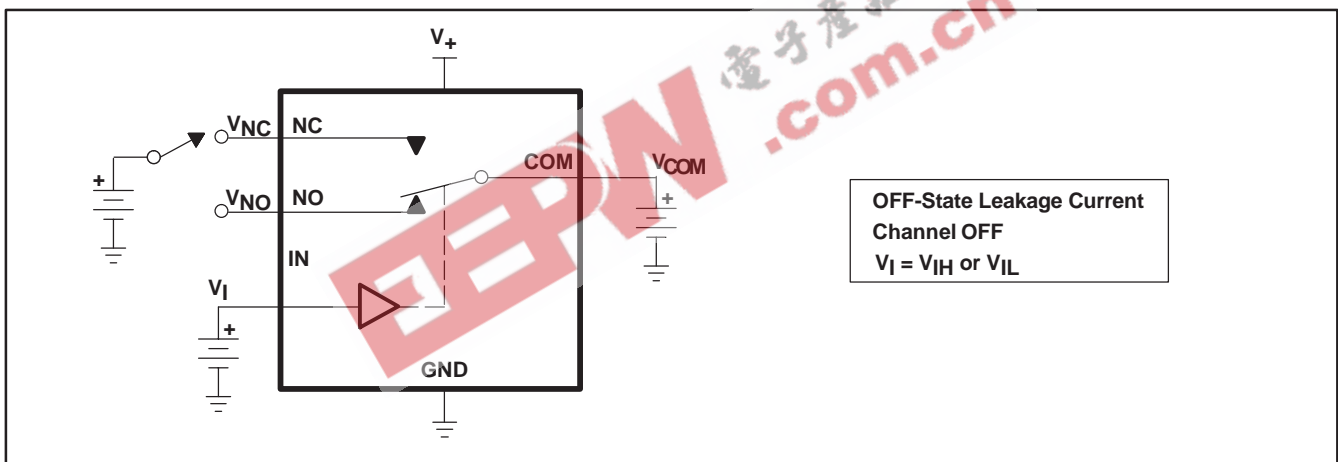


Figure 15. OFF-State Leakage Current ( $I_{NC(OFF)}$ ,  $I_{NC(PWROFF)}$ ,  $I_{NO(OFF)}$ ,  $I_{NO(PWROFF)}$ ,  $I_{COM(OFF)}$ ,  $I_{COM(PWROFF)}$ )

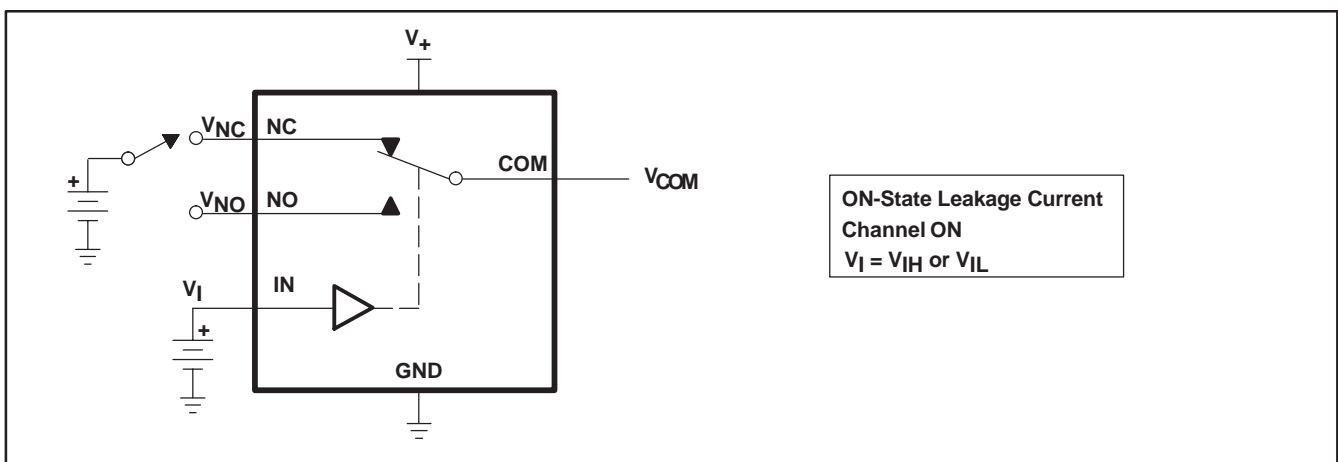


Figure 16. ON-State Leakage Current ( $I_{COM(ON)}$ ,  $I_{NC(ON)}$ ,  $I_{NO(ON)}$ )

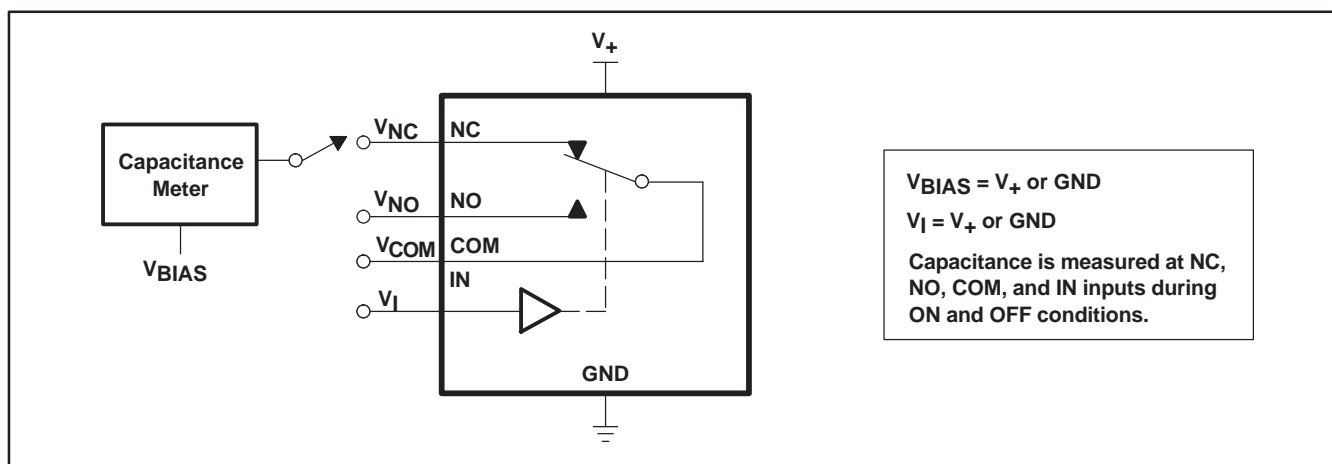
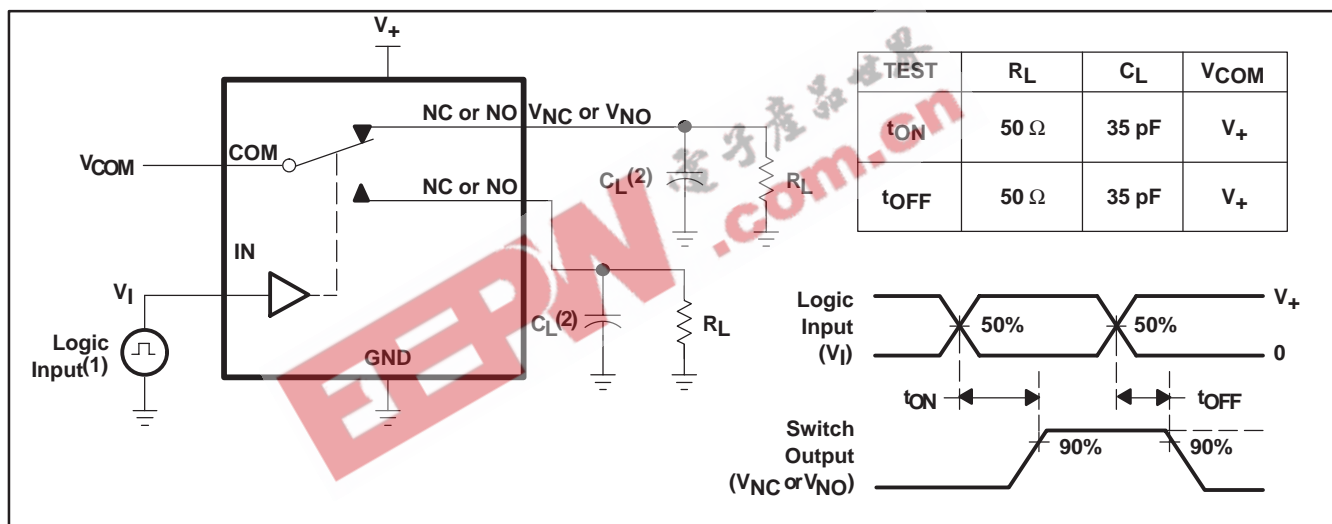


Figure 17. Capacitance ( $C_I$ ,  $C_{COM(ON)}$ ,  $C_{NC(OFF)}$ ,  $C_{NO(OFF)}$ ,  $C_{NC(ON)}$ ,  $C_{NO(ON)}$ )



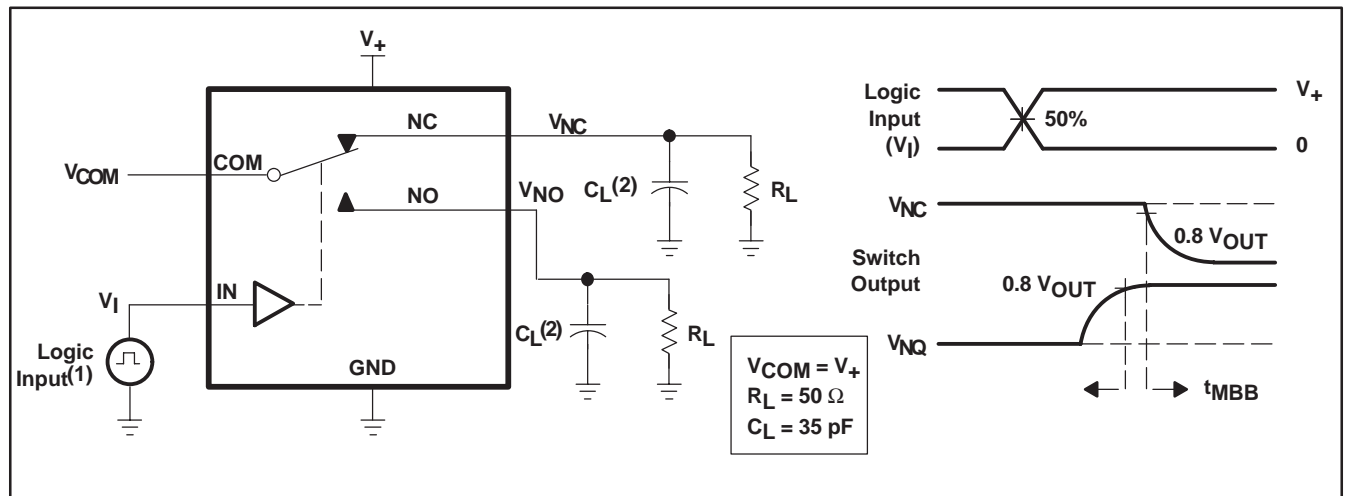
(1) All input pulses are supplied by generators having the following characteristics: PRR ≤ 10 MHz, Z<sub>O</sub> = 50 Ω, t<sub>r</sub> < 5 ns, t<sub>f</sub> < 5 ns.

(2) C<sub>L</sub> includes probe and jig capacitance.

Figure 18. Turn-On (t<sub>ON</sub>) and Turn-Off Time (t<sub>OFF</sub>)

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- (1) All input pulses are supplied by generators having the following characteristics:  $\text{PRR} \leq 10 \text{ MHz}$ ,  $Z_O = 50 \Omega$ ,  $t_r < 5 \text{ ns}$ ,  $t_f < 5 \text{ ns}$ .  
(2)  $C_L$  includes probe and jig capacitance.

Figure 19. Make-Before-Break Time ( $t_{MBB}$ )

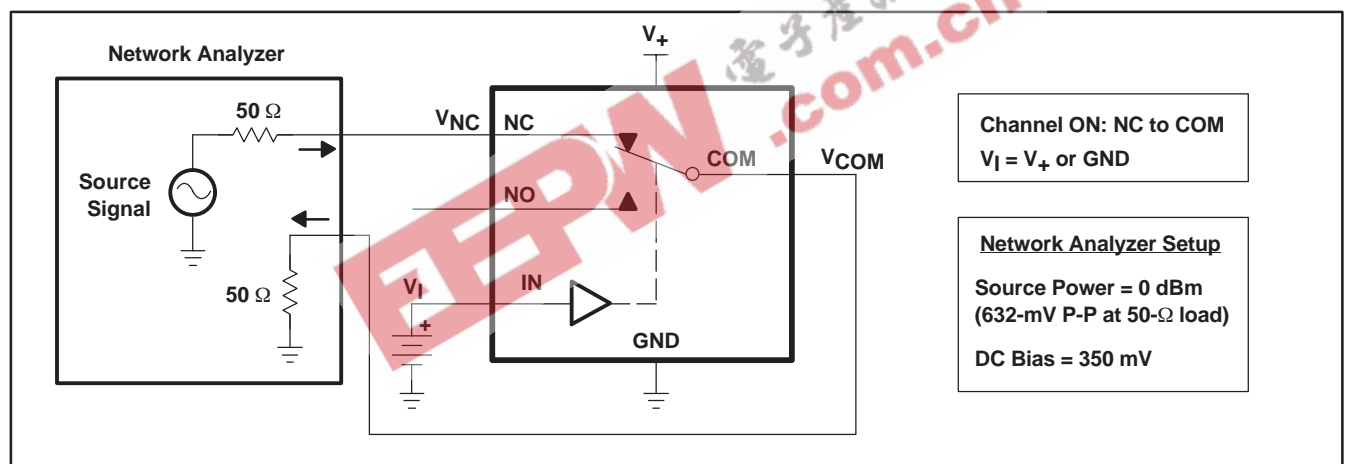


Figure 20. Bandwidth (BW)

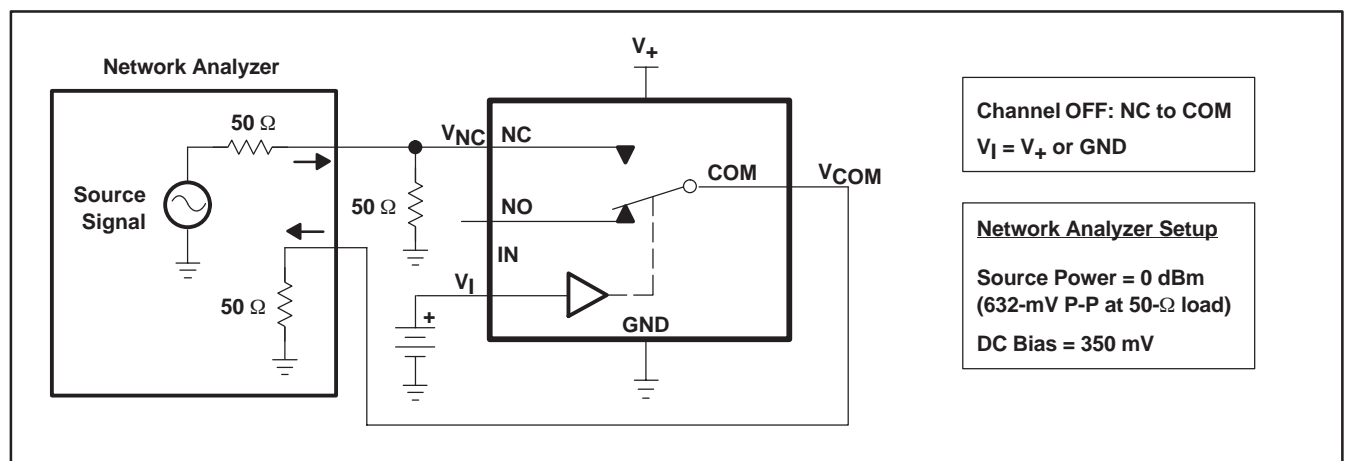


Figure 21. OFF Isolation ( $O_{ISO}$ )

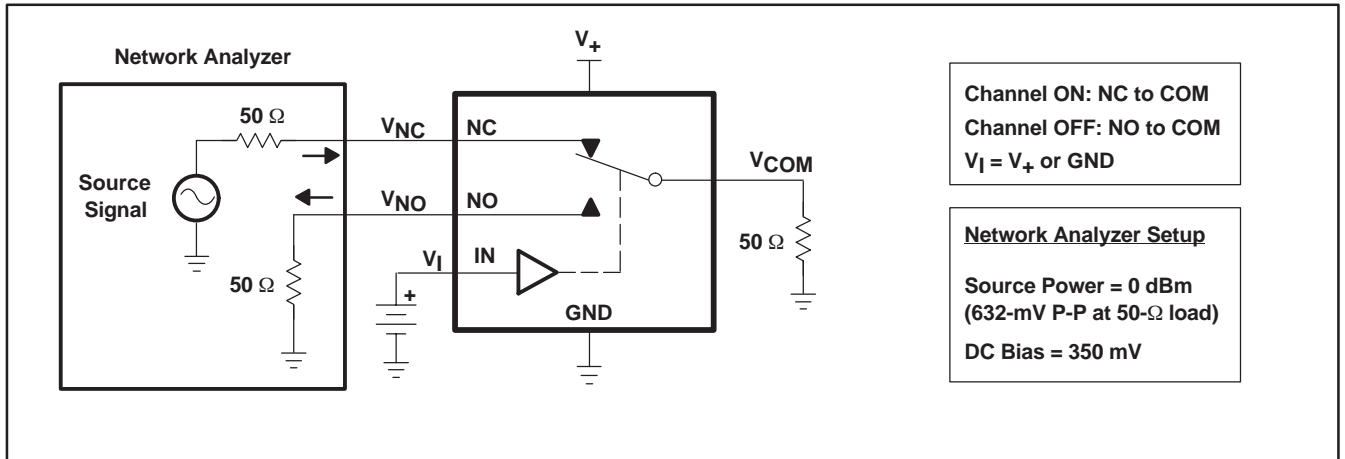
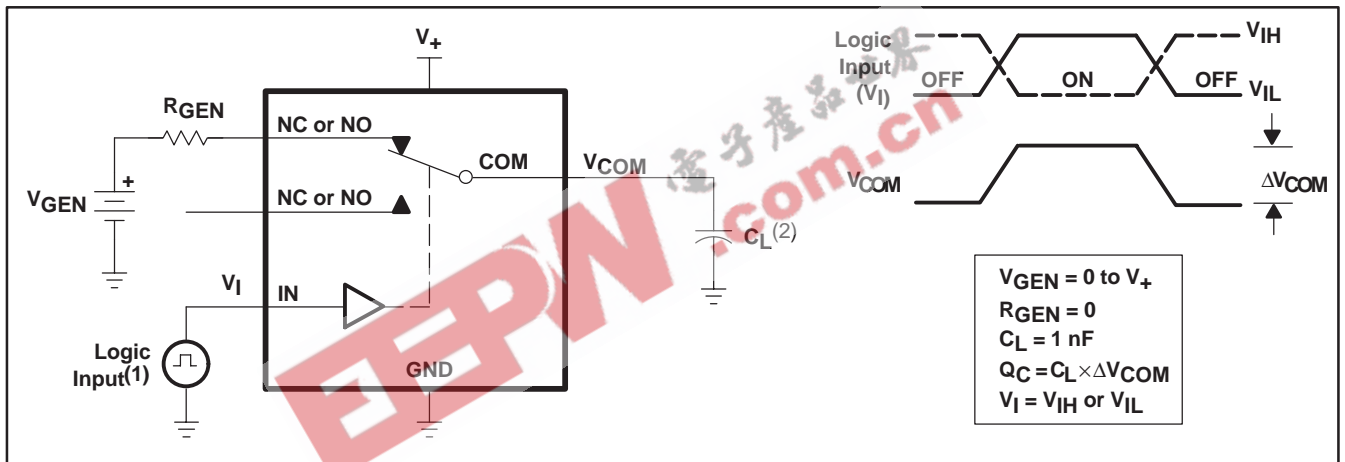
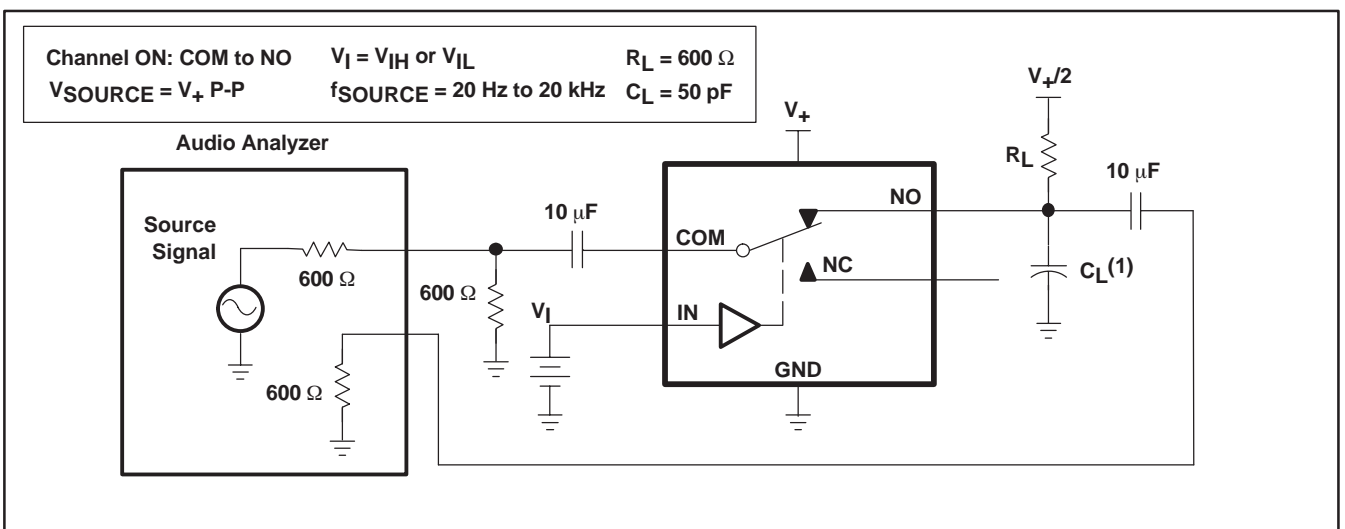


Figure 22. Crosstalk ( $X_{TALK}$ )



- (1) All input pulses are supplied by generators having the following characteristics:  $PRR \leq 10$  MHz,  $Z_O = 50 \Omega$ ,  $t_r < 5$  ns,  $t_f < 5$  ns.  
(2)  $C_L$  includes probe and jig capacitance.

Figure 23. Charge Injection ( $Q_C$ )



- (1)  $C_L$  includes probe and jig capacitance.

Figure 24. Total Harmonic Distortion (THD)

**PACKAGING INFORMATION**

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
TS5A23160DGSR	ACTIVE	MSOP	DGS	10	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TS5A23160DGSRE4	ACTIVE	MSOP	DGS	10	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TS5A23160DGST	ACTIVE	MSOP	DGS	10	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TS5A23160DGSTE4	ACTIVE	MSOP	DGS	10	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

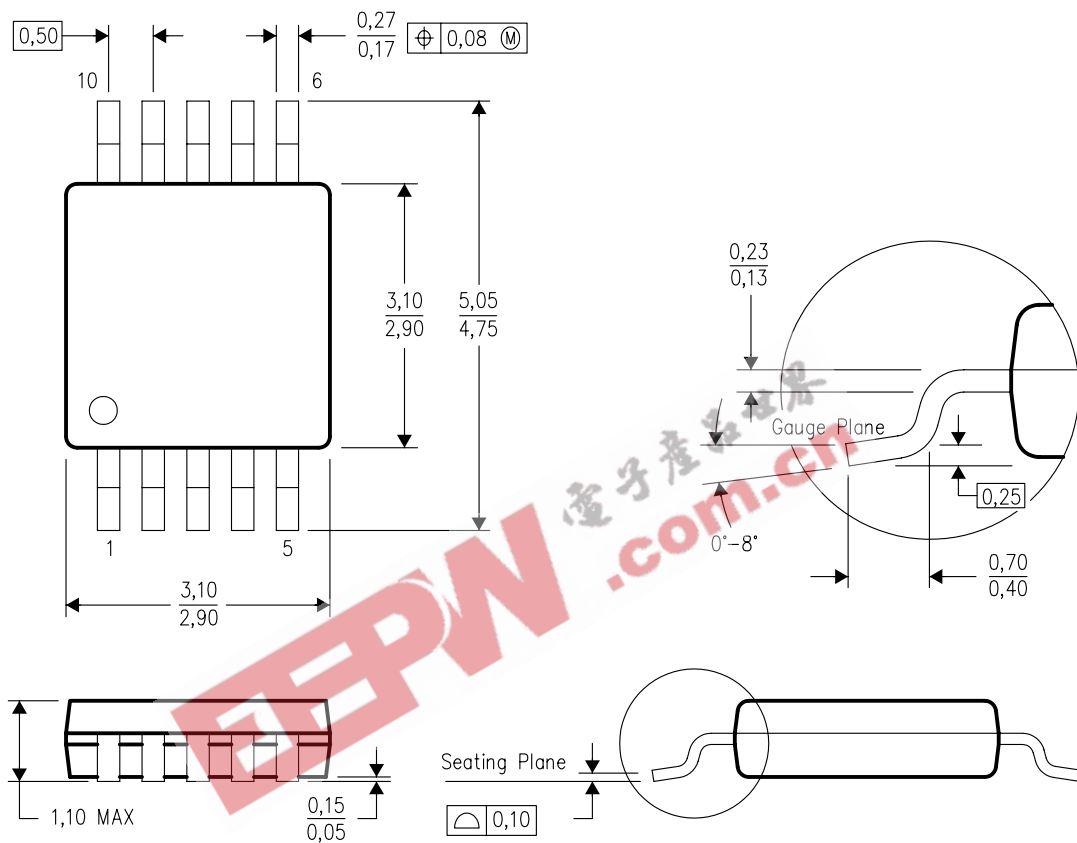
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# MECHANICAL DATA

DGS (S-PDSO-G10)

PLASTIC SMALL-OUTLINE PACKAGE



4073272/C 02/04

- NOTES:
- All linear dimensions are in millimeters.
  - This drawing is subject to change without notice.
  - Body dimensions do not include mold flash or protrusion.
  - Falls within JEDEC MO-187 variation BA.

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