1.8V LVCMOS Surface Mount Crystal Clock Oscillator 6111, 6121, 6131



XO

2111 Comprehensive Drive Phone: 630-851-4722 Fax: 630-851-5040 www.conwin.com

US Headquarters: 630-851-4722 European Headquarters: +353-61-472221 The Connor-Winfield models 6111, 6121, and 6131 are a 6 x 3.5mm, 1.8V LVCMOS, Ceramic, Surface Mount, Fixed Frequency Crystal Oscillators (XO) designed for use in all applictions requiring precision clocks. The RoHS compliant, surface mount package is designed for high-density mounting and is optimum for mass production.

Features:

30 to 70 MHz

1.8V Operation

RoHS Compliant

Tri-State Enable / Disable Function

Overall Frequency Tolerance:

6111 ± 25 ppm

 $6121 \pm 50 \text{ ppm}$

6131 ± 100 ppm

Temperature Range: 0 to 70°C Ceramic Surface Mount Package Tape and Reel Packaging

Absolute Maximum Ratings

Parameter	Minimum	Nominal	Maximum	Units	Notes
Storage Temperature	-55	30	125	°C	
Supply Voltage (Vcc)	-0.5	- 6	3.6	Vdc	
Operating Specifications					
Parameter	Minimum	Nominal	Maximum	Units	Notes

Parameter	Minimum	Nominal	Maximum	Units	Notes	
Frequency Range (Fo)	30	-	70	MHz		
Frequency Tolerance 6111 6121 6131	-25 -50 -100	- - -	25 50 100	ppm	1	
Operating Temp Range	0	-	70	°C		
Supply Voltage (Vdd)	1.71	1.8	1.89	Vdc		
Supply Current (Icc)	-	-	15	mA		

Input Characteristics

Parameter	Minimum	Nominal	Maximum	Units	Notes
Enable Voltage - (Vih)	≥ 70% Vdd	-	-	Vdc	2
Disable Voltage - (Vil)	-	-	≤30% Vdd	Vdc	
Enable Time	-	-	100	nS	
Disable Time	-	-	100	nS	

LVCMOS Output Characteristics

Parameter	Minimum	Nominal	Maximum	Units	Notes
Load	-	-	15	рF	
Voltage High (Voh) Low (Vol)	1.54 -		0.19	Vdc	
Current High (loh) Low (lol)	-2 -		2	mA	
Duty Cycle at 50% of Vcc	40	50	60	%	
Rise / Fall Time 10% to 90%	-	-	3.5	nS	
Start-Up Time	-	-	10	mS	
Jitter	-	-	5	pS RMS	

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Inclusive of calibration @ 25°C, frequency vs temperature stability, supply voltage change, load change, shock and vibration, 10 years aging. Inclusive of calibration @ 25°C, requerity vs. composition 2. Oscillator output is enabled with no connection on pad 1.



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Package Characteristics

Package Hermetically sealed ceramic package and metal cover

Environmental Characteristics

The specimen shall meet electrical characteristics after tested 5 cycles of -55°C / 30 minutes and +125°C / 30 minutes Temperature Cycle

Hermetical No bubbles appear in Flourinert (FC-43) at 125°C ±5°C for 5 minutes

Solvent Resistance Marking will withstand immersion in Isopropyl Alcohol or Trichloroethylene

Soldering

General Conditions 260°C max x 10 sec max x 2 times max or 230°C max x 180 sec max x 1 time

Typical Operation Data

ta (Vapor phase reflow)
20 to 100 sec up to 215°C, 50 sec
at 215°C, then down to room temperature per 1 to 5°C / sec

Mechanical Characteristics

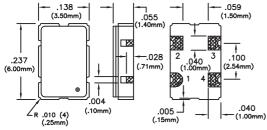
The specimen shall meet electrical characteristics after tested 3 times Free Drop testing on the hard wooden board from a height of 75 cm. Free Drop

The specimen shall meet electrical characteristics after tested by the following conditions: 10-55Hz 1.5mm Amplitude, 55-2000 Hz 20 G's, 2 hours for each plane Vibration

After applied Thermal Shock of 260°C max x 10 sec max x 2 times, or 230°C max x 180 sec max, the specimen shall meet electrical characteristics Thermal Shock

(EIAJ-RCX-0102.101 Condition 1a)
Flux: MIL-F-14256 (WW Rosin=25%, Isopropyl Alcohol = 75%)
Solder: QQ-S-571 (Sn = 63%, Pb = 37%)
Solder bath temperature: 235°C ±5°C
Depth of immersion: Up to electrical terminal
Immersing time: Within 2 sec ±0.5 sec into solder bath Solderability

After performing the above procedures, a newly soldered coverage shall be greater than 90%

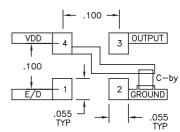


Dimensional Tolerance: ±.008 (.20mm)

Pad Connection

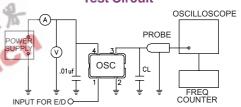
- Tri-State E/D
- Ground
- Output
- **VDD**

Suggested Pad Layout

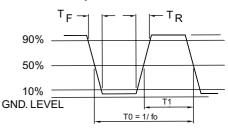


Bypass capacitor, C-by, should be ceramic capacitor ≥ .01uf.

Test Circuit



Output Waveform



SYMMETRY=(T1/T0)X100%

Ordering Information



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