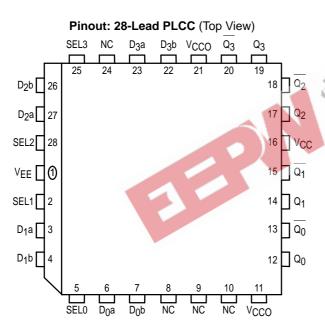
Quad 2:1 Multiplexer

The MC10E/100E157 contains four 2:1 multiplexers with differential outputs. The output data are controlled by the individual Select (SEL) inputs. The individual select control makes the devices well suited for random logic designs.

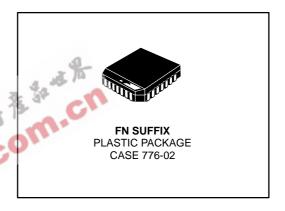
- Individual Select Controls
- 550ps Max. D to Output
- 800ps Max. SEL to Output
- Extended 100E VEE Range of 4.2V to 5.46V
- Internal 75kΩ Input Pulldown Resistors

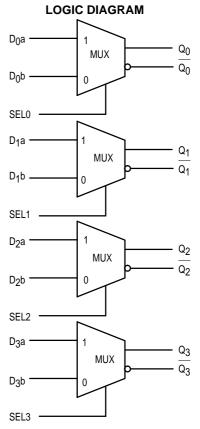
MC10E157 MC100E157

QUAD 2:1 MULTIPLEXER



 * All VCC and VCCO pins are tied together on the die.





PIN NAMES

Pin	Function							
D ₀ a – D ₃ a	Input Data a							
$D_0b - D_3b$	Input Data b							
SEL0 – SEL3	Select Inputs							
<u>Q</u> 0 – <u>Q</u> 3	True Outputs							
$Q_0 - Q_3$	Inverted Outputs							

TRUTH TABLE

SEL	Data
Н	а
L	b

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DC CHARACTERISTICS ($V_{EE} = V_{EE}(min)$ to $V_{EE}(max)$; $V_{CC} = V_{CCO} = GND$)

		0°C		25°C			85°C					
Symbol	Characteristic	min	typ	max	min	typ	max	min	typ	max	Unit	Condition
lН	Input HIGH Current										μΑ	
	D	1		200			200			200		
	SEL			150			150			150		
IEE	Power Supply Current										mA	
	10E	1	32	38		32	38		32	38		1
	100E	l	32	38		32	38		37	44		

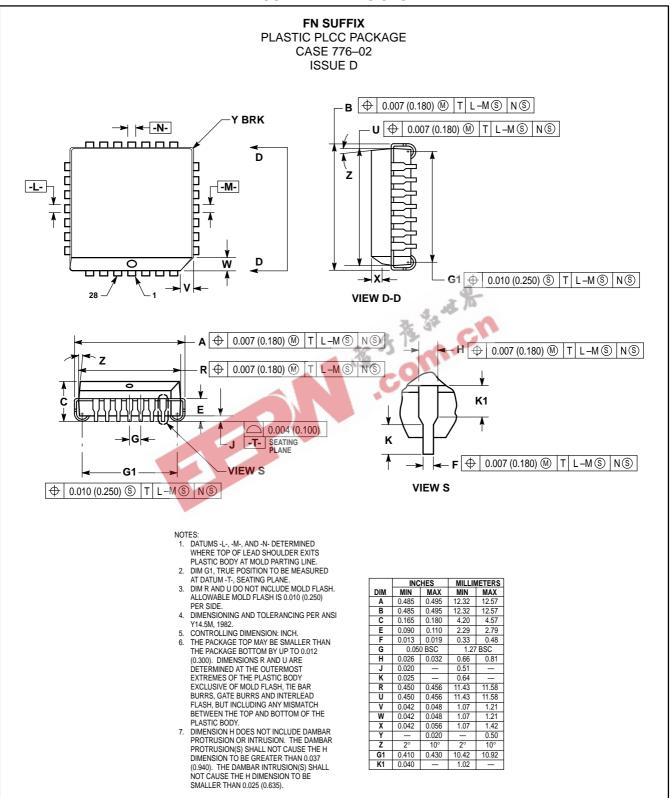
AC CHARACTERISTICS ($V_{EE} = V_{EE}(min)$ to $V_{EE}(max)$; $V_{CC} = V_{CCO} = GND$)

		0°C		25°C			85°C					
Symbol	Characteristic	min	typ	max	min	typ	max	min	typ	max	Unit	Condition
^t PLH	Propagation Delay to Output										ps	
^t PHL	D	220	380	550	220	380	550	220	380	550		
	SEL	425	600	800	425	600	800	425	600	800		
tSKEW	Within-Device Skew		70			70		S	70		ps	1
t _r	Rise/Fall Times						40.10				ps	
t _f	20 - 80% vice skew is defined as identical trans	275	400	650	275	400	650	275	400	650		
 Within-device skew is defined as identical transitions on similar paths through a device. 												

Within-device skew is defined as identical transitions on similar paths through a device.



OUTLINE DIMENSIONS



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