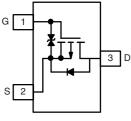


Si1013R/X Vishay Siliconix

# P-Channel 1.8-V (G-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω <b>)</b>	I <sub>D</sub> (mA)			
- 20	1.2 at V <sub>GS</sub> = - 4.5 V	- 350			
	1.6 at V <sub>GS</sub> = - 2.5 V	- 300			
	2.7 at V <sub>GS</sub> = - 1.8 V	- 150			

SC-75A or SC-89



SC-75A (SOT-416): Si1013R - Marking Code D SC-89 (SOT-490): Si1013X - Marking Code B

Top View

Ordering Information:

Si1013R-T1-E3 (SC-75A, Lead (Pb)-free) Si1013R-T1-GE3 (SC-75A, Lead (Pb)-free and Halogen-free) Si1013X-T1-E3 (SC-89, Lead (Pb)-free) Si1013X-T1-GE3 (SC-89, Lead (Pb)-free and Halogen-free)

#### FEATURES

- Halogen-free Option Available
- High-Side Switching
- Low On-Resistance: 1.2  $\Omega$
- Low Threshold: 0.8 V (Typ.)
- Fast Switching Speed: 14 ns
- 1.8 V Operation
- TrenchFET<sup>®</sup> Power MOSFETs
- 2000 V ESD Protection

#### APPLICATIONS

- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories
- Battery Operated Systems
- Power Supply Converter Circuits
- Load/Power Switching Cell Phones, Pagers

#### BENEFITS

- Ease in Driving Switches
- Low Offset (Error) Voltage
- Low-Voltage Operation
- High-Speed Circuits
- Low Battery Voltage Operation

Parameter	Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 20		V
Gate-Source Voltage		V <sub>GS</sub>	± 6		v
Continuous Durin Courset (T. 150 °C) <sup>b</sup>	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	- 400	- 350	
Continuous Drain Current $(T_J = 150 \text{ °C})^b$	T <sub>A</sub> = 85 °C		- 300	- 275	
Pulsed Drain Current <sup>a</sup>		I <sub>DM</sub>	- 1000		mA
Continuous Source Current (diode conduction) <sup>b</sup>		۱ <sub>S</sub>	- 275	- 250	
	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	175	150	mW
Maximum Power Dissipation <sup>b</sup> for SC-75	T <sub>A</sub> = 85 °C		90	80	
Marian Branco Biasia ati adh (m. 00.00	T <sub>A</sub> = 25 °C		275	250	
Maximum Power Dissipation <sup>b</sup> for SC-89	T <sub>A</sub> = 85 °C		160	140	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C
Gate-Source ESD Rating (HBM, Method 3015)	ESD	2000		V	

Notes:

a. Pulse width limited by maximum junction temperature.

b. Surface Mounted on FR4 board.



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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static					•	•	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = -250 \ \mu A$	- 0.45			V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, \text{ V}_{GS} = \pm 4.5 \text{ V}$		± 1	± 2	μΑ	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = -16 V, V_{GS} = 0 V$		- 0.3	- 100	nA	
		$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$			- 5	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = -5 V, V_{GS} = -4.5 V$	- 700			mA	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 350 mA		0.8	1.2	Ω	
		V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 300 mA		1.2	1.6		
		V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 150 mA		1.8	2.7		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 250 mA		0.4		S	
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = - 150 mA, V <sub>GS</sub> = 0 V		- 0.8	- 1.2	V	
Dynamic <sup>b</sup>							
Total Gate Charge	Qg			1500			
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = - 10 V, $V_{GS}$ = - 4.5 V, $I_D$ = - 250 mA	-	150		рС	
Gate-Drain Charge	Q <sub>gd</sub>	- 4a	16. I''	450			
Turn-On Delay Time	t <sub>d(on)</sub>		- C.V.	5			
Rise Time	t <sub>r</sub>	$V_{DD} = -10 \text{ V}, \text{ R}_{L} = 47 \Omega$		9			
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ - 200 mA, $V_{GEN}$ = - 4.5 V, $R_G$ = 10 $\Omega$		35		ns	
Fall Time	t <sub>f</sub>			11			

Notes:

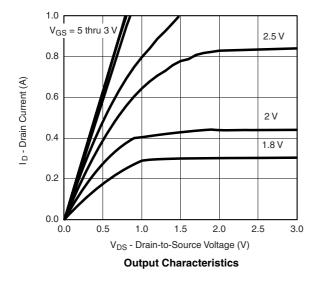
a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

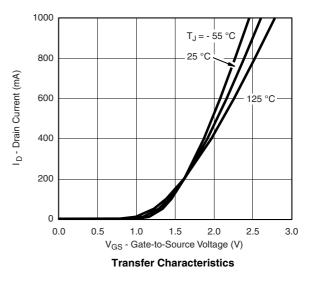
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### **TYPICAL CHARACTERISTICS** $T_A = 25$ °C, unless otherwise noted

For the following graphs, P-Channel negative polarities for all voltage and current values are represented as positive values.

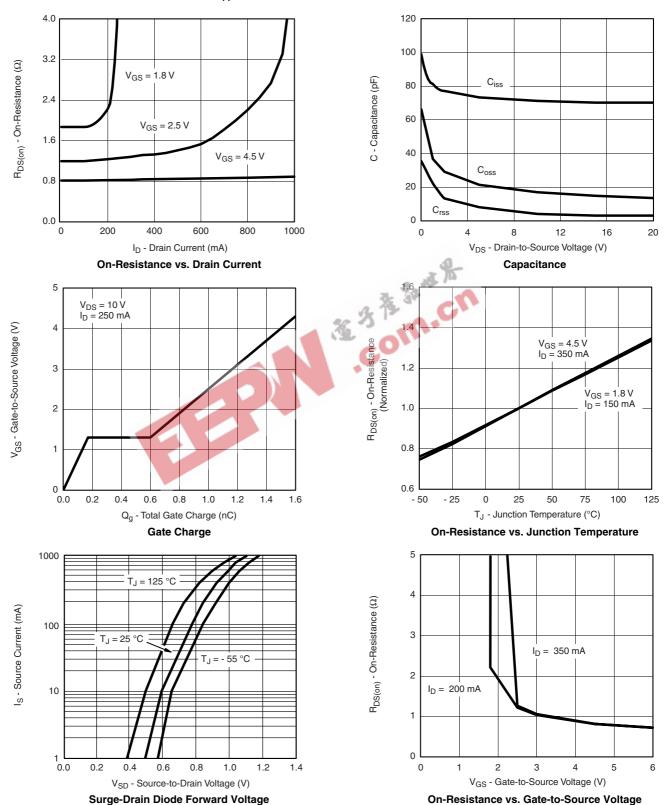








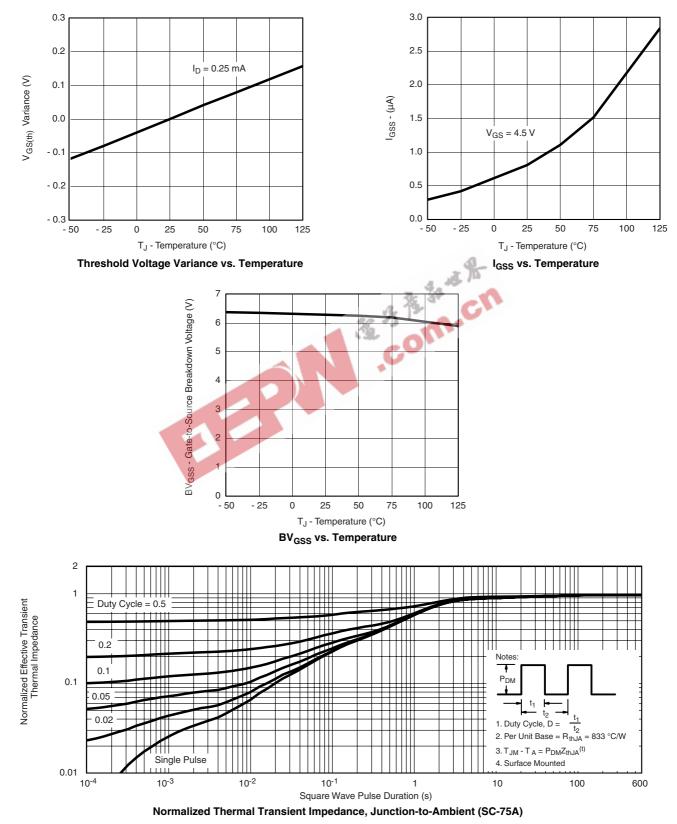
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**TYPICAL CHARACTERISTICS**  $T_A = 25 \text{ °C}$ , unless otherwise noted

## Si1013R/X

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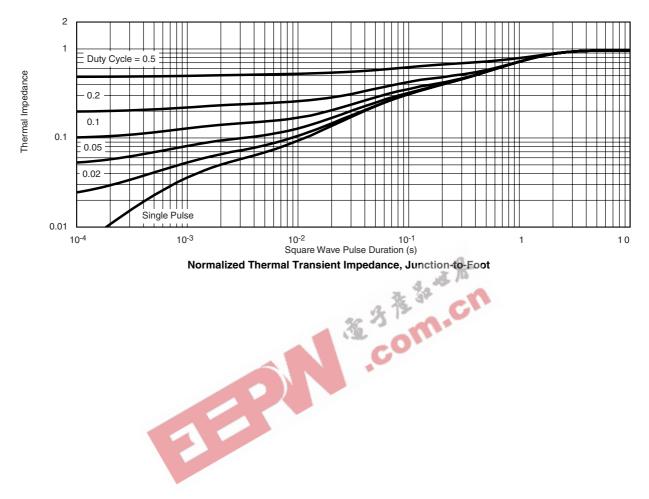
### **TYPICAL CHARACTERISTICS** $T_A = 25$ °C, unless otherwise noted

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### **TYPICAL CHARACTERISTICS** $T_A = 25 \text{ °C}$ , unless otherwise noted

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