Unit: mm

TOSHIBA Diode Silicon Epitaxial Planar Type

# **1SS226**

### **Ultra High Speed Switching Application**

• Small package : SC-59

 $\begin{array}{ll} \bullet & \text{Low forward voltage} & \vdots \ V_{F\ (3)} = 0.9 V\ (typ.) \\ \bullet & \text{Fast reverse recovery time:} \ t_{rr} = 1.6 ns\ (typ.) \\ \bullet & \text{Small total capacitance} & \vdots \ C_{T} = 0.9 pF\ (typ.) \end{array}$ 

## **Absolute Maximum Ratings (Ta = 25°C)**

Characteristic	Symbol	Rating	Unit
Maximum (peak) reverse voltage	V <sub>RM</sub>	85	V
Reverse voltage	V <sub>R</sub>	80	V
Maximum (peak) forward current	I <sub>FM</sub>	300 (*)	mA
Average forward current	Io	100 (*)	mA
Surge current (10ms)	I <sub>FSM</sub>	2 (*)	Α
Power dissipation	Р	150	mW
Junction temperature	Tj	125	°C
Storage temperature range	T <sub>stg</sub>	-55~125	°C

1. ANODE 1 2. CATHODE 2 S-MINI 3. ANODE 2/CATHODE 1 JEDEC TO-236MOD EIAJ SC-59 TOSHIBA 1-3G1G

Weight: 0.012g

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliab

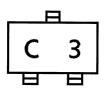
temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(\*) Unit rating. Total rating = Unit rating × 0.7.

### **Electrical Characteristics (Ta = 25°C)**

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Forward voltage	V <sub>F (1)</sub>	_	I <sub>F</sub> = 1mA	_	0.60	_	٧
	V <sub>F (2)</sub>	_	I <sub>F</sub> = 10mA	_	0.72	_	
	V <sub>F (3)</sub>	_	I <sub>F</sub> = 100mA	_	0.90	1.20	
Reverse current	I <sub>R (1)</sub>	_	V <sub>R</sub> = 30V	_	_	0.1	μА
	I <sub>R (2)</sub>	_	V <sub>R</sub> = 80V	-	_	0.5	
Total capacitance	C <sub>T</sub>	_	V <sub>R</sub> = 0, f = 1MH <sub>z</sub>	_	0.9	3.0	pF
Reverse recovery time	t <sub>rr</sub>	_	I <sub>F</sub> = 10mA (Fig.1)	_	1.6	4.0	ns

### Marking



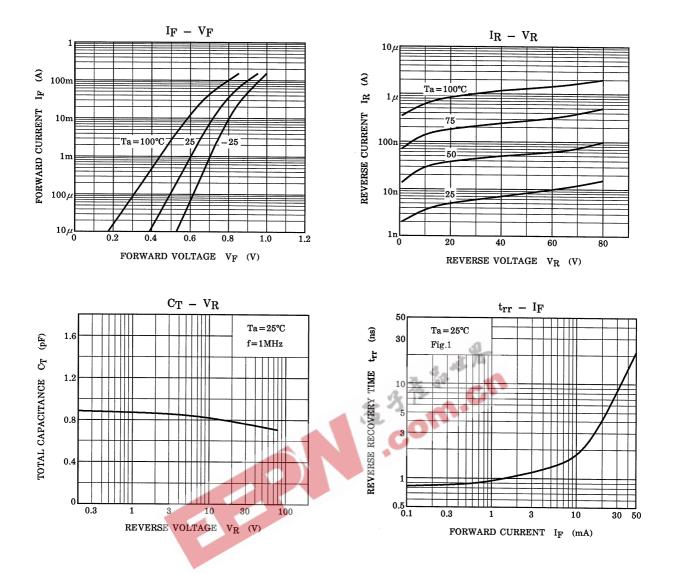
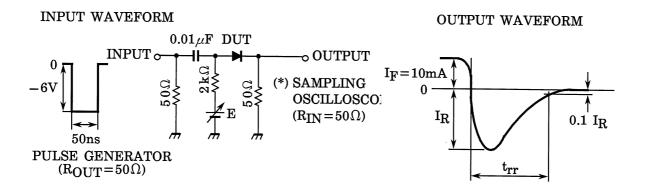


Fig.1 Reverse recovery time (t<sub>rr</sub>) test circuit



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2007-11-01

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20070701-EN GENERAL

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