
HD74HC298

Quad. 2-input Multiplexers (with storage)

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Description

This circuit is controlled by the signals word select and clock. When the word select input is taken low word 1 (A_1, B_1, C_1 and D_1) is presented to the inputs of the flip-flops, and when word select is high word 2 (A_2, B_2, C_2 and D_2) is presented to the inputs of the flip-flops. The selected word is clocked to the output terminals on the negative edge of the clock pulse.

Features

- High Speed Operation: t_{pd} (Clock to Q) = 19 ns typ ($C_L = 50 \text{ pF}$)
- High Output Current: Fanout of 10 LSTTL Loads
- Wide Operating Voltage: $V_{CC} = 2$ to 6 V
- Low Input Current: 1 μA max
- Low Quiescent Supply Current: I_{CC} (static) = 4 μA max

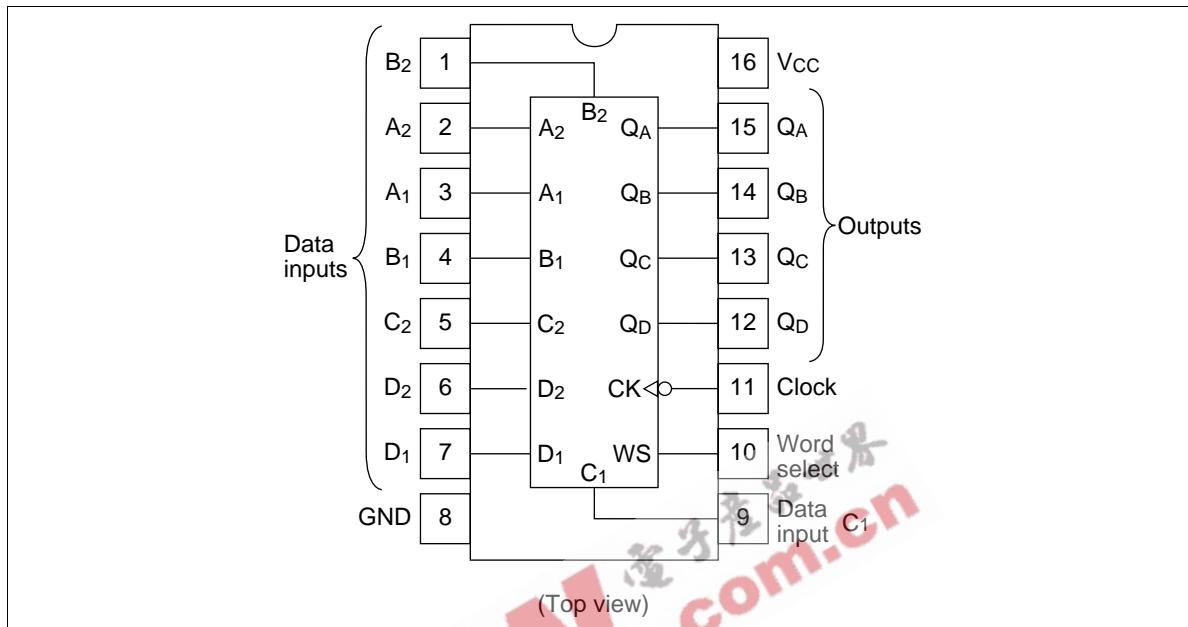
Function Table

Inputs		Outputs			
Word Select	Clock	Q_A	Q_B	Q_C	Q_D
L		a_1	b_1	c_1	d_1
H		a_2	b_2	c_2	d_2
X	H	Q_{A0}	Q_{B0}	Q_{C0}	Q_{D0}



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Pin Arrangement



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DC Characteristics

Item	Symbol	V _{cc} (V)	Ta = -40 to +85°C					Unit	Test Conditions
			Min	Typ	Max	Min	Max		
Input voltage	V _{ih}	2.0	1.5	—	—	1.5	—	V	
		4.5	3.15	—	—	3.15	—		
		6.0	4.2	—	—	4.2	—		
	V _{il}	2.0	—	—	0.5	—	0.5	V	
		4.5	—	—	1.35	—	1.35		
		6.0	—	—	1.8	—	1.8		
Output voltage	V _{oh}	2.0	1.9	2.0	—	1.9	—	V	V _{in} = V _{ih} or V _{il} I _{oh} = -20 μA
		4.5	4.4	4.5	—	4.4	—		
		6.0	5.9	6.0	—	5.9	—		
		4.5	4.18	—	—	4.13	—		I _{oh} = -4 mA
		6.0	5.68	—	—	5.63	—		I _{oh} = -5.2 mA
	V _{ol}	2.0	—	0.0	0.1	—	0.1	V	V _{in} = V _{ih} or V _{il} I _{ol} = 20 μA
		4.5	—	0.0	0.1	—	0.1		
		6.0	—	0.0	0.1	—	0.1		
		4.5	—	—	0.26	—	0.33		I _{ol} = 4 mA
		6.0	—	—	0.26	—	0.33		I _{ol} = 5.2 mA
Input current	I _{in}	6.0	—	—	±0.1	—	±1.0	μA	V _{in} = V _{cc} or GND
Quiescent supply current	I _{cc}	6.0	—	—	4.0	—	40	μA	V _{in} = V _{cc} or GND, I _{out} = 0 μA

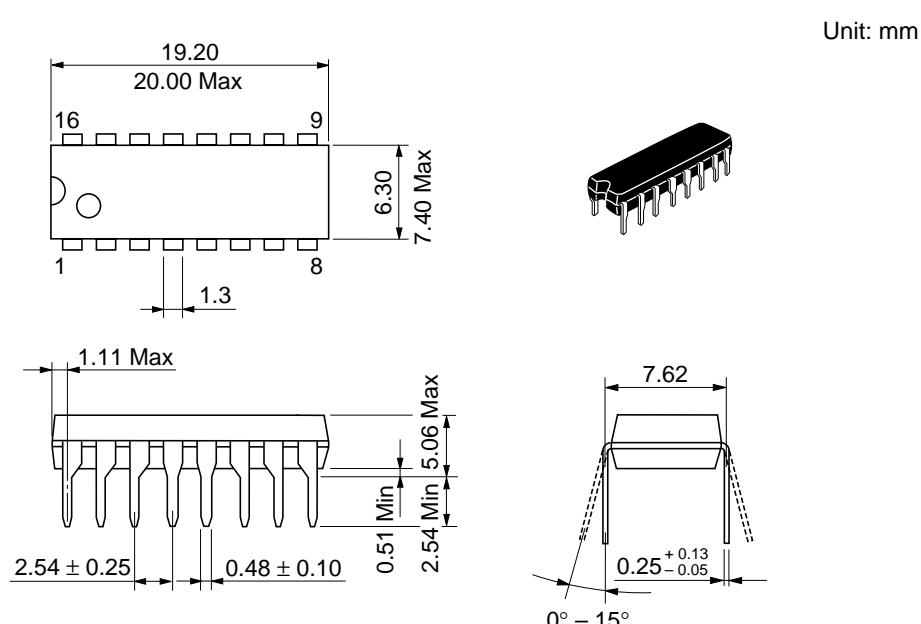
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AC Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

Item	Symbol	$V_{cc} (\text{V})$	Ta = -40 to +85°C				Unit	Test Conditions
			Min	Typ	Max	Min		
Propagation delay time	t_{PLH}	2.0	—	—	170	—	215	ns Clock to Q
	t_{PHL}	4.5	—	19	34	—	43	
		6.0	—	—	29	—	37	
Pulse width	t_w	2.0	80	—	—	100	—	ns Clock
		4.5	16	10	—	20	—	
		6.0	14	—	—	17	—	
Setup time	t_{su}	2.0	150	—	—	190	—	ns
		4.5	30	16	—	38	—	
		6.0	26	—	—	33	—	
Hold time	t_h	2.0	5	—	—	5	—	ns
		4.5	5	-5	—	5	—	
		6.0	5	—	—	5	—	
Output rise/fall time	t_{TLH}	2.0	—	—	75	—	95	ns
	t_{THL}	4.5	—	5	15	—	19	
		6.0	—	—	13	—	16	
Input capacitance	C_{in}	—	—	5	10	—	10	pF

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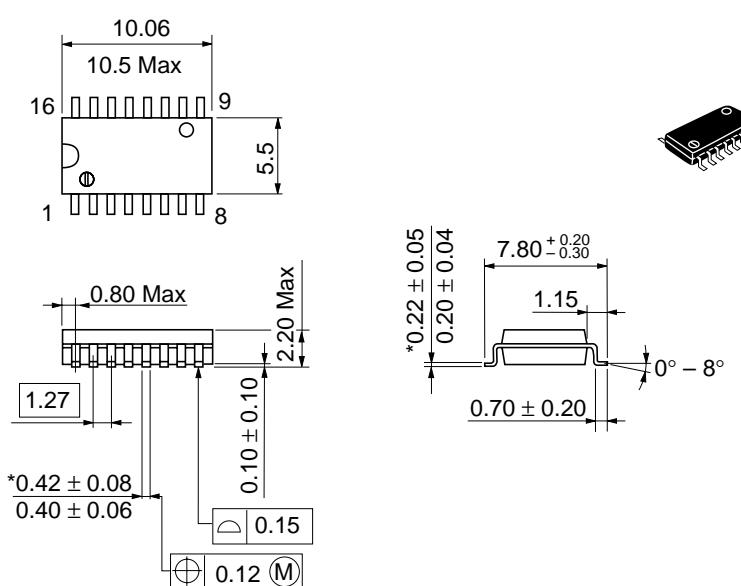
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Hitachi Code	DP-16
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	1.07 g

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Unit: mm

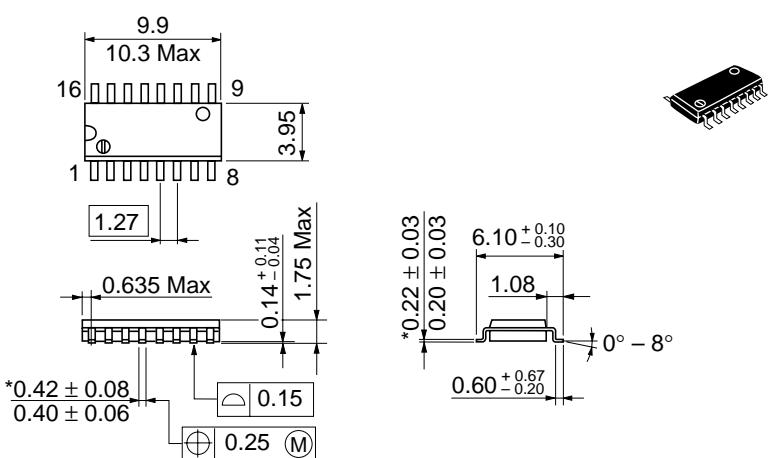


*Dimension including the plating thickness
Base material dimension

Hitachi Code	FP-16DA
JEDEC	—
EIAJ	Conforms
Weight (reference value)	0.24 g

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Unit: mm



Hitachi Code	FP-16DN
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.15 g

*Dimension including the plating thickness
Base material dimension