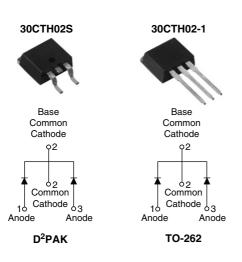


Vishay High Power Products

Hyperfast Rectifier, 2 x 15 A FRED Pt[™]



PRODUCT SUMMARY				
t _{rr} (maximum)	30 ns			
I _{F(AV)}	2 x 15 A			
V _R	200 V			

FEATURES

- Hyperfast recovery time
- Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature
- Designed and qualified for industrial level

DESCRIPTION/APPLICATIONS

Vishay HPP's 200 V series are the state of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Peak repetitive reverse voltage	V _{RRM}		200	V	
Average rectified forward current		T _C = 159 °C	15		
per device	re I _{F(AV)}		30	А	
Non-repetitive peak surge current	I _{FSM}	T _C = 25 °C	200		
Operating junction and storage temperatures	T _J , T _{Stg}		- 65 to 175	°C	

ELECTRICAL SPECIFICATIONS ($T_J = 25 \degree C$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	200	-	-	V	
Forward voltage V _F	M	I _F = 15 A	-	0.92	1.05	V	
	VF	I _F = 15 A, T _J = 125 °C	-	0.78	0.85	v	
		$V_R = V_R$ rated	-	-	10		
Reverse leakage current I _R	'R	$T_J = 125 \ ^{\circ}C, \ V_R = V_R \ rated$	-	5	300	μΑ	
Junction capacitance	CT	V _R = 200 V	-	57	-	pF	
Series inductance	LS	Measured lead to lead 5 mm from package body	-	8	-	nH	

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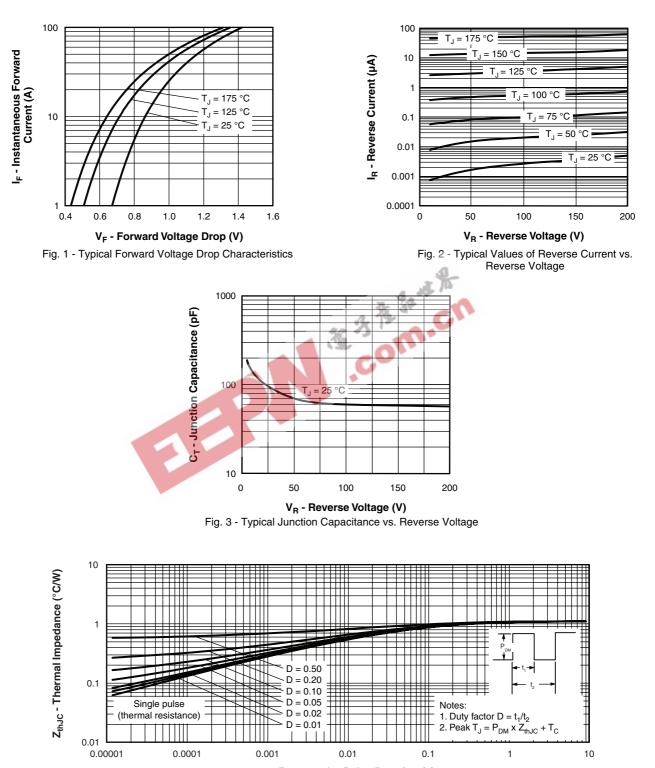
DYNAMIC RECOVERY CHARACTERISTICS ($T_C = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS	
Reverse recovery time	t _{rr}	$I_F = 1 \text{ A}, dI_F/dt = 50 \text{ A}/\mu \text{s}, V_R = 30 \text{ V}$		-	-	35	
		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 100 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	-	30	
		T _J = 25 °C	I _F = 15 A dI _F /dt = 200 A/μs V _R = 160 V	-	26	-	ns
		T _J = 125 °C		-	40	-	
Peak recovery current	I _{RRM}	T _J = 25 °C		-	2.8	-	٨
		T _J = 125 °C		-	6.0	-	A
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	37	-	nC
		T _J = 125 °C		-	120	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}	- 65	A A	175	°C	
Thermal resistance, junction to case per diode	R _{thJC}	-8-	34	1.1	°C/W	
Weight		- 3 19	2.0	-	g	
weight		3	0.07	-	oz.	
Mounting torque		6.0	<u> </u>	12	kgf · cm	
		(5.0)		(10)	(lbf ⋅ in)	
Marking device		Case style D ² P	AK	30CT	H02S	
Marking device		Case style TO-262		30CTH02-1		



Hyperfast Rectifier, 2 x 15 A FRED Pt^{TM}

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30CTH02S/30CTH02-1 /ISHA Vishay High Power Products Hyperfast Rectifier, 2 x 15 A FRED Pt[™] 180 100 Allowable Case Temperature (°C) I_F = 15 A 170 DC t_{rr} (ns) 160 Square wave (D = 0.50) 150 Rated V_R applied V_R = 160 V T_J = 125 °C See note (1) T_J = 25 °C 140 10 0 5 10 15 20 25 100 1000 dl_F/dt (A/µs) I_{F(AV)} - Average Forward Current (A) Fig. 5 - Maximum Allowable Case Temperature vs. Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt Average Forward Current 25 Average Power Loss (W) 20 **RMS** limit 15 = 0.01 D D = 0.0210 D = 0.05D = 0.1D = 0.2 5 V_R = 160 V D = 0.5DC T_J = 125 °C T_J = 25 °C 0 10 15 0 5 10 20 25 100 1000 dl_F/dt (A/µs) I_{F(AV)} - Average Forward Current (A) Fig. 8 - Typical Stored Charge vs. dl_F/dt Fig. 6 - Forward Power Loss Characteristics

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = Rated V_R



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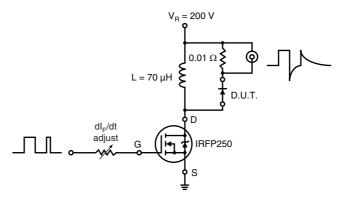


Fig. 9 - Reverse Recovery Parameter Test Circuit

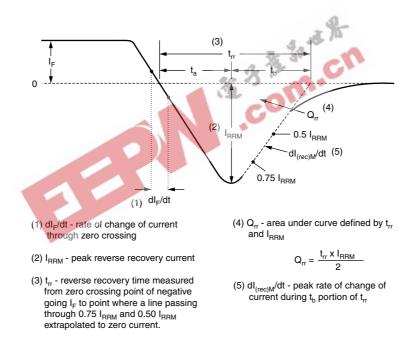
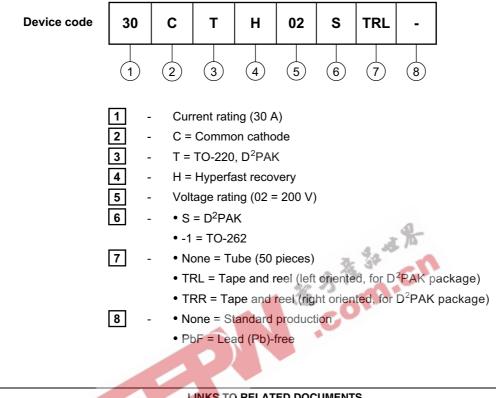


Fig. 10 - Reverse Recovery Waveform and Definitions

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LINKS TO RELATED DOCUMENTS					
Dimensions				http://www.vishay.com/doc?95014	
Part marking information				http://www.vishay.com/doc?95008	
Packaging information				http://www.vishay.com/doc?95032	



Vishay

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