

54F/74F157A Quad 2-Input Multiplexer

General Description

The 'F157A is a high-speed quad 2-input multiplexer. Four bits of data from two sources can be selected using the common Select and Enable inputs. The four outputs present the selected data in the true (non-inverted) form. The 'F157A can also be used to generate any four of the 16 different functions to two variables.

Features

■ Guaranteed 4000V minimum ESD protection

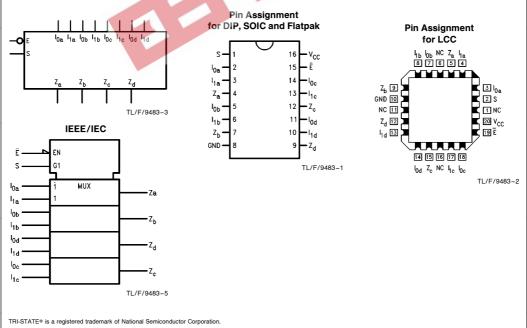
Commercial	Military	Package Number	Package Description		
74F157APC		N16E	16-Lead (0.300" Wide) Molded Dual-In-Line		
	54F157ADM (Note 2)	J16A	16-Lead Ceramic Dual-In-Line		
74F157ASC (Note 1)		M16A	16-Lead (0.150" Wide) Molded Small Outline, JEDEC		
74F157ASJ (Note 1)		M16D	16-Lead (0.300" Wide) Molded Small Outline, EIAJ		
	54F157AFM (Note 2)	W16A	16-Lead Cerpack		
	54F157ALM (Note 2)	E20A	20-Lead Ceramic Leadless Chip Carrier, Type C		

Note 1: Devices also available in 13" reel. Use suffix = SCX and SJX.

Note 2: Military grade device with environmental and burn-in processing. Use suffix = DMQB, FMQB and LMQB.

Logic Symbols

Connection Diagrams



Unit Loading/Fan Out

		54F/74F				
Pin Names	Description	U.L. HIGH/LOW	Input I _{IH} /I _{IL} Output I _{OH} /I _{OL}			
$I_{0a}-I_{0d}$	Source 0 Data Inputs	1.0/1.0	20 μA/ – 0.6 mA			
I _{1a} -I _{1d}	Source 1 Data Inputs	1.0/1.0	20 μA/ - 0.6 mA			
Ē	Enable Input (Active LOW)	1.0/1.0	20 μA/ - 0.6 mA			
S	Select Input	1.0/1.0	20 μA/ – 0.6 mA			
$Z_a - Z_d$	Outputs	50/33.3	-1 mA/20 mA			

Functional Description

The 'F157A is a quad 2-input multiplexer. It selects four bits of data from two sources under the control of a common Select input (S). The Enable input $(\overline{\mathsf{E}})$ is active LOW. When $\overline{\mathsf{E}}$ is HIGH, all of the outputs (Z) are forced LOW regardless of all other inputs. The 'F157A is the logic implementation of a 4-pole, 2-position switch where the position of the switch is determined by the logic levels supplied to the Select input. The logic equations for the outputs are shown below:

$$\mathsf{Z}_n = \overline{\mathsf{E}} \bullet (\mathsf{I}_{1n}\,\mathsf{S} \,+\, \mathsf{I}_{0n}\,\overline{\mathsf{S}})$$

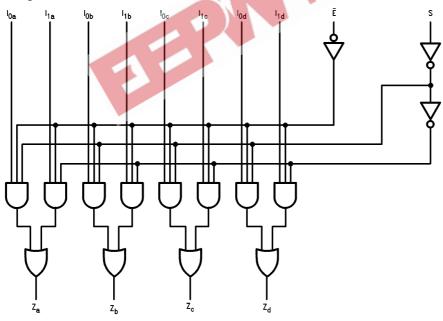
A common use of the 'F157A is the moving of data from two groups of registers to four common output busses. The particular register from which the data comes is determined by the state of the Select input. A less obvious use is as a function generator. The 'F157A can generate any four of the 16 different functions of two variables with one variable common. This is useful for implementing highly irregular logic.

Truth Table

	Output			
Ē	s	I ₀	l ₁	Z
Н	Х	Х	X	L
L	Н	X	£	L
L	Н	X	E #P	Н
L	L	L. C.	X	L
L	L	X H	X	Н

H = HIGH Voltage LevelL = LOW Voltage LevelX = Immaterial

Logic Diagram



TL/F/9483-4

Please note that this diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Storage Temperature -65°C to +150°C Ambient Temperature under Bias -55°C to $+125^{\circ}\text{C}$ -55°C to +175°C Junction Temperature under Bias Plastic -55°C to $+150^{\circ}\text{C}$

V_{CC} Pin Potential to Ground Pin -0.5V to +7.0VInput Voltage (Note 2) -0.5V to +7.0VInput Current (Note 2) -30~mA to +5.0~mA

Voltage Applied to Output in HIGH State (with $V_{CC} = 0V$)

Standard Output $\begin{array}{c} -0.5 \text{V to V}_{CC} \\ -0.5 \text{V to } +5.5 \text{V} \end{array}$ TRI-STATE® Output

Current Applied to Output

twice the rated $I_{\mbox{OL}}$ (mA) in LOW State (Max)

ESD Last Passing Voltage (Min)

Note 1: Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Recommended Operating Conditions

Free Air Ambient Temperature

Military -55°C to +125°C Commercial $0^{\circ}C$ to $\,+\,70^{\circ}C$

Supply Voltage

+4.5V to +5.5VMilitary +4.5V to +5.5V Commercial

DC Electrical Characteristics

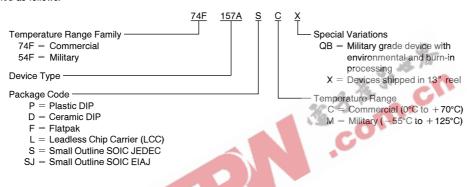
	ther voltage limit or current limit		11.18						
Symbol	Paramet	ter	54F/74F			Units	V _{CC}	Conditions	
			Min	Typ Max		9,111			
V_{IH}	Input HIGH Voltage		2.0		Car	V		Recognized as a HIGH Signal	
V _{IL}	Input LOW Voltage				0.8	V		Recognized as a LOW Signal	
V _{CD}	Input Clamp Diode Volt	age	1 1	()	-1.2	٧	Min	$I_{\text{IN}} = -18 \text{ mA}$	
V _{OH}	Output HIGH Voltage	54F 10% V _{CC} 74F 10% V _{CC} 74F 5% V _{CC}	2.5 2.5 2.7			٧	Min	$I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$	
V _{OL}	Output LOW Voltage	54F 10% V _{CC} 74F 10% V _{CC}			0.5 0.5	V	Min	$I_{OL} = 20 \text{ mA}$ $I_{OL} = 20 \text{ mA}$	
I _{IH}	Input HIGH Current	54F 74F			20.0 5.0	μΑ	Max	$V_{IN} = 2.7V$	
I _{BVI}	Input HIGH Current Breakdown Test	54F 74F			100 7.0	μΑ	Max	V _{IN} = 7.0V	
I _{CEX}	Output HIGH Leakage Current	54F 74F			250 50	μΑ	Max	$V_{OUT} = V_{CC}$	
V _{ID}	Input Leakage Test	74F	4.75			٧	0.0	$I_{\text{ID}} = 1.9 \ \mu\text{A}$ All Other Pins Grounded	
I _{OD}	Output Leakage Circuit Current	74F			3.75	μΑ	0.0	V _{IOD} = 150 mV All Other Pins Grounded	
I _{IL}	Input LOW Current				-0.6	mA	Max	V _{IN} = 0.5V	
Ios	Output Short-Circuit Cu	-60		-150	mA	Max	$V_{OUT} = 0V$		
Іссн	Power Supply Current			15	23	mA	Max	V _O = HIGH	
I _{CCL}	Power Supply Current			15	23	mA	Max	$V_O = LOW$	

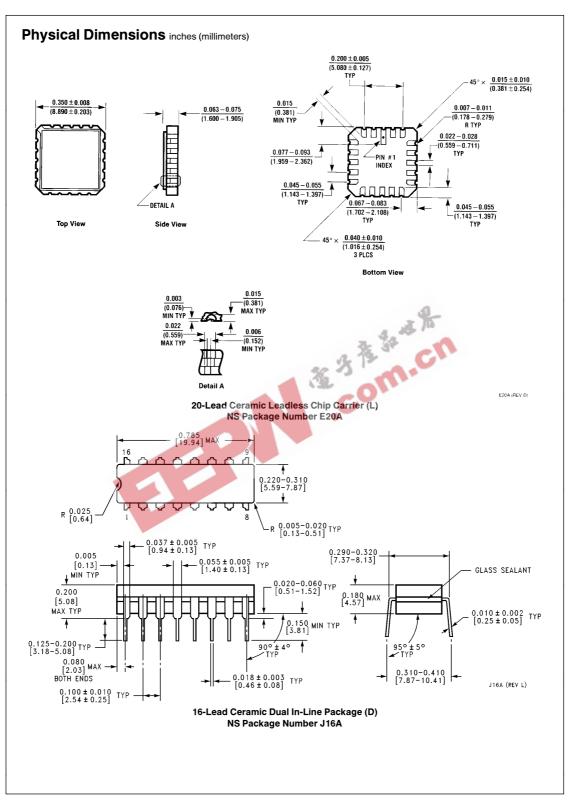
AC Electrical Characteristics

Symbol	Parameter				54F T _A , V _{CC} = Mil C _L = 50 pF		74F T _A , V _{CC} = Com C _L = 50 pF		Units
		Min	Тур	Max	Min	Max	Min	Max	
t _{PLH}	Propagation Delay S to Z _n	4.0 3.0	7.0 5.0	10.0 7.0	4.0 3.0	12.0 9.0	4.0 3.0	11.0 8.0	ns
t _{PLH}	Propagation Delay E to Z _n	5.0 2.5	7.0 4.5	9.5 6.5	5.0 2.5	13.0 7.5	5.0 2.5	11.0 7.0	ns
t _{PLH}	Propagation Delay I _n to Z _n	2.5 2.5	4.5 4.0	6.0 5.5	2.5 1.5	7.5 7.5	2.5 2.0	6.5 7.0	ns

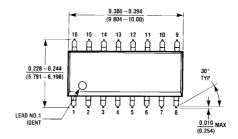
Ordering Information

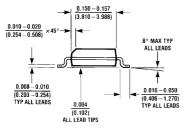
The device number is used to form part of a simplified purchasing code where the package type and temperature range are defined as follows:

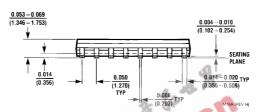




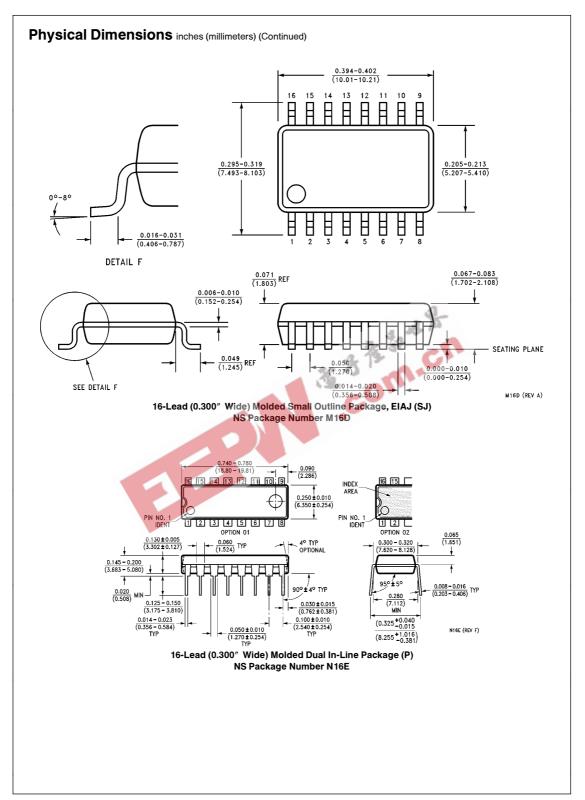








16-Lead (0.150" Wide) Molded Small Outline Package, JEDEC (S) NS Package Number M16A



Physical Dimensions inches (millimeters) (Continued) 0.050 - 0.0800.371 - 0.390(1.270 - 2.032)(9.423 - 9.906) $\frac{0.050\pm0.005}{(1.270\pm0.127)} \text{ TYP}$ $\frac{0.004 - 0.006}{(0.102 - 0.152)} \text{ TYF}$ 0.007 - 0.018(0.178 – 0.457) TYP <-- 0.000 MIN TYP 0.250 - 0.370 (6.350 - 9.398)0.300 0.245 - 0.275 (7.620) MAX GLASS $\overline{(6.223-6.985)}$ **Y** 0.008 - 0.012(0.203 - 0.305)DETAIL A PIN NO. 1 0.250 - 0.370DETAIL A IDENT (6.350 - 9.398)TYP WIGAUREV HD 16-Lead Ceramic Flatpak (F) NS Package Number W16A 0.026 - 0.040(0.660 - 1.016)E PA

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