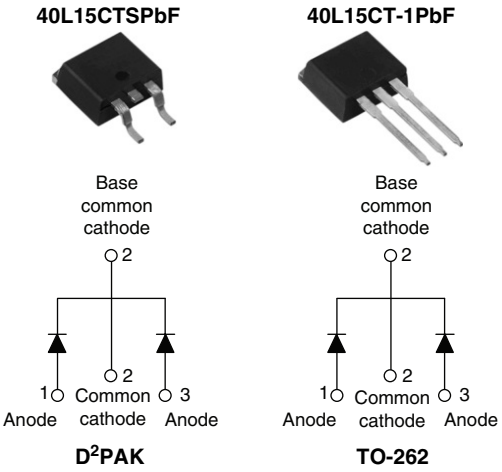
Schottky Rectifier, 2 x 20 A



FEATURES

- 125 °C T_J operation (V_R < 5 V)
- Center tap module
- Optimized for OR-ing applications
- Ultra low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for Q101 level

Pb Available

RoHS* COMPLIANT

DESCRIPTION

The center tap Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

PRODUCT SUMMARY	
$I_{F(AV)}$	2 x 20 A
V_R	15 V
I_{RM}	600 mA at 100 °C

MAJOR RATINGS AND CHARACTERISTICS			
SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Rectangular waveform	40	A
V_{RRM}		15	V
I_{FSM}	t_p = 5 µs sine	700	A
V_F	19 Apk, T_J = 125 °C (per leg, typical)	0.25	V
T_J		- 55 to 125	°C

VOLTAGE RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	40L15CTSPbF 40L15CT-1PbF	UNITS
Maximum DC reverse voltage	V_R	T_J = 100 °C	15	V
Maximum working peak reverse voltage	V_{RWM}			

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	per leg	$I_{F(AV)}$	50 % duty cycle at T_C = 85 °C, rectangular waveform		20	A
	per device				40	
Maximum peak one cycle non-repetitive surge current per leg		I_{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	700	
			10 ms sine or 6 ms rect. pulse		330	
Non-repetitive avalanche energy per leg		E_{AS}	T_J = 25 °C, I_{AS} = 2 A, L = 6 mH		10	mJ
Repetitive avalanche current per leg		I_{AR}	Current decaying linearly to zero in 1 µs Frequency limited by T_J maximum V_A = 1.5 x V_R typical		2	A

* Pb containing terminations are not RoHS compliant, exemptions may apply

ELECTRICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Maximum forward voltage drop per leg See fig. 1	V_{FM} (1)	19 A	T_J = 25 °C	-	0.41	V
		40 A		-	0.52	
		19 A	T_J = 125 °C	0.25	0.33	
		40 A		0.37	0.50	
Reverse leakage current per leg See fig. 2	I_{RM} (1)	T_J = 25 °C	V_R = Rated V_R	-	10	mA
		T_J = 100 °C		-	600	
Threshold voltage	$V_{F(TO)}$	T_J = T_J maximum		0.182		V
Forward slope resistance	r_t			7.6		mΩ
Maximum junction capacitance per leg	C_T	V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz) 25 °C		-	2000	pF
Typical series inductance per leg	L_S	Measured lead to lead 5 mm from package body		8	-	nH
Maximum voltage rate of change	dV/dt	Rated V_R		10 000		V/µs

Note

(1) Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS

PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range		T_J		- 55 to 125	°C
Maximum storage temperature range		T_{Stg}		- 55 to 150	
Maximum thermal resistance, junction to case per leg		R_{thJC}	DC operation See fig. 4	1.5	°C/W
Typical thermal resistance, case to heatsink		R_{thCS}	Mounting surface, smooth and greased	0.50	
Maximum thermal resistance, junction to ambient		R_{thJA}	DC operation	40	
Approximate weight				2	g
				0.07	oz.
Mounting torque	minimum		Non-lubricated threads	6 (5)	kgf · cm (lbf · in)
	maximum			12 (10)	
Marking device			Case style D^{2}PAK	40L15CTS	
			Case style TO-262	40L15CT-1	

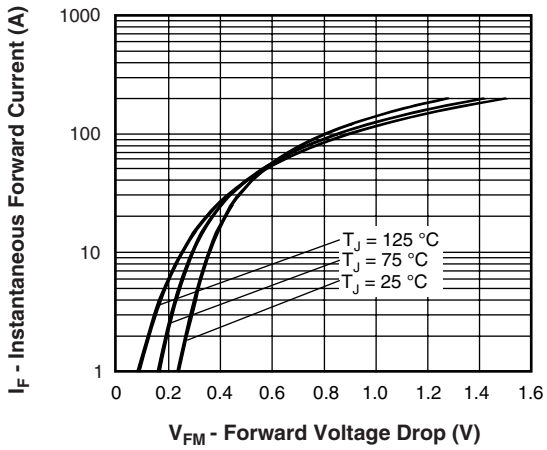


Fig. 1 - Maximum Forward Voltage Drop Characteristics

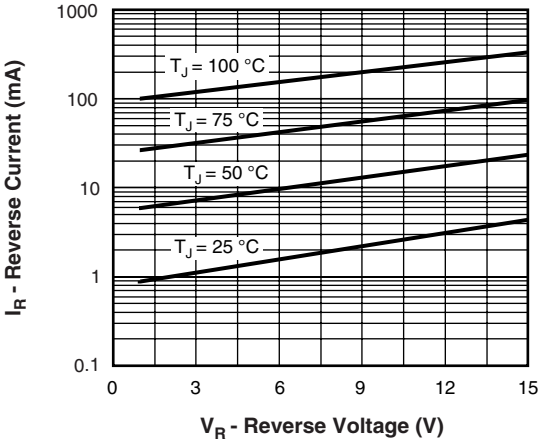


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

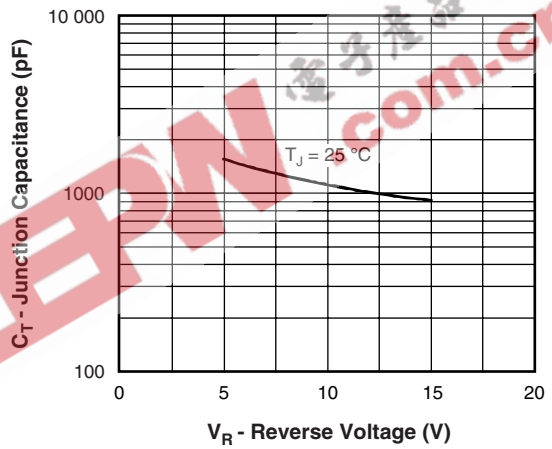


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

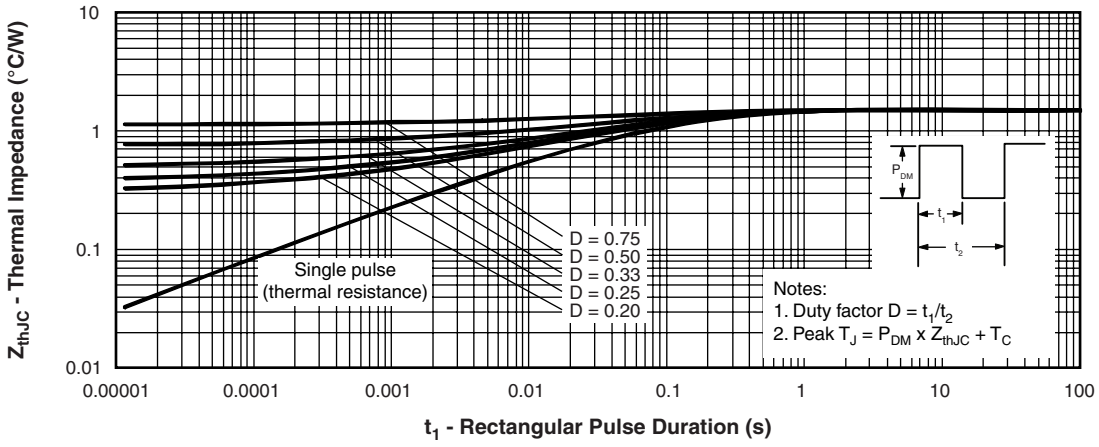


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

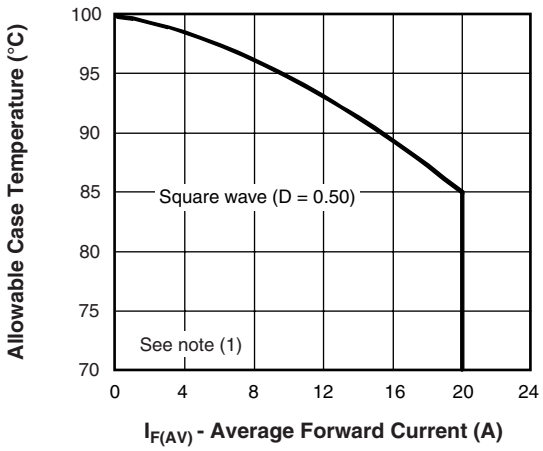


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

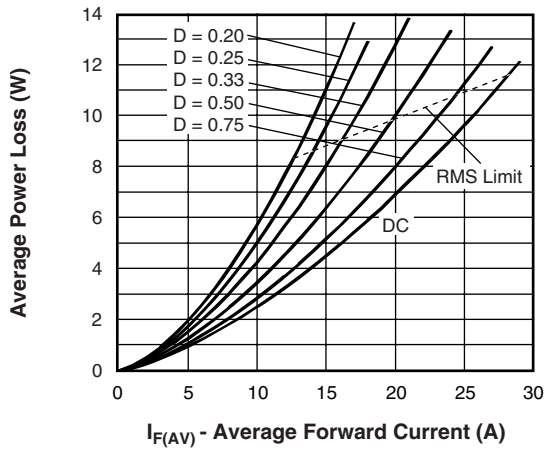


Fig. 6 - Forward Power Loss Characteristics

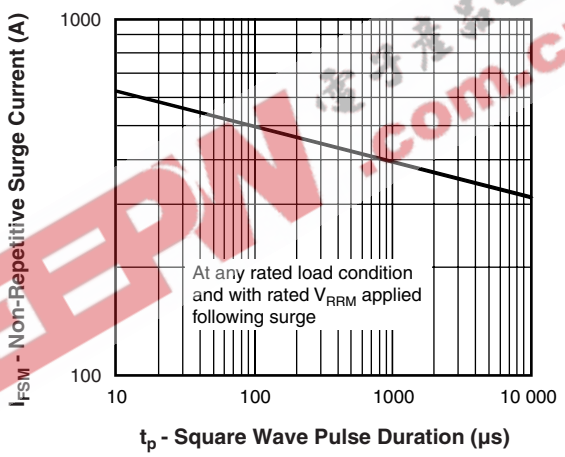


Fig. 7 - Maximum Non-Repetitive Surge Current

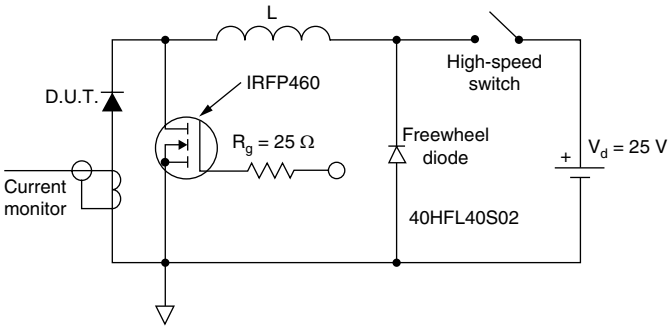


Fig. 8Unclamped Inductive Test Circuit

Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;
Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}$/D) (see fig. 6);
Pd_{REV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = 80 % rated V_R

ORDERING INFORMATION TABLE

Device code

40	L	15	C	T	S	TRL	PbF
1	2	3	4	5	6	7	8

1 - Current rating (40 A)

2 - L = Schottky "L" series

3 - Voltage rating (15 V)

4 - C = Common cathode

5 - T = TO-220

6 - • S = D^2PAK
• -1 = TO-262

7 - • None = Tube (50 pieces)
• TRL = Tape and reel (left oriented - for D^2PAK only)
• TRR = Tape and reel (right oriented - for D^2PAK only)

8 - • None = Standard production
• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS	
Dimensions	http://www.vishay.com/doc?95014
Part marking information	http://www.vishay.com/doc?95008
Packaging information	http://www.vishay.com/doc?95032

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