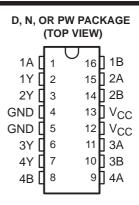
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- Inputs Are TTL-Voltage Compatible
- Center-Pin V_{CC} and GND Configurations **Minimize High-Speed Switching Noise**
- **EPIC™** (Enhanced-Performance Implanted CMOS) 1-µm Process
- 500-mA Typical Latch-Up Immunity at 125°C
- **Package Options Include Plastic** Small-Outline (D), Plastic Thin Shrink Small-Outline (PW), and Standard Plastic 300-mil DIPs (N) Packages



description

The 74ACT11008 contains four independent 2-input AND gates. It performs the Boolean function Y = A•B or $Y = \overline{A} + \overline{B}$ in positive logic.

The 74ACT11008 is characterized for operation from -40°C to 85°

FUNCTION TABLE (each gate)

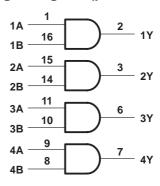
INP	JTS	OUTPUT
Α	В	Y
Н	Н	H
4	X	L
Х	L	L

logic symbol†

	_		
1A	_1	&	2
1B	16		1Y
ID	15		
2A			3
	14		2Y
2B	11		
3A			6
	10		3Y
3B	9		
4A	9		7
	8		4Y
4B			
			•

[†]This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

logic diagram (positive logic)





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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage range, V _{CC}	0.5 V to 6 V
Input voltage range, V _I (see Note 1)	0.5 V to V _{CC} + 0.5 V
Output voltage range, VO (see Note 1)	0.5 V to V _{CC} + 0.5 V
Input clamp current, I_{IK} ($V_I < 0$ or $V_I > V_{CC}$)	±20 mA
Output clamp current, I _{OK} (V _O < 0 or V _O > V _{CC})	±50 mA
Continuous output current, $I_O(V_O = 0 \text{ to } V_{CC})$	±50 mA
Continuous current through V _{CC} or GND	±100 mA
Maximum power dissipation at $T_A = 55^{\circ}C$ (in still air) (see Note 2):	D package 1.3 W
	N package 1.1 W
	PW package 0.5 W
Storage temperature range, T _{stg}	–65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.
 - 2. The maximum package power dissipation is calculated using a junction temperature of 150 °C and a board trace length of 750 mils, except for the N package, which has a trace length of zero.

recommended operating conditions

	4 19	MIN	MAX	UNIT
Vcс	Supply voltage	4.5	5.5	V
VIH	High-level input voltage	2		V
V _{IL}	Low-level input voltage		0.8	V
VI	Input voltage	0	VCC	V
VO	Output voltage	0	VCC	V
loн	High-level output current		-24	mA
loL	Low-level output current		24	mA
Δt/Δν	Input transition rise or fall rate	0	10	ns/V
TA	Operating free-air temperature	-40	85	°C



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electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	Voo	T _A = 25°C		MIN	MAX	UNIT	
PARAMETER	TEST CONDITIONS	VCC	MIN	TYP	MAX	IVIIIN	IVIAA	UNIT
W	I _{OH} = -50 μA	4.5 V	4.4			4.4		V
		5.5 V	5.4			5.4		
VOH	I _{OH} = -24 mA	4.5 V	3.94			3.7		
		5.5 V	4.94			4.7		
	ΙΟL = 50 μΑ	4.5 V			0.1		0.1	V
		5.5 V			0.1		0.1	
VOL	I _{OL} = 24 mA	4.5 V			0.36		0.44	
		5.5 V			0.36		0.44	
I _{OH} †	V _O = 3.85 V	5.5 V				-75		mA
l _{OL} †	V _O = 1.65 V	5.5 V				75		mA
l _l	$V_I = V_{CC}$ or GND	5.5 V			±0.1		±1	μΑ
Icc	$V_I = V_{CC}$ or GND, $I_O = 0$	5.5 V	-		4		40	μΑ
∆ICC [‡]	One input at 3.4 V, Other inputs at GND or V _{CC}	5.5 V	水市		0.9		1	mA
Ci	$V_I = V_{CC}$ or GND	5 V		3.5	·			pF

[†] Not more than one output should be tested at a time, and the duration of the test should not exceed 1 second.

switching characteristics over recommended ranges of supply voltage and free-air temperature (unless otherwise noted) (see Figure 1)

PARAMETER	FROM	TO (OUTPUT)	T _A = 25°C			MIN MAX	LINIT	
PARAMETER	(INPUT)		MIN	TYP	MAX	IVIIN	WAX	UNIT
t _{PLH}	A or B		1.5	5.8	8	1.5	9	ns
t _{PHL}	AOIB	1	1.5	5.2	7.7	1.5	8.2	115

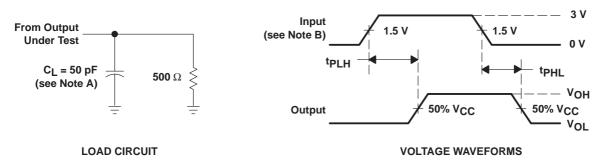
operating characteristics, $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$

PARAMETER		TEST CON	TYP	UNIT	
C _{pd}	Power dissipation capacitance per gate	$C_L = 50 pF$,	f = 1 MHz	29	pF

[‡] This is the increase in supply current for each input that is at one of the specified TTL voltage levels rather than 0 or V_{CC}.

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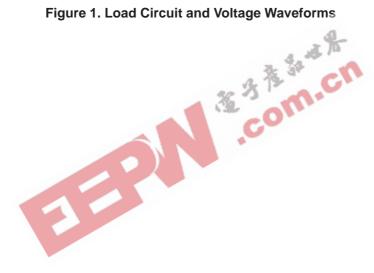
PARAMETER MEASUREMENT INFORMATION



NOTES: A. C_L includes probe and jig capacitance.

- B. Input pulses are supplied by generators having the following characteristics: PRR \leq 1 MHz, $Z_O = 50 \ \Omega$, $t_f = 3 \ ns$, $t_f = 3 \ ns$.
- C. The outputs are measured one at a time with one input transition per measurement.

Figure 1. Load Circuit and Voltage Waveforms



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