

## DM74LS266

### Quad 2-Input Exclusive-NOR Gate with Open-Collector Outputs

#### General Description

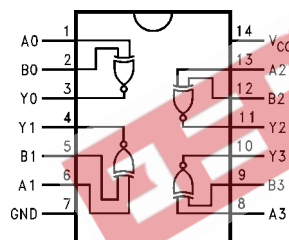
This device contains four independent gates each of which performs the logic exclusive-NOR function. Outputs are open collector.

#### Ordering Code:

Order Number	Package Number	Package Description
DM74LS266M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow
DM74LS266N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

#### Connection Diagram



#### Truth Table

Inputs		Outputs
A	B	Y
L	L	H
L	H	L
H	L	L
H	H	H

H = HIGH Voltage Level  
L = LOW Voltage Level

**Absolute Maximum Ratings**(Note 1)

Supply Voltage	7V
Input Voltage	7V
Operating Free Air Temperature Range	0°C to +70°C
Storage Temperature Range	-65°C to +150°C

**Note 1:** The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

**Recommended Operating Conditions**

Symbol	Parameter	Min	Nom	Max	Units
$V_{CC}$	Supply Voltage	4.75	5	5.25	V
$V_{IH}$	HIGH Level Input Voltage	2			V
$V_{IL}$	LOW Level Input Voltage			0.8	V
$V_{OH}$	HIGH Level Output Voltage			5.5	V
$I_{OL}$	LOW Level Output Current			8	mA
$T_A$	Free Air Operating Temperature	0		70	°C

**Electrical Characteristics**

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
$V_I$	Input Clamp Voltage	$V_{CC} = \text{Min}, I_I = -18 \text{ mA}$			-1.5	V
$I_{CEX}$	HIGH Level Output Current	$V_{CC} = \text{Min}, V_O = 5.5\text{V}, V_{IL} = \text{Max}$			100	$\mu\text{A}$
$V_{OL}$	LOW Level Output Voltage	$V_{CC} = \text{Min}, I_{OL} = \text{Max}, V_{IH} = \text{Min}$			0.5	V
		$I_{OL} = 4 \text{ mA}, V_{CC} = \text{Min}$			0.4	
$I_I$	Input Current @ Max Input Voltage	$V_{CC} = \text{Max}, V_I = 7\text{V}$			0.2	mA
$I_{IH}$	HIGH Level Input Current	$V_{CC} = \text{Max}, V_I = 2.7\text{V}$			40	$\mu\text{A}$
$I_{IL}$	LOW Level Input Current	$V_{CC} = \text{Max}, V_I = 0.4\text{V}$			-0.8	mA
$I_{OS}$	Short Circuit Output Current	$V_{CC} = \text{Max (Note 3)}$	-20		-100	mA
$I_{CC}$	Supply Current	$V_{CC} = \text{Max}$			13	mA

**Note 2:** All typicals are at  $V_{CC} = 5\text{V}, T_A = 25^\circ\text{C}$ .

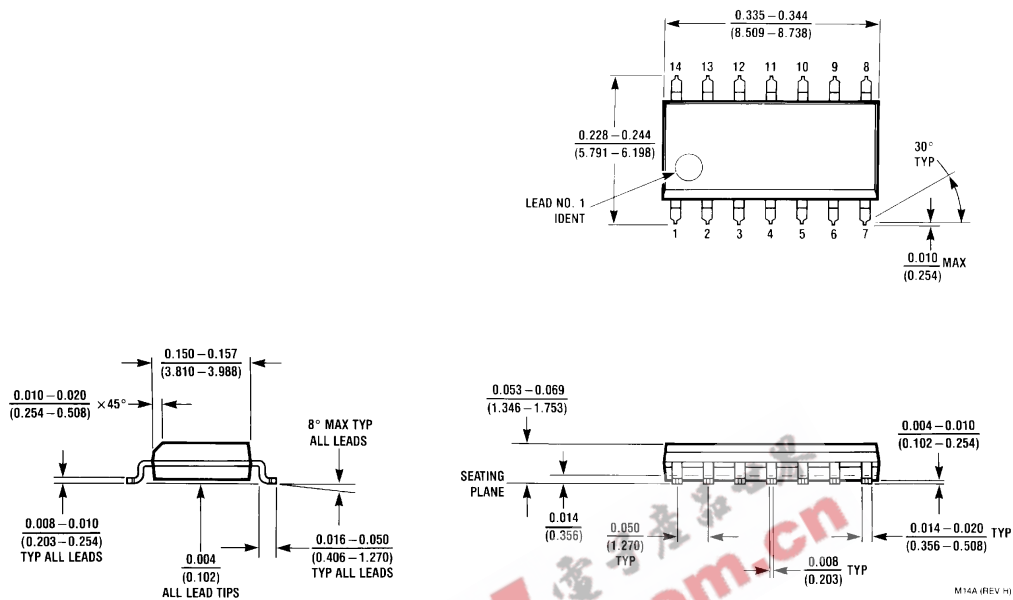
**Note 3:** Not more than one output should be shorted at a time, and the duration should not exceed one second.

**Switching Characteristics**

$V_{CC} = 5\text{V}, T_A = 25^\circ\text{C}$

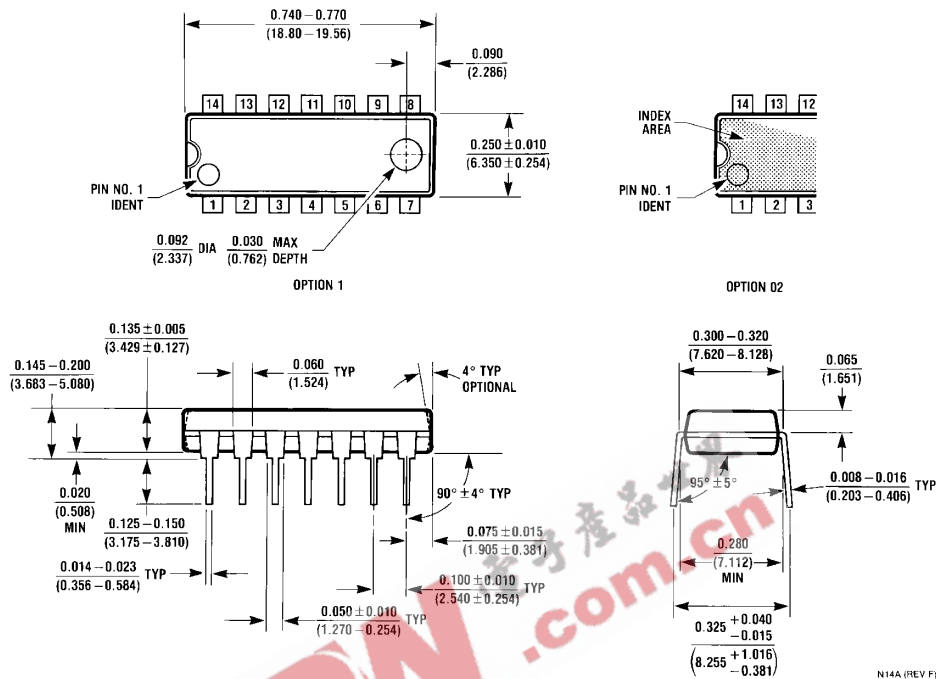
Symbol	Parameter	$R_L = 2 \text{ k}\Omega$ $C_L = 15 \text{ pF}$		Units
		Min	Max	
$t_{PLH}$	Propagation Delay Time LOW-to-HIGH Level Output		23	ns
$t_{PHL}$	Propagation Delay Time HIGH-to-LOW Level Output		23	ns

# Physical Dimensions inches (millimeters) unless otherwise noted



14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow  
Package Number M14A

# Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



**14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide  
Package Number N14A**

N14A (REV F)

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