

FDR8305N

Dual N-Channel 2.5V Specified PowerTrench® MOSFET

General Description

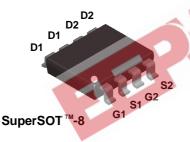
These N-Channel 2.5V specified MOSFETs are produced using Fairchild Semiconductor's advanced PowerTrench process that has been especially tailored to minimize the on-state resistance and yet maintain low gate charge for superior switching performance.

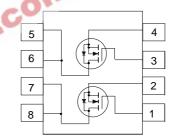
Applications

- Load switch
- Motor driving
- Power Management

Features

- 4.5 A, 20 V. $R_{DS(ON)} = 0.022~\Omega~$ @ $V_{GS} = 4.5~V$ $R_{DS(ON)} = 0.028~\Omega~$ @ $V_{GS} = 2.5~V$.
- Low gate charge (16.2nC typical).
- Fast switching speed.
- \bullet High performance trench technology for extremely low $R_{\text{DS}(\text{ON})}.$
- Small footprint (38% smaller than a standard SO-8);low profile package (1 mm thick); power handling capability similar to SO-8.





Absolute Maximum Ratings T_A = 25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V _{DSS}	Drain-Source Voltage		20	V
V _{GSS}	Gate-Source Voltage		±8	V
I _D	Drain Current - Continuous	(Note 1a)	4.5	A
	- Pulsed		20	
P _D	Power Dissipation for Single Operation	(Note 1a)	0.8	W
T _J , T _{stg}	Operating and Storage Junction Temperature Range		-55 to +150	°C

Thermal Characteristics

R _e JA	Thermal Resistance, Junction-to-Ambient	(Note 1a)	156	°C/W
R _e JC	Thermal Resistance, Junction-to-Case	(Note 1)	40	°C/W

Package Marking and Ordering Information

Device Marking	Marking Device Reel Size		Tape Width	Quantity	
.8305	FDR8305N	13"	12mm	3000 units	

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Char	acteristics	•	•			
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	20			V
<u>ΔBV_{DSS}</u> ΔT _J	Breakdown Voltage Temperature Coefficient	$I_D = 250 \mu A$, Referenced to $25^{\circ}C$		14		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 16 V, V _{GS} = 0 V			1	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 8 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -8 V, V _{DS} = 0 V			-100	nA
On Char	acteristics (Note 2)					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.4	0.85	1.5	V
<u>ΔVGS(th)</u> ΔTJ	Gate Threshold Voltage Temperature Coefficient	I_D = 250 μ A, Referenced to 25°C		-3		mV/°C
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 4.5 V, I _D = 4.5 A V _{GS} =4.5 V, I _D =4.5 A, T _J =125°C V _{GS} = 2.5 V, I _D = 4 A	1	0.015 0.026 0.020	0.022 0.040 0.028	Ω
I _{D(on)}	On-State Drain Current	$V_{GS} = 4.5 \text{ V}, V_{DS} = 5 \text{ V}$	10	l.		Α
g FS	Forward Transconductance	V _{DS} = 4.5 V, I _D = 4.5 A		24		S
Dvnamic	Characteristics	132 -011				
Ciss	Input Capacitance	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V},$		1600		pF
Coss	Output Capacitance	f = 1.0 MHz		380		pF
C _{rss}	Reverse Transfer Capacitance			200		pF
Switchin	g Characteristics (Note 2)					
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 10 \text{ V}, I_D = 1 \text{ A},$		12	22	ns
t _r	Turn-On Rise Time	$V_{GS} = 4.5 \text{ V}, R_{GEN} = 6 \Omega$		15	27	ns
t _{d(off)}	Turn-Off Delay Time	7		35	55	ns
t _f	Turn-Off Fall Time	7		18	30	ns
Qg	Total Gate Charge	$V_{DS} = 10 \text{ V}, I_{D} = 4.5 \text{ A},$		16.2	23	nC
Q _{gs}	Gate-Source Charge	V _{GS} = 4.5 V		2.5		nC
Q _{gd}	Gate-Drain Charge			5.5		nC
Drain-Sc	ource Diode Characteristics a	and Maximum Ratings				
l _S	Maximum Continuous Drain-Source			Ĭ	0.67	Α
V _{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_S = 0.67 \text{ A}$ (Note		0.65	1.2	V

Notes:

1. R_{\textit{\}





 $156^{\circ}\text{C/W}\,$ on a minimum mounting pad of 2oz copper.

Scale 1:1 on letter size paper

2. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

Typical Characteristics

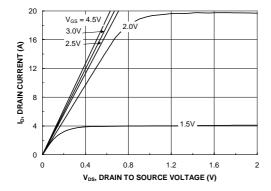


Figure 1. On-Region Characteristics.

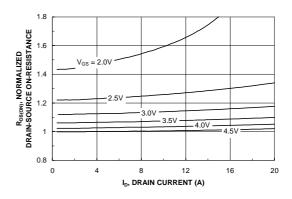


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

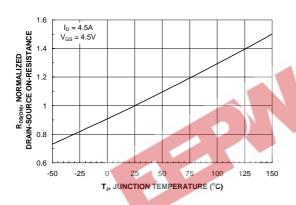


Figure 3. On-Resistance Variation with Temperature.

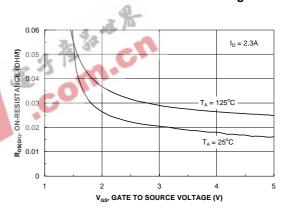


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

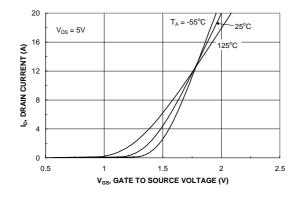


Figure 5. Transfer Characteristics.

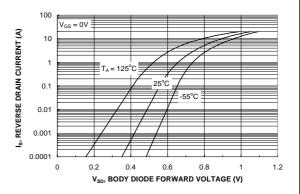
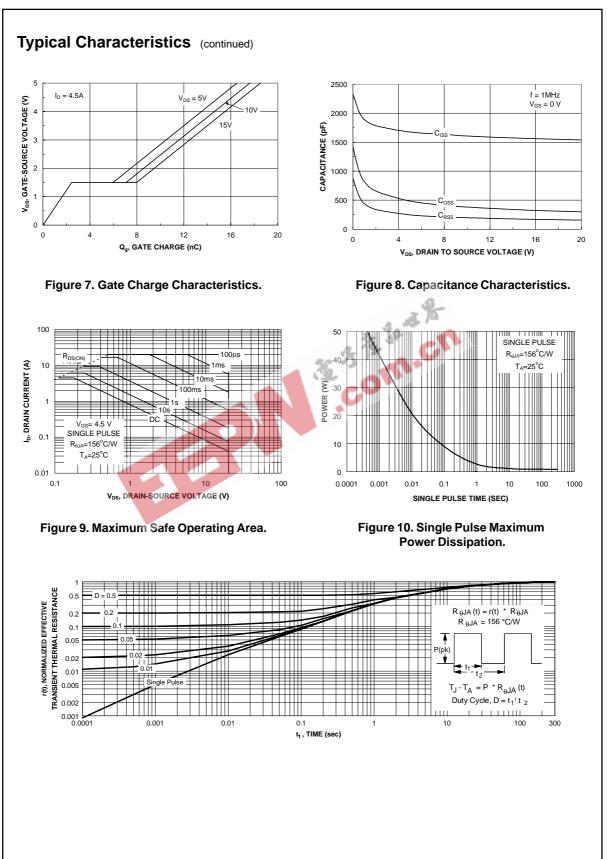
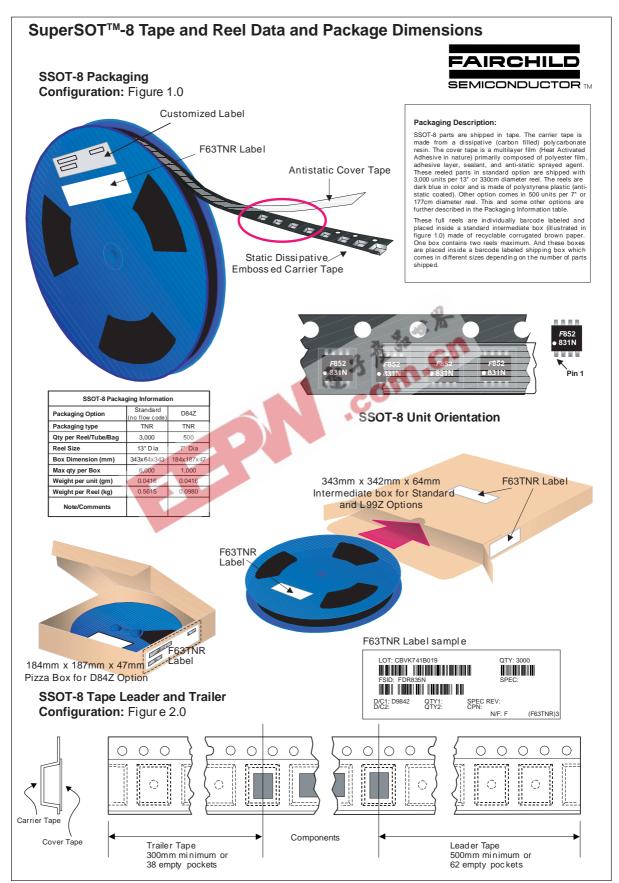
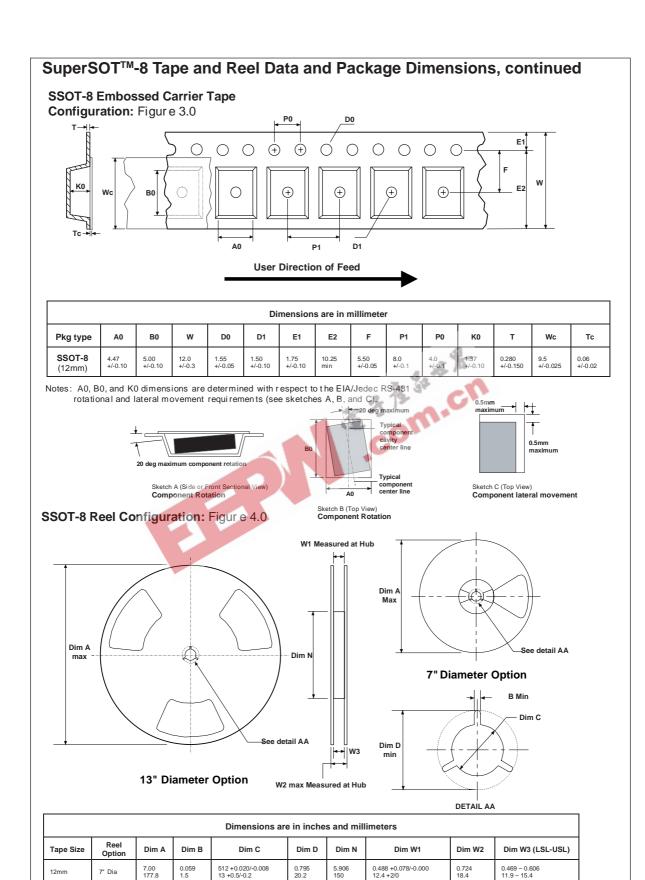


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

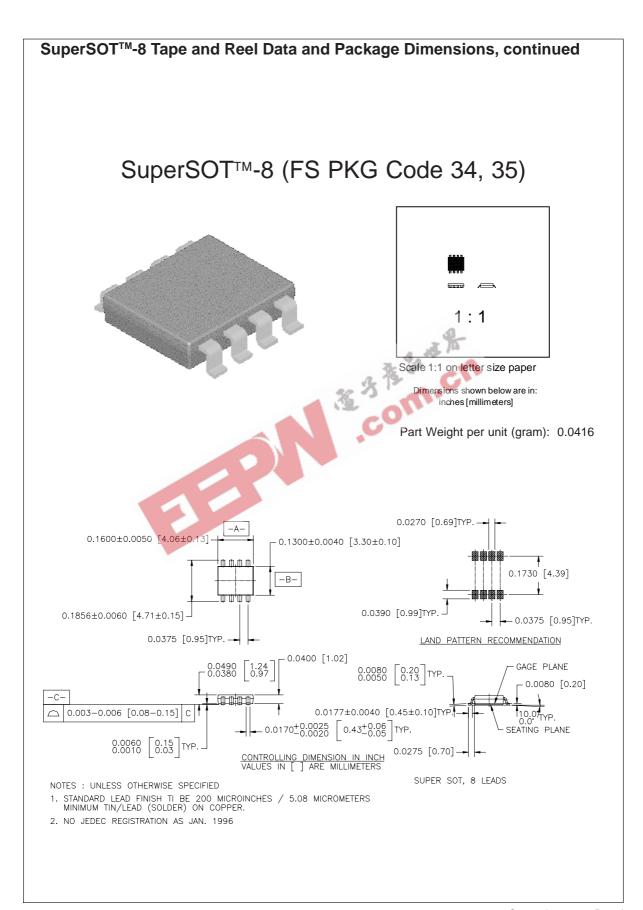






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