2.5V Drive Pch MOS FET RTM002P02

Structure

Silicon P-channel MOS FET

● Features

- 1) Low On-resistance.
- 2) Small package (VMT3).
- 3) 2.5V drive.

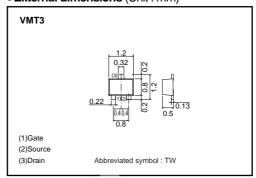
Applications

Switching

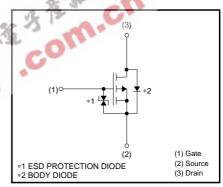
Packaging specifications



●External dimensions (Unit : mm)



●Inner circuit



● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit
Drain-source voltage		V_{DSS}	-20	V
Gate-source voltage		V _{GSS}	±12	V
Droin ourrent	Continuous	ID	±0.2	A
Drain current	Pulsed	I _{DP} *1	±0.4	Α
Total power dissipation		P _D *2	0.15	W
Channel temperature		Tch	150	°C
Range of storage temperature		Tstg	-55 to +150	°C

^{*1} Pw≤10μs, Duty cycle≤1%

●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth(ch-a)*	833	°C/W

^{*} Each terminal mounted on a recommended land

^{*2} Each terminal mounted on a recommended land

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	-	_	±10	μА	Vgs= ±12V, Vps=0V
Drain-source breakdown voltage	V _{(BR) DSS}	-20	_	_	V	I _D = -1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	-	_	-1	μΑ	V _{DS} = -20V, V _{GS} =0V
Gate threshold voltage	V _{GS (th)}	-0.7	_	-2.0	V	V _{DS} = -10V, I _D = -1mA
		-	1.0	1.5	Ω	I _D = -0.2A, V _G S= -4.5V
Static drain-source on-state resistance	R _{DS (on)} *	-	1.1	1.6	Ω	I _D = -0.2A, V _G S= -4V
resistance		-	2.0	3.0	Ω	I _D = -0.15A, V _G S= -2.5V
Forward transfer admittance	Y _{fs} *	0.2	_	_	S	V _{DS} = -10V, I _D = -0.15A
Input capacitance	Ciss	_	50	_	pF	V _{DS} = -10V
Output capacitance	Coss	_	5	_	pF	Vgs= 0V
Reverse transfer capacitance	Crss	_	5	_	pF	f=1MHz
Turn-on delay time	t _{d (on)} *	-	9	_	ns	V _{DD} ≒ −15V
Rise time	tr *	-	6	_	ns	I _D = -0.15A V _G s= -4.5V
Turn-off delay time	t _{d (off)} *	_	35	_	ns	$R_{L}=100\Omega$
Fall time	t _f *	-	45	_	ns	R _G = 10Ω

*Pulsed

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
orward voltage	Vsp	_	_	-1.2	V	I _S = -0.1A, V _G S=0V
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