

Silicon PNP Power Transistors

2SA1725

DESCRIPTION

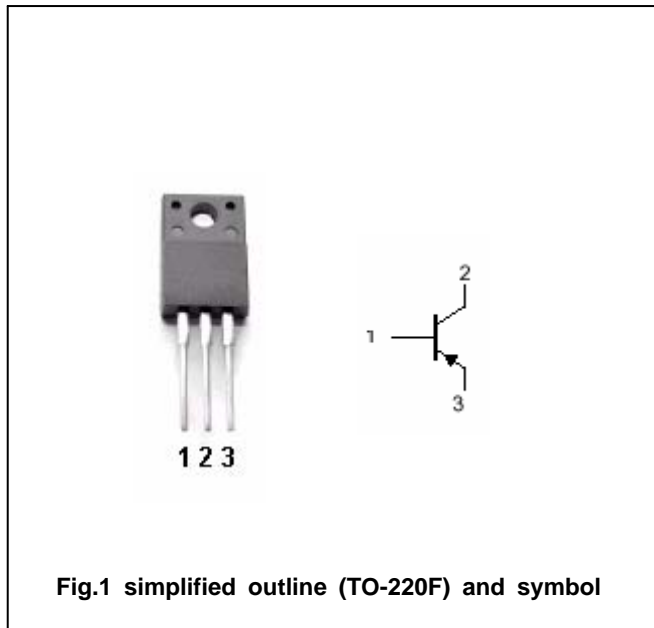
- With TO-220F package
- Complement to type 2SC4511

APPLICATIONS

- Audio and general purpose

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter



Absolute maximum ratings (Ta=25)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	-80	V
V _{CEO}	Collector-emitter voltage	Open base	-80	V
V _{EBO}	Emitter-base voltage	Open collector	-6	V
I _C	Collector current		-6	A
I _B	Base current		-3	A
P _C	Collector dissipation	T _C =25	30	W
T _j	Junction temperature		150	
T _{stg}	Storage temperature		-55~150	

Silicon PNP Power Transistors

2SA1725

CHARACTERISTICS

T_j=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-25mA ; I _B =0	-80			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-2A ; I _B =-0.2A			-0.5	V
I _{CBO}	Collector cut-off current	V _{CB} =-80V ; I _E =0			-10	μ A
I _{EBO}	Emitter cut-off current	V _{EB} =-6V ; I _C =0			-10	μ A
h _{FE}	DC current gain	I _C =-2A ; V _{CE} =-4V	50		180	
C _{OB}	Output capacitance	I _E =0 ; V _{CB} =-10V ; f=1MHz		150		pF
f _T	Transition frequency	I _E =0.5A ; V _{CE} =-12V		20		MHz

Switching times

t _{on}	Turn-on time	I _C =-3.0A ; I _{B1} =-I _{B2} =-0.3A V _{CC} =-30V , R _L =10		0.18		μ s
t _s	Storage time			1.10		μ s
t _f	Fall time			0.21		μ s

◆ h_{FE} Classifications

O	P	Y
50-100	70-140	90-180

Silicon PNP Power Transistors

2SA1725

PACKAGE OUTLINE

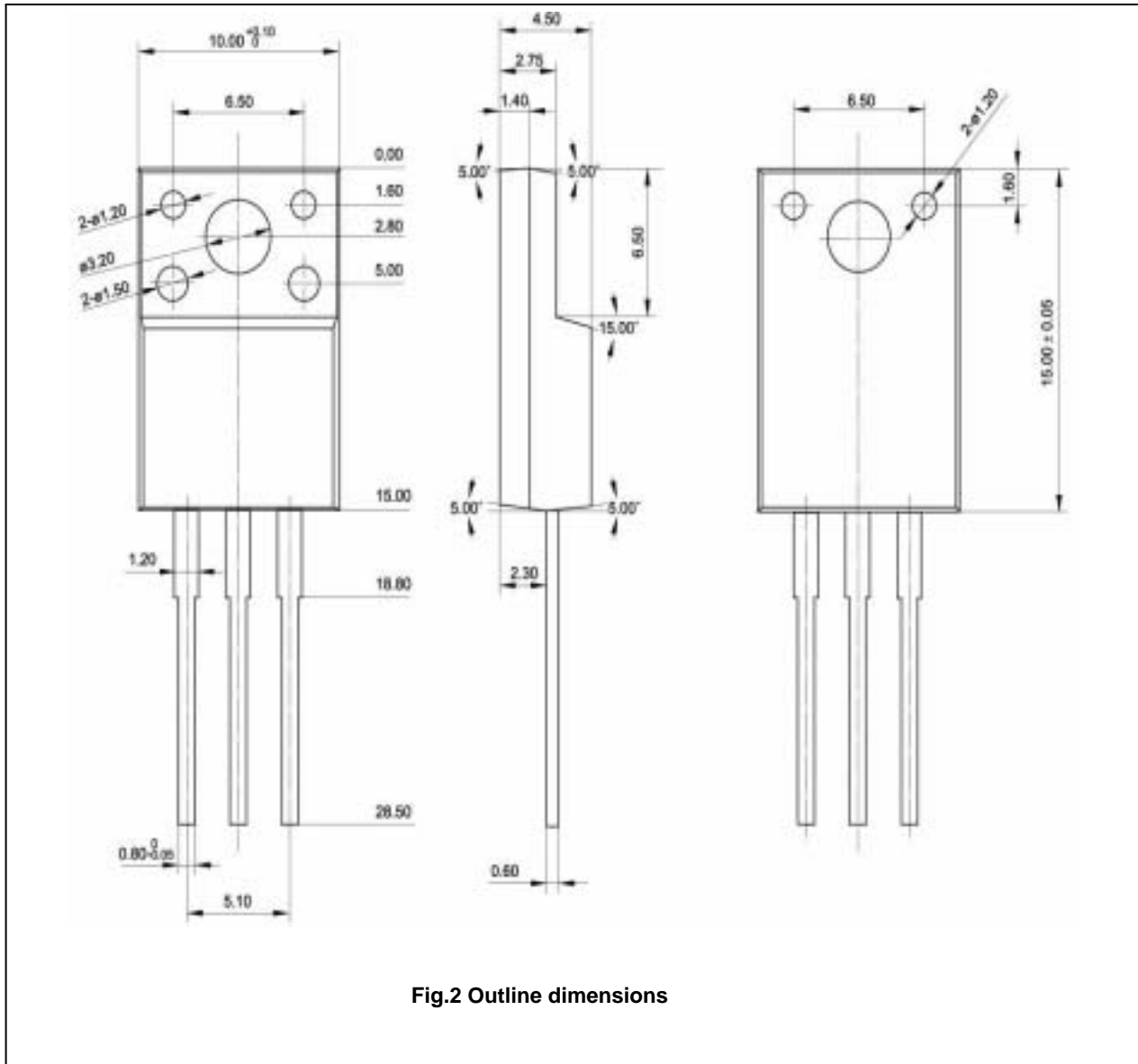


Fig.2 Outline dimensions

Silicon PNP Power Transistors

2SA1725

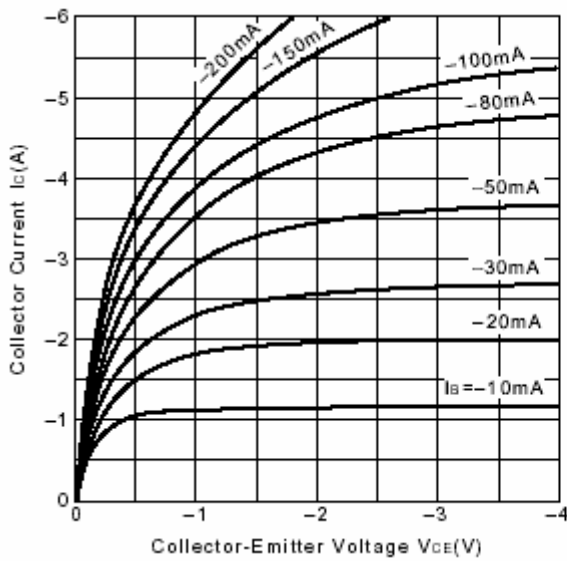


Fig.3 Static Characteristic

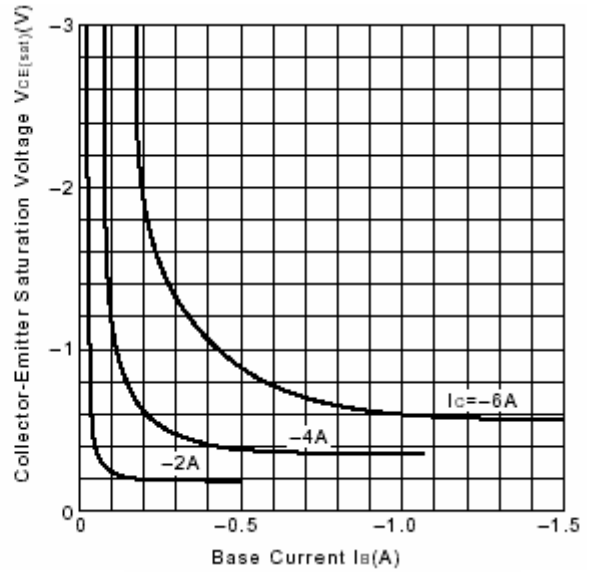


Fig.4 $V_{CE(sat)}$ - I_B Characteristics

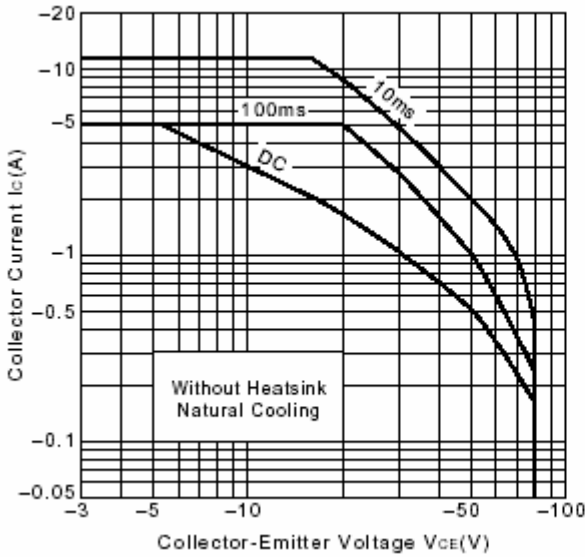


Fig.5 Safe Operating Area

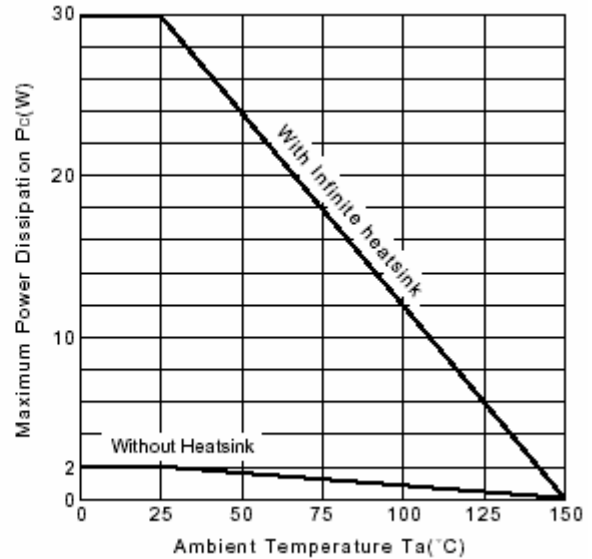


Fig.6 P_c - T_a Derating

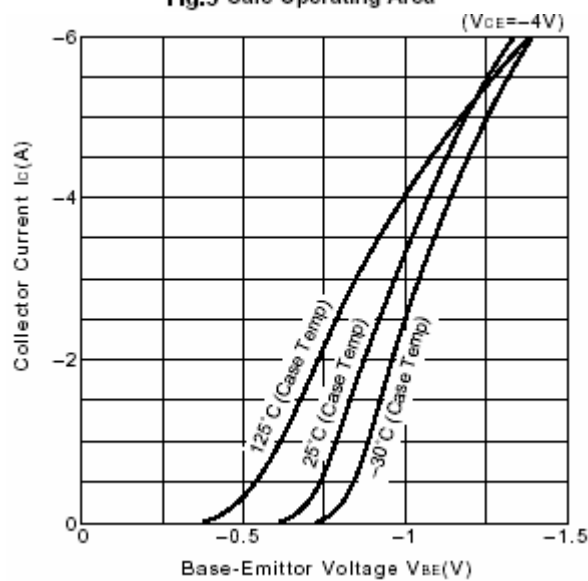


Fig.7 I_C - V_{BE}

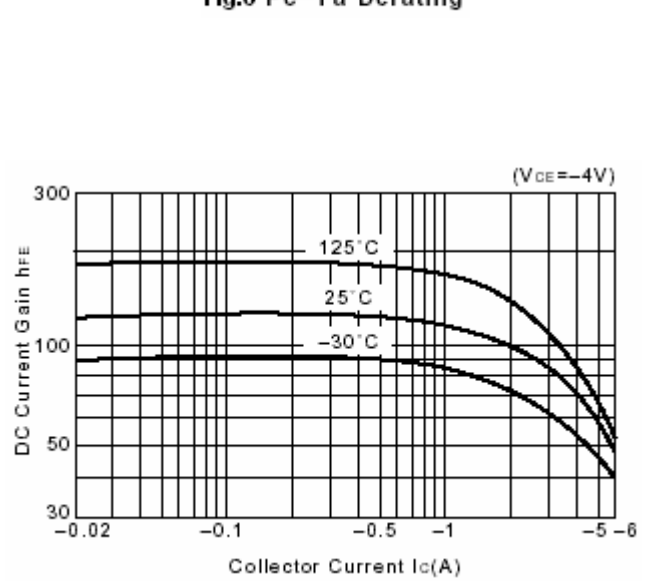


Fig.8 DC current Gain