

Silicon NPN Power Transistors

2SC3894

DESCRIPTION

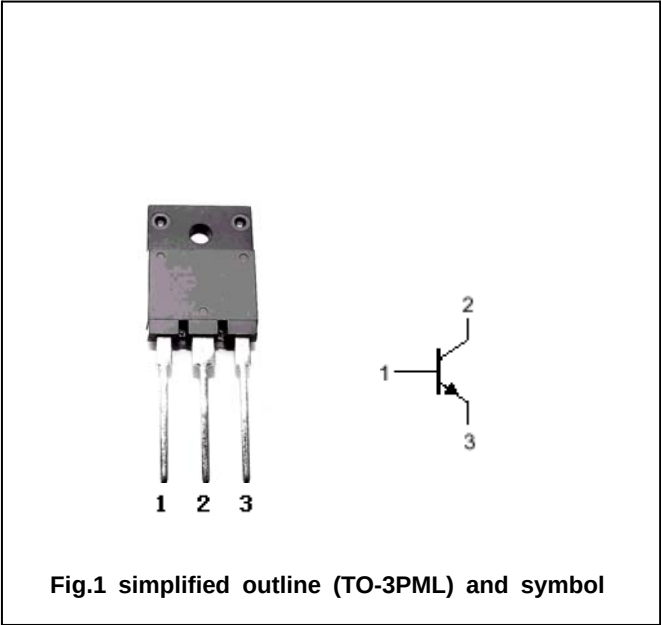
- With TO-3PML package
- High speed
- High breakdown voltage
- High reliability

APPLICATIONS

- Ultrahigh-definition CRT display horizontal deflection output applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter



Absolute maximum ratings (Ta=25℃)

SYMBOL	PARAMETER	CONDITIONS	MAX	UNIT
V _{CBO}	Collector-base voltage	Open emitter	1500	V
V _{CEO}	Collector-emitter voltage	Open base	800	V
V _{EBO}	Emitter-base voltage	Open collector	6	V
I _C	Collector current		6	A
I _{CM}	Collector current-peak		16	A
P _C	Collector dissipation	Ta=25℃	3.0	W
		T _C =25℃	60	
T _j	Junction temperature		150	℃
T _{stg}	Storage temperature		-55~150	℃

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CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-emitter sustaining voltage	I _C =0.1A ; I _B =0	800			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =4A ; I _B =1A			5.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =4A ; I _B =1A			1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =800V ; I _E =0			10	μ A
I _{CES}	Collector cut-off current	V _{CE} =1500V ; R _{BE} =0			1.0	mA
h _{FE-1}	DC current gain	I _C =1A ; V _{CE} =5V	8			
h _{FE-2}	DC current gain	I _C =4A ; V _{CE} =5V	4		8	

Switching times

t _{stg}	Storage time	I _C =4A ; V _{CC} =200V I _{B1} =0.8A ; I _{B2} =1.6A R _L =50 Ω			3.0	μ s
t _f	Fall time			0.1	0.2	μ s

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PACKAGE OUTLINE

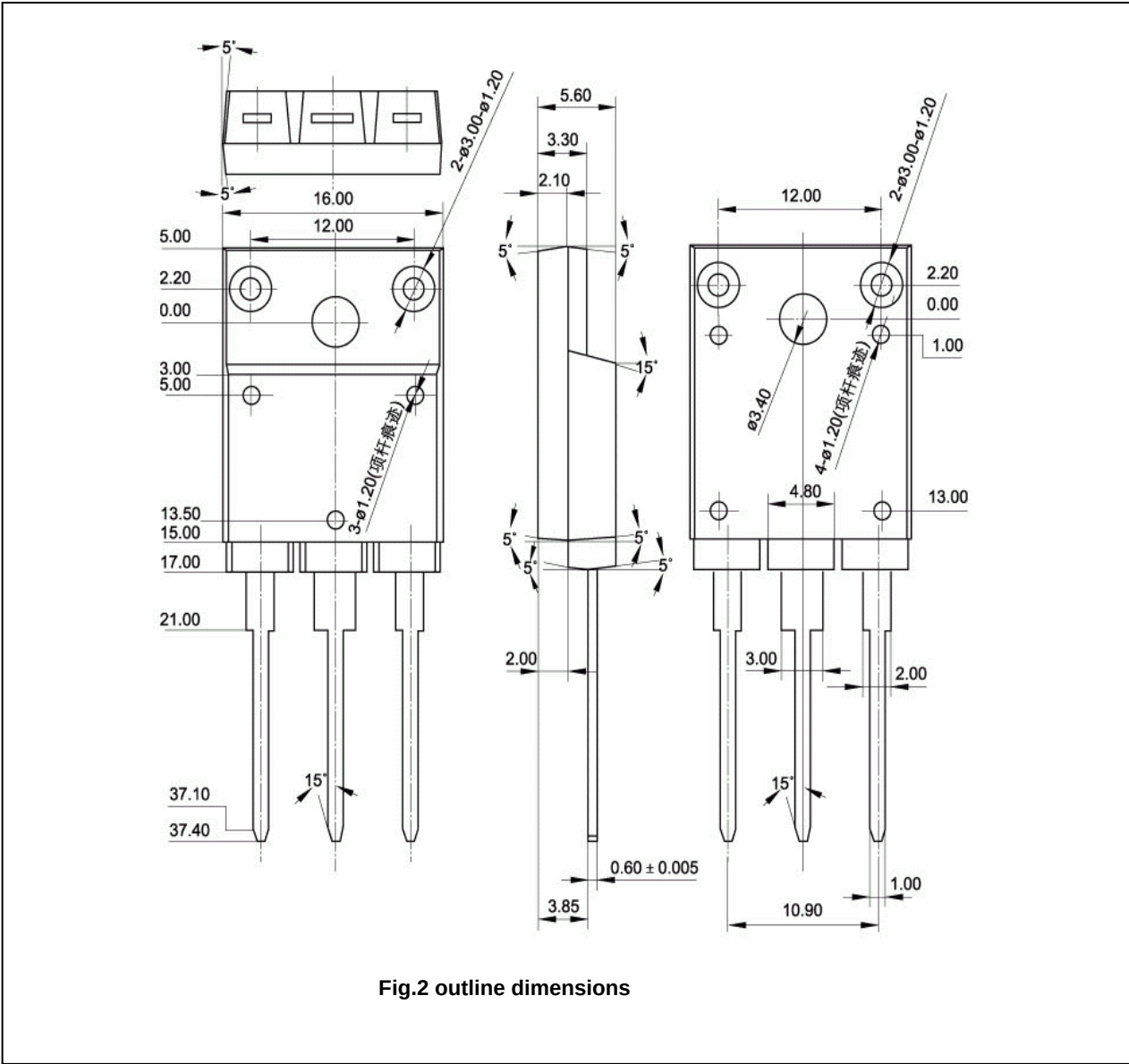


Fig.2 outline dimensions

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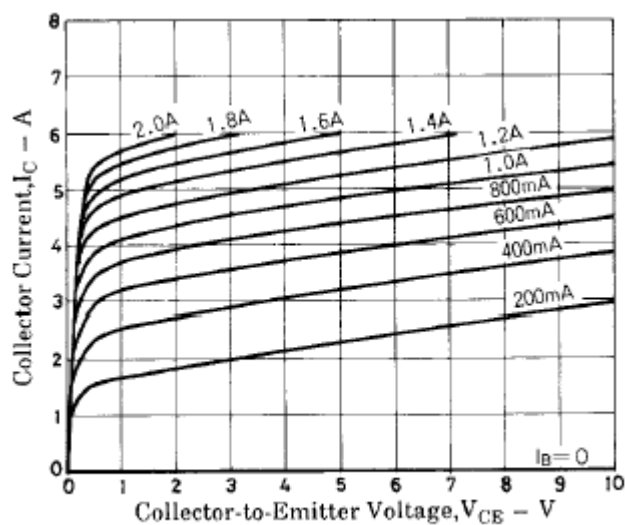


Fig.3 Static Characteristic

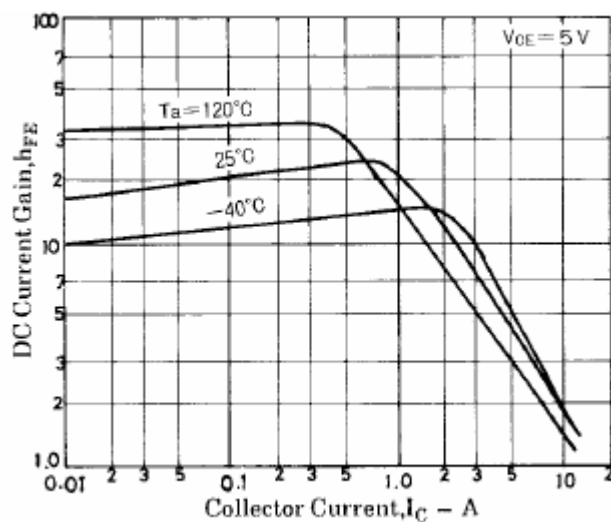


Fig.4 DC current Gain

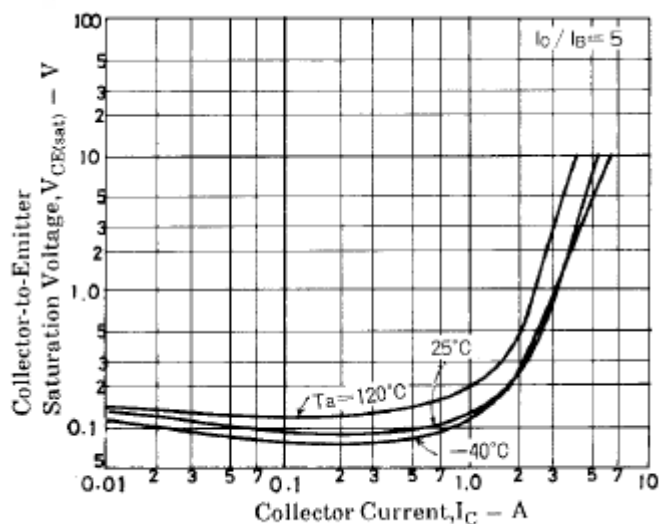


Fig.5 Collector-Emmitter Saturation Voltage

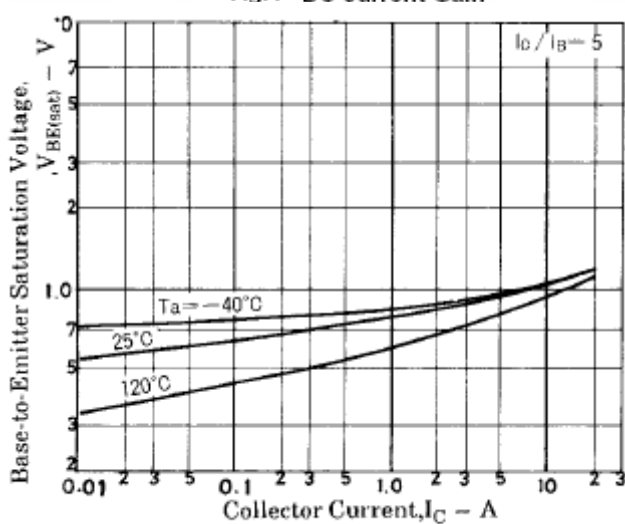


Fig.6 Base-Emmitter Saturation Voltage

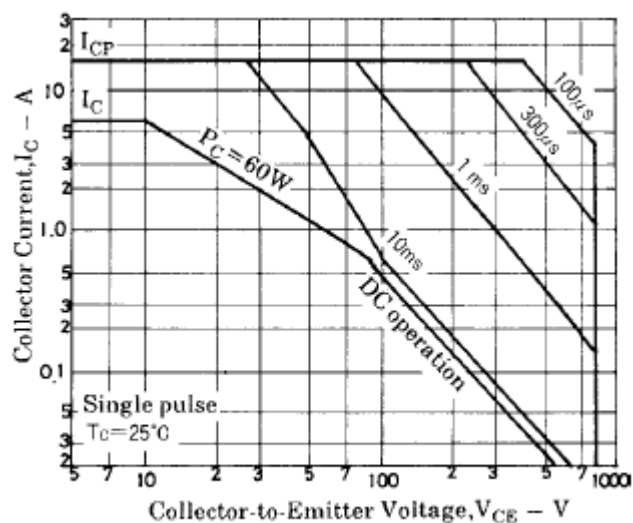


Fig.7 Safe Operating Area