

Silicon NPN Power Transistors

2SC4140

DESCRIPTION

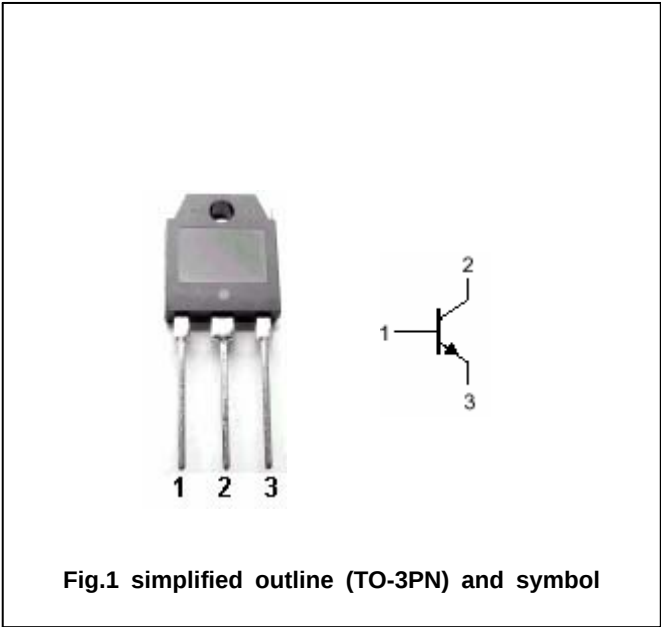
- With TO-3PN package
- High voltage
- High speed switching

APPLICATIONS

- For switching regulator and general purpose applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector;connected to mounting base
3	Emitter



Absolute maximum ratings(Ta=)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	500	V
V _{CEO}	Collector-emitter voltage	Open base	400	V
V _{EBO}	Emitter-base voltage	Open collector	10	V
I _C	Collector current		18	A
I _{CP}	Collector current-pulse		36	A
I _B	Base current		6	A
P _C	Collector power dissipation	T _C =25	130	W
T _j	Junction temperature		150	
T _{stg}	Storage temperature		-55~150	

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CHARACTERISTICS

Tj=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{(BR)CEO}$	Collector-emitter breakdown voltage	$I_C=25mA$; $I_B=0$	400			V
V_{CEsat}	Collector-emitter saturation voltage	$I_C=10A$; $I_B=2A$			0.5	V
V_{BEsat}	Base-emitter saturation voltage	$I_C=10A$; $I_B=2A$			1.3	V
I_{CBO}	Collector cut-off current	$V_{CB}=500V$; $I_E=0$			100	μA
I_{EBO}	Emitter cut-off current	$V_{EB}=10V$; $I_C=0$			100	μA
h_{FE}	DC current gain	$I_C=10A$; $V_{CE}=4V$	10		30	
f_T	Transition frequency	$I_E=-2A$; $V_{CE}=12V$		10		MHz
C_{OB}	Collector output capacitance	$f=1MHz$; $V_{CB}=10V$		165		pF
Switching times						
t_{on}	Turn-on time	$I_C=10A$; $I_{B1}=1A$; $I_{B2}=-2A$; $R_L=20\Omega$ $V_{CC}=200V$			1.0	μs
t_{stg}	Storage time				3.0	μs
t_f	Fall time				0.5	μs

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PACKAGE OUTLINE

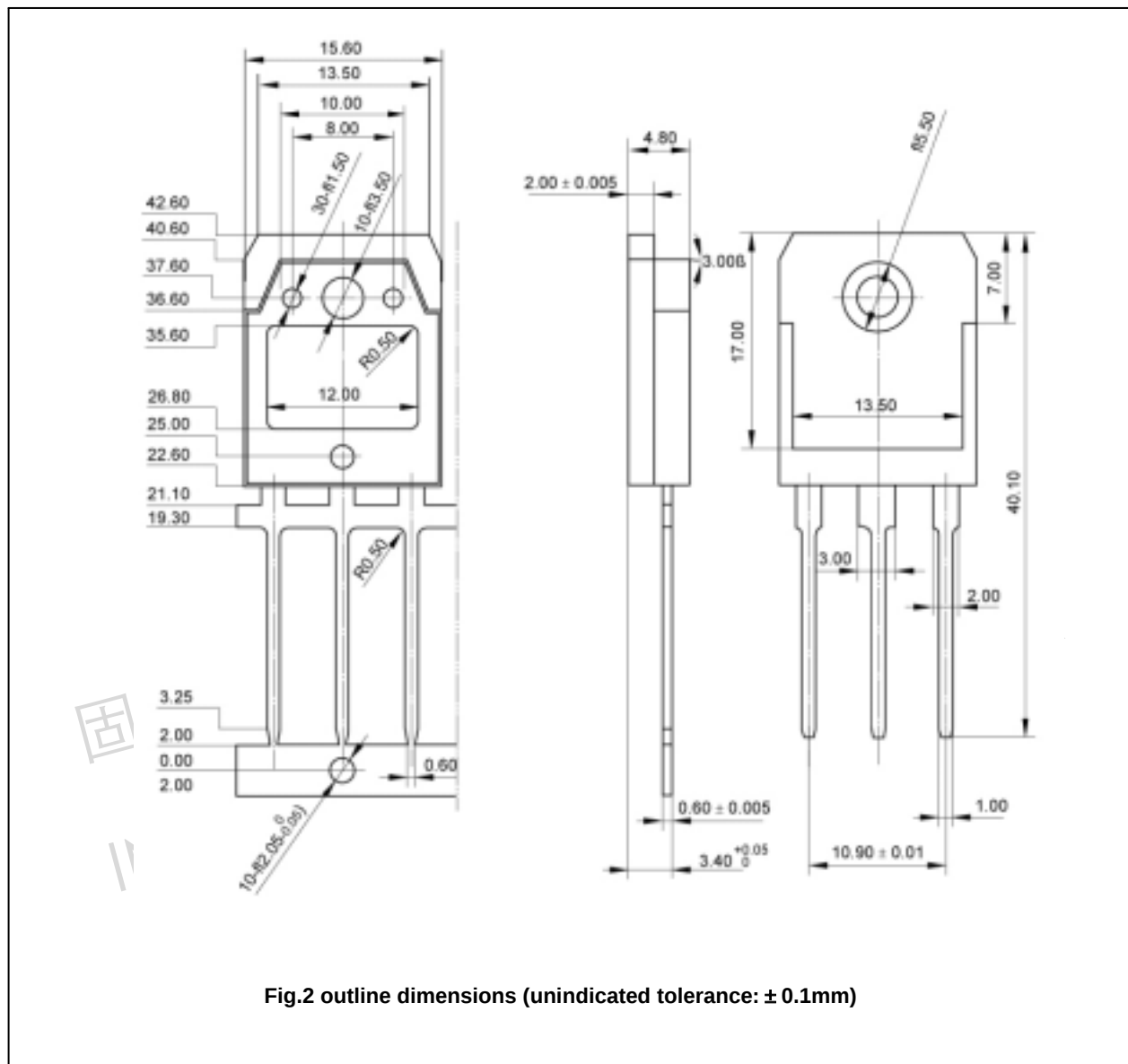


Fig.2 outline dimensions (unindicated tolerance: ± 0.1mm)

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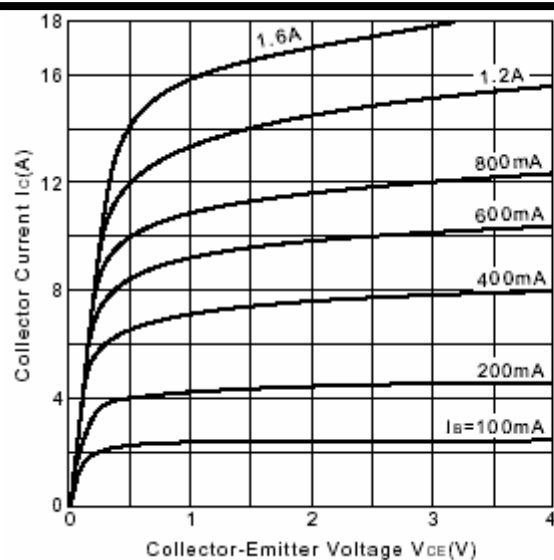


Fig.3 Static Characteristic

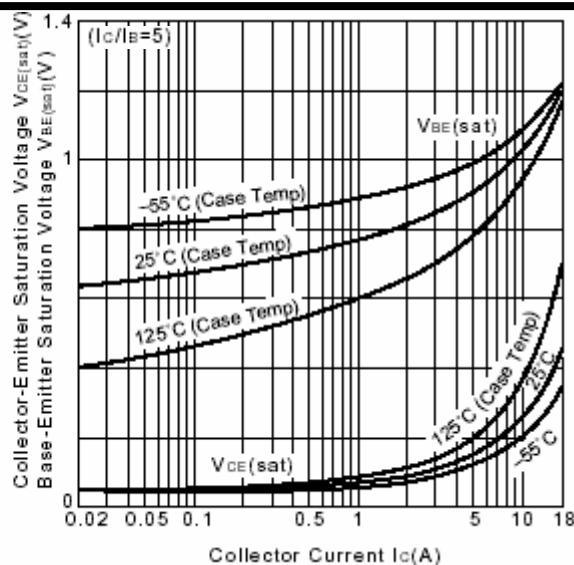
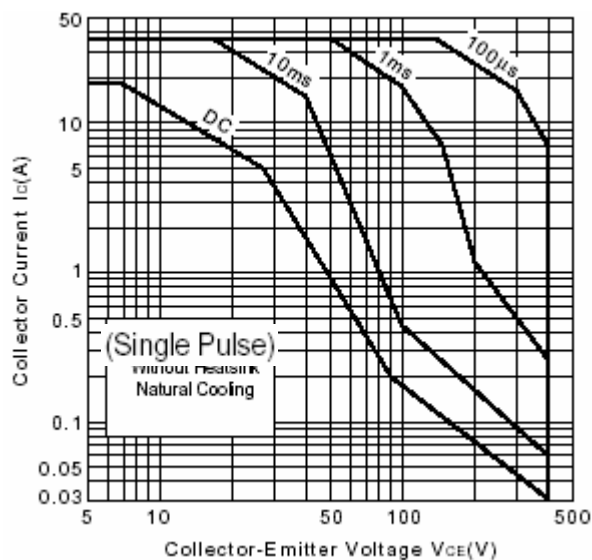
Fig.4 Base-Emitter Saturation Voltage
Collector-Emitter Saturation Voltage

Fig.5 Safe Operating Area

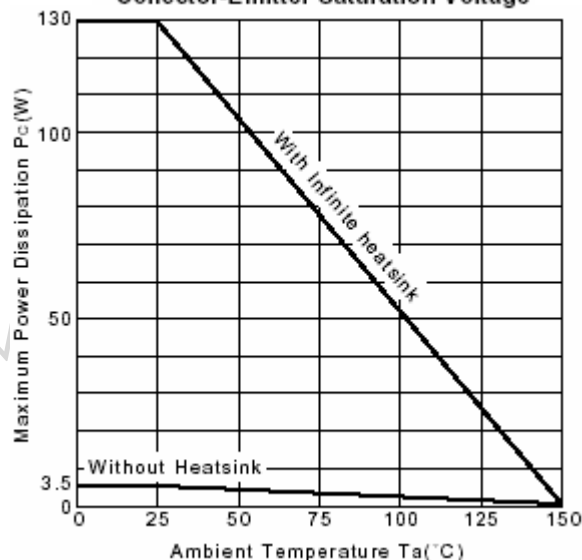
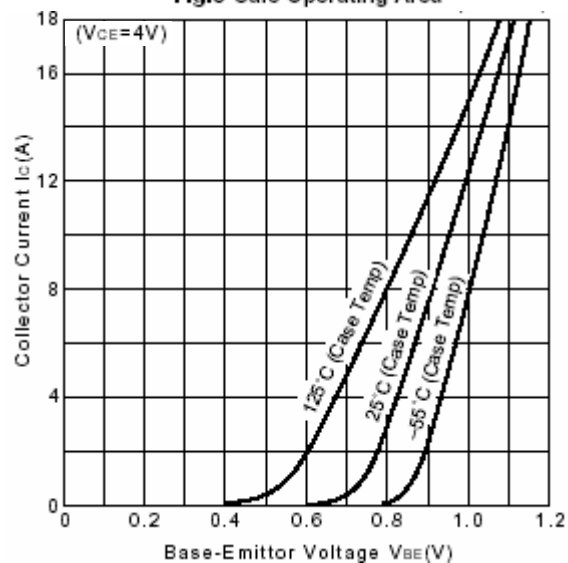
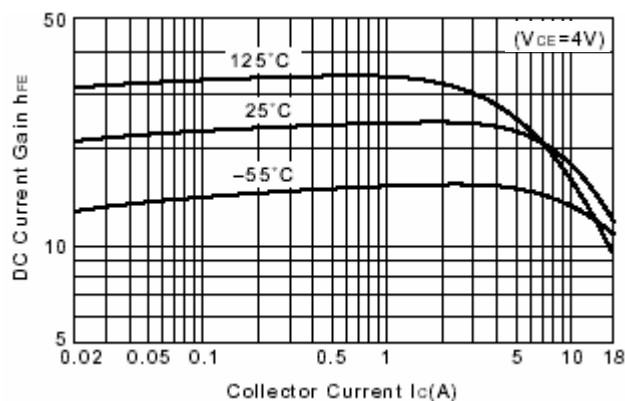
Fig.6 P_C - T_a DeratingFig.7 I_C - V_{BE} 

Fig.8 DC current Gain