

Silicon PNP Power Transistors

2SA1184

DESCRIPTION

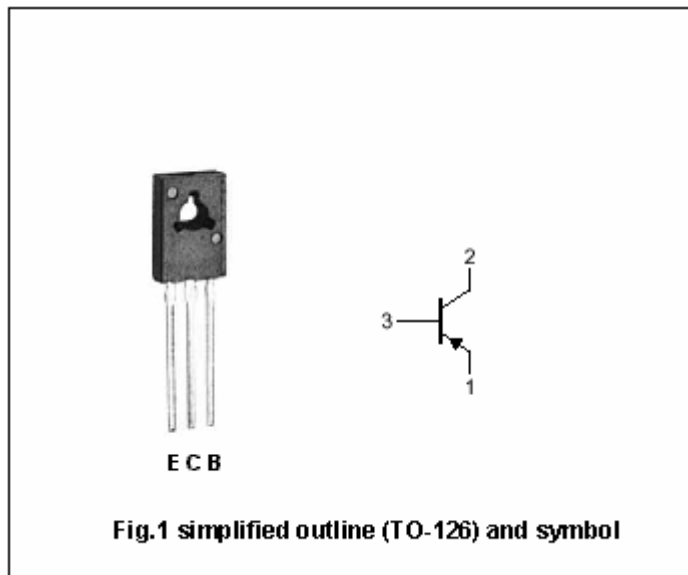
- With TO-126 package
- High breakdown voltage

APPLICATIONS

- Audio frequency power amplifier
- High frequency power amplifier

PINNING

PIN	DESCRIPTION
1	Emitter
2	Collector;connected to mounting base
3	Base



Absolute maximum ratings(Ta=25□)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	-120	V
V _{CEO}	Collector-emitter voltage	Open base	-120	V
V _{EBO}	Emitter-base voltage	Open collector	-5	V
I _C	Collector current		-1	A
I _B	Base current		-0.1	A
P _D	Total power dissipation	T _a =25□	1.5	W
		T _C =25□	15	
T _J	Junction temperature		150	□
T _{stg}	Storage temperature		-55□+150	□

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CHARACTERISTICS

T_j=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-10mA; I _B =0	-120			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =-10μA; I _C =0	-5			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-500mA; I _B =-50mA			-1.0	V
V _{BE}	Base-emitter on voltage	I _C =-500mA ; V _{CE} =-5V			-1.0	V
I _{CBO}	Collector cut-off current	V _{CB} =-120V; I _E =0			-1	μA
I _{EBO}	Emitter cut-off current	V _{EB} =-5V; I _C =0			-1	μA
h _{FE}	DC current gain	I _C =-100mA ; V _{CE} =-5V	80		240	
f _T	Transition frequency	I _C =-0.1A ; V _{CE} =5V		120		MHz

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PACKAGE OUTLINE

