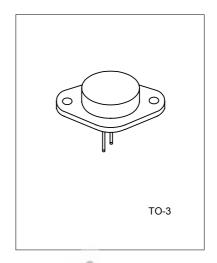
SILICON NPN TRANSISTORS

The UTC 2N3772 is a power-base power transistor in TO-3 metal case. It is designed for linear amplifiers, series pass regulators, and inductive switching applications.



ABSOLUTE MAXIMUM RATINGS (Ta=25°C, unless otherwise specified)

PARAMETERS	SYMBOL	VALUE	UNITS
Collector-Base Voltage	Vсво	100	V
Collector-Emitter Voltage	VCEO	60	V
Emitter-Base Voltage	VEBO	7	V
Collector-Emitter Voltage	VCEV	80	V
Collector Current	lc	30	Α
Collector Peak Current(1)	ICM	30	Α
Base Current	lB	5	Α
Base Peak Current(1)	Івм	15	Α
Total Dissipation at Ta=25°C	Ptot	150	W
Storage Temperature	TSTG	-65 ~ +200	°C
Max. Operating Junction Temperature	Tj	200	°C

ELECTRICAL CHARACTERISTICS(Ta=25°C, unless otherwise specified)

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT			
OFF CHARACTERISTICS									
Collector-Emitter Sustaining	VCEX(sus)	Ic=0.2 A, VBE(OFF)=1.5V,	80			V			
Voltage		RBE=100 Ohms							
Collector-Emitter Sustaining Voltage	VCER(sus)	Ic=0.2 A, RBE=100 Ohms	70			V			
Collector-Emitter Sustaining Voltage	VCEO(sus)	Ic=0.2 A, IB=0	40			V			
Collector Cut-off Current	ICEO	Vce=50V,IB=0			10	mA			
Collector Cut-off Current	ICEX	VCE=100V,VBE(off)=1.5V.			5	mA			
		VCE=30V,VBE(off)=1.5V,			10				
		Ta=150°C							
Collector Cut-off Current	Ісво	VCE=50V,IE=0			5	mA			
Emitter Cut-off Current	lево	VBE=7V,IC=0			5	mA			
ON CHARACTERISTICS		·							
DC Current Gain(note)	hFE	Ic=10A,VCE=4V,	15		60				
		Ic=20A,VcE=4V	5						

UTC UNISONIC TECHNOLOGIES CO. LTD

UTC 2N3772

SILICON NPN TRANSISTOR

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
Collector-Emitter Saturation Voltage	Vce(sat)	Ic=10A,IB=1.5A			1.4	V	
		Ic=20A,IB=4A			4.0		
Base-Emitter On Voltage	VBE(on)	Ic=10A,VcE=4V			2.2	V	
SECOND BREAKDOWN				_			
Second Breakdown Collector with	Is/b	VCE=60V,T=1.0s, Non-repetitive	2.5			Α	
Base Forward Biased							
DYNAMIC CHARACTERISTICS							
Current Gain-Bandwidth Product	fT	Ic=1A,VcE=4V,f=50kHz	0.2			MHz	
Small-Signal Current Gain	hFE	Ic=1A,VcE=4V,f=1kHz	40				

Note(1):Pulse Test: Puls Width<= $300\mu s$, Duty Cycle<=2%



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