2N3903 is a Preferred Device

General Purpose Transistors

NPN Silicon



ON Semiconductor™

http://onsemi.com

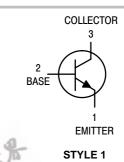
MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	VCEO	40	Vdc
Collector-Base Voltage	V _{CBO}	60	Vdc
Emitter-Base Voltage	V _{EBO}	6.0	Vdc
Collector Current – Continuous	IC	200	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	625 5.0	mW mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	1.5 12	Watts mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS (Note 1.)

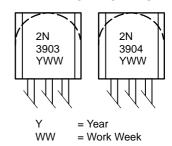
		,		
Characteristic		Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	T	R ₀ JA	200	°C/W
Thermal Resistance, Junction to Case		$R_{\theta JC}$	83.3	°C/W

1. Indicates Data in addition to JEDEC Requirements.





MARKING DIAGRAMS



ORDERING INFORMATION

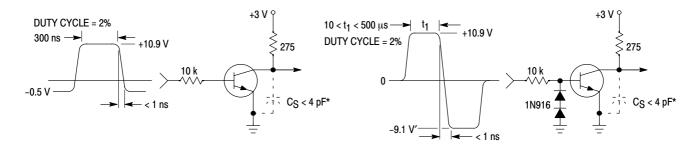
Device	Package	Shipping
2N3903	TO-92	5000 Units/Box
2N3903RLRM	TO-92	2000/Ammo Pack
2N3904	TO-92	5000 Units/Box
2N3904RLRA	TO-92	2000/Tape & Reel
2N3904RLRE	TO-92	2000/Tape & Reel
2N3904RLRM	TO-92	2000/Ammo Pack
2N3904RLRP	TO-92	2000/Ammo Pack
2N3904RL1	TO-92	2000/Tape & Reel
2N3904ZL1	TO-92	2000/Ammo Pack

Preferred devices are recommended choices for future use and best overall value.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

	Characteristic		Symbol	Min	Max	Unit
OFF CHARACTER	ISTICS					
Collector–Emitter Bre	eakdown Voltage (Note 2.) (I _C = 1.0 mAdc, I _B =	0)	V(BR)CEO	40	_	Vdc
Collector–Base Brea	kdown Voltage ($I_C = 10 \mu Adc, I_E = 0$)		V _(BR) CBO	60	_	Vdc
Emitter-Base Break	down Voltage ($I_E = 10 \mu Adc, I_C = 0$)		V _{(BR)EBO}	6.0	_	Vdc
	(V _{CE} = 30 Vdc, V _{EB} = 3.0 Vdc)		I _{BL}	_	50	nAdc
	ent (V _{CE} = 30 Vdc, V _{EB} = 3.0 Vdc)		ICEX	_	50	nAdc
ON CHARACTERIS			OLX			1
DC Current Gain (No			hee			I _
$(I_C = 0.1 \text{ mAdc}, V_C)$	•	2N3903	hFE	20	_	
(10 = 0.1 111/100, 1/	JE - 1.0 vd0)	2N3904		40	_	
$(I_C = 1.0 \text{ mAdc}, V_C)$	c = 1.0 Vdc	2N3903		35	_	
(IC = 1.0 IIIAGC, V	JE - 1.0 vac)	2N3904		70	_	
(I _C = 10 mAdc, V _C	cr = 1.0 V/dc	2N3903		50	150	
(10 = 10 111/100, 10	,E = 1.0 vd0)	2N3904		100	300	
$(I_C = 50 \text{ mAdc}, V_C)$	ar = 1.0 V/dc	2N3903		30	-	
(10 = 00 111/100, 10	,E = 1.0 vdo)	2N3904		60	_	
$(I_C = 100 \text{ mAdc}, V)$	$r_{or} = 1.0 \text{ Vdc}$	2N3903		15		
(IC = 100 IIIAdc, v	CE = 1.0 vac)	2N3904	43	30	_	
		ZN3904	其用	30	_	
Collector-Emitter Sa	turation Voltage (Note 2.)	- 7	VCE(sat)			Vdc
$(I_C = 10 \text{ mAdc}, I_B)$		A 78. 9	OL(3at)	_	0.2	
$(I_C = 50 \text{ mAdc}, I_B)$		23年		_	0.3	
		76				
	ition Voltage (Note 2.)	-01	VBE(sat)			Vdc
(IC = 10 mAdc, IB)	= 1.0 mAdc)		` '	0.65	0.85	
$(I_C = 50 \text{ mAdc}, I_B)$	= 5.0 mAdc)			_	0.95	
SMALL-SIGNAL C	CHARACTERISTICS					
Current-Gain - Band	dwidth Product		f⊤			MHz
$(I_C = 10 \text{ mAdc}, V_C)$	E = 20 Vdc, f = 100 MHz)	2N3903		250	_	
		2N3904		300	-	
Output Capacitance	(V _{CB} = 5.0 Vdc, I _E = 0, f = 1.0 MHz)		C _{obo}	_	4.0	pF
Input Capacitance (V	(EB = 0.5 Vdc, IC = 0, f = 1.0 MHz)		C _{ibo}	_	8.0	pF
Input Impedance						kΩ
	- 40 \/do f 4 0 \/ l=\	2N2002	h _{ie}	1.0	0.0	K 52
(IC = 1.0 mAdc, V)	CE = 10 Vdc, f = 1.0 kHz)	2N3903		1.0	8.0	
		2N3904		1.0	10	
Voltage Feedback Ra	atio		h _{re}			X 10 ⁻⁴
	CE = 10 Vdc, f = 1.0 kHz)	2N3903	··ie	0.1	5.0	7.10
(10 - 1.0 1111 100; 1)	JE = 10 vdo, 1 = 1.0 id iz)	2N3904		0.5	8.0	
		2110004		0.0	0.0	
Small-Signal Curren	t Gain		h _{fe}			_
$(I_C = 1.0 \text{ mAdc}, V_C)$	CE = 10 Vdc, f = 1.0 kHz)	2N3903		50	200	
, ,	,	2N3904		100	400	
Output Admittance (I	C = 1.0 mAdc, V _{CE} = 10 Vdc, f = 1.0 kHz)		h _{oe}	1.0	40	μmhos
				1.0	70	<u>'</u>
Noise Figure			NF			dB
$(I_C = 100 \mu Adc, V_C)$	$CE = 5.0 \text{ Vdc}, R_S = 1.0 \text{ k} \Omega, f = 1.0 \text{ kHz}$	2N3903		_	6.0	
		2N3904		_	5.0	
SWITCHING CHAP	RACTERISTICS					
Delay Time	(V _{CC} = 3.0 Vdc, V _{BE} = 0.5 Vdc,		t _d	_	35	ns
Rise Time	$I_C = 10 \text{ mAdc}, I_{B1} = 1.0 \text{ mAdc})$		t _r	-	35	ns
Storage Time	(V _{CC} = 3.0 Vdc, I _C = 10 mAdc,	2N3903	t.		175	ns
Glorage Tille	I _{B1} = I _{B2} = 1.0 mAdc)	2N3903 2N3904	t _S	_	200	115
Fall Time			t _f	_	50	ns
i uii iiiiii			ี	-	50	110

Pulse Test: Pulse Width ≤ 300 μs; Duty Cycle ≤ 2%.



* Total shunt capacitance of test jig and connectors

Figure 1. Delay and Rise Time Equivalent Test Circuit

Figure 2. Storage and Fall Time Equivalent Test Circuit

TYPICAL TRANSIENT CHARACTERISTICS

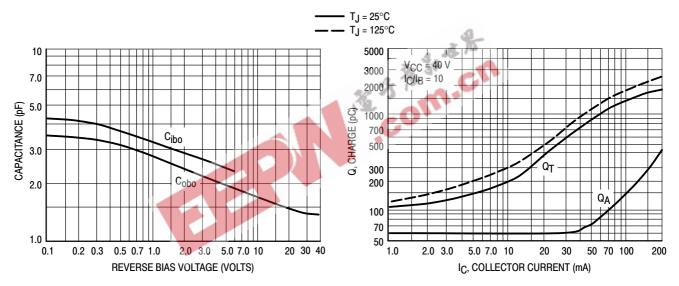
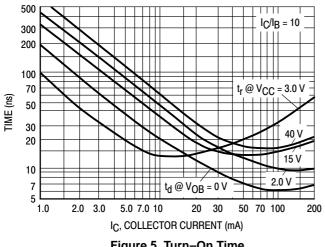


Figure 3. Capacitance

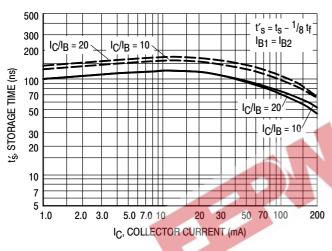
Figure 4. Charge Data



500 $V_{CC} = 40 \text{ V}$ 300 $I_{C}/I_{B} = 10$ 200 100 t_r, RISE TIME (ns) 70 50 30 20 10 1.0 2.0 3.0 5.0 7.0 10 70 100 200 IC, COLLECTOR CURRENT (mA)

Figure 5. Turn-On Time

Figure 6. Rise Time



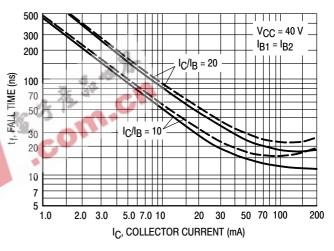
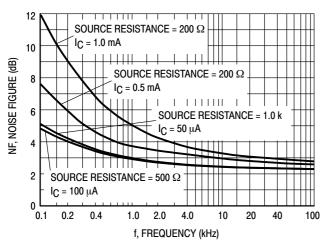


Figure 7. Storage Time

Figure 8. Fall Time

TYPICAL AUDIO SMALL-SIGNAL CHARACTERISTICS **NOISE FIGURE VARIATIONS**

 $(V_{CE} = 5.0 \text{ Vdc}, T_A = 25^{\circ}\text{C}, Bandwidth = 1.0 \text{ Hz})$



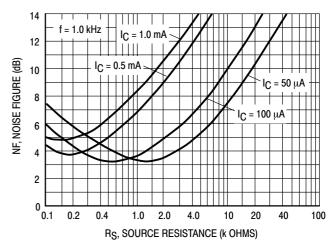


Figure 9.

Figure 10.

h PARAMETERS

(V_{CE} = 10 Vdc, f = 1.0 kHz, $T_A = 25$ °C)

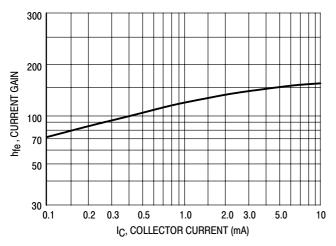
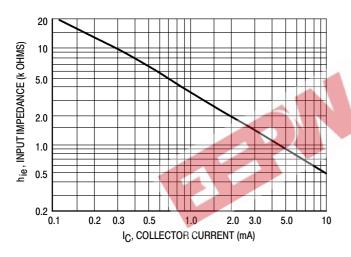


Figure 11. Current Gain

Figure 12. Output Admittance



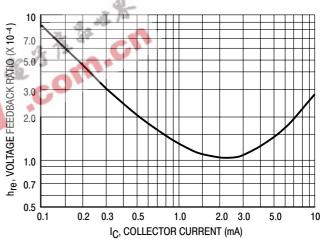


Figure 13. Input Impedance

Figure 14. Voltage Feedback Ratio

TYPICAL STATIC CHARACTERISTICS

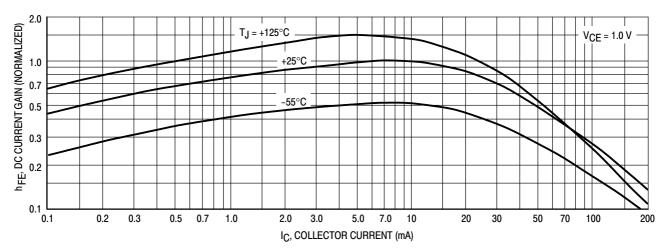


Figure 15. DC Current Gain

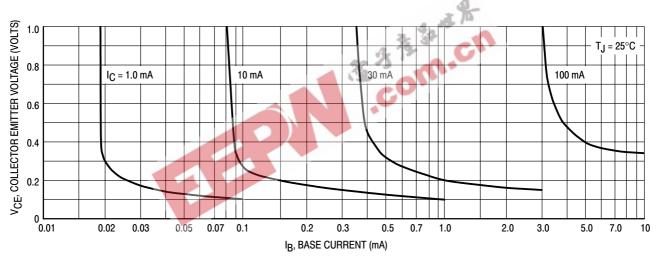


Figure 16. Collector Saturation Region

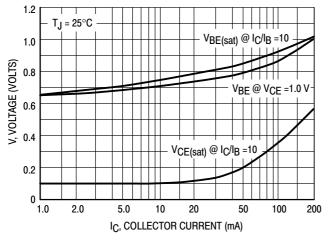


Figure 17. "ON" Voltages

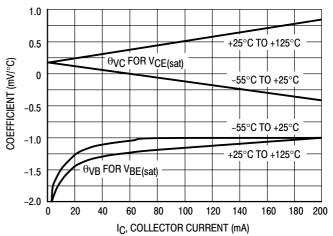
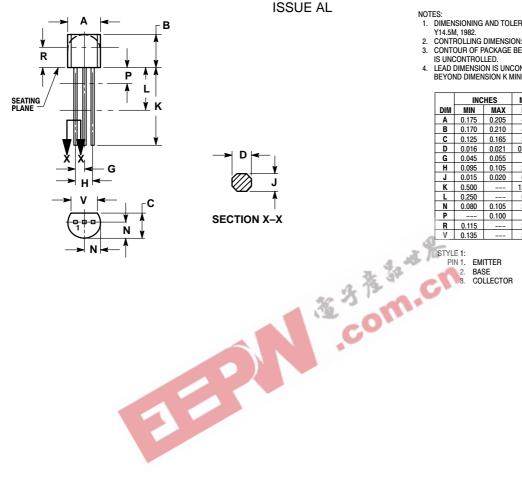


Figure 18. Temperature Coefficients

PACKAGE DIMENSIONS

TO-92 TO-226AA CASE 29-11 **ISSUE AL**





NOTES:

- NOTES:

 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

 2. CONTROLLING DIMENSION: INCH.

 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.

 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	ETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.175	0.205	4.45	5.20	
В	0.170	0.210	4.32	5.33	
C	0.125	0.165	3.18	4.19	
D	0.016	0.021	0.407	0.533	
G	0.045	0.055	1.15	1.39	
Н	0.095	0.105	2.42	2.66	
7	0.015	0.020	0.39	0.50	
K	0.500		12.70		
L	0.250		6.35		
N	0.080	0.105	2.04	2.66	
P		0.100		2.54	
R	0.115		2.93		
V	0.105		2 42		

STYLE 14:
PIN 1. EMITTER
2. COLLECTOR
3. BASE



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