DATA SHEET



SILICON POWER TRANSISTOR 2SA1652

PNP SILICON EPITAXIAL TRANSISTOR FOR HIGH-SPEED SWITCHING

The 2SA1652 is a mold power transistor developed for highspeed switching and features a very low collector-to-emitter saturation. This transistor is ideal for use in switching power supplies, DC/DC converters, motor drivers, solenoid drivers, and other low-voltage power supply devices, as well as for high-current switching.

FEATURES

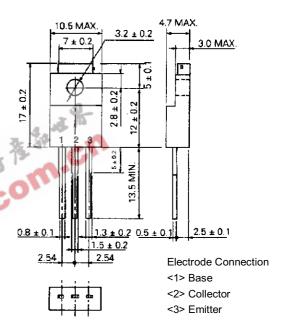
- Mold package that does not require an insulating board or insulation bushing
- · Fast switching speed
- Low collector-to-emitter saturation voltage: $V_{\text{CE(sat)}} \leq -0.3 \text{ V (MAX.) } @ \text{Ic} = -6 \text{ A}$

QUALITY GRADES

Standard

Please refer to "Quality Grades on NEC Semiconductor Devices" (Document No. C11531E) published by NEC Corporation to know the specification of quality grade on the devices and its recommended applications.

PACKAGE DRAWING (UNIT: mm)



ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Parameter	Symbol	Conditions	Ratings	Unit
Collector to base voltage	Vсво		-150	٧
Collector to emitter voltage	VCEO		-100	٧
Emitter to base voltage	V _{EBO}		-7.0	٧
Collector current	Ic(DC)		-10	Α
Collector current	IC(pulse)	PW ≤ 300 µs, duty cycle ≤ 10%	-20	Α
Base current	I _{B(DC)}		-6.0	Α
Total power dissipation	Рт	Tc = 25°C	25	W
Total power dissipation	Рт	Ta = 25°C	2.0	W
Junction temperature	Tj		150	°C
Storage temperature	T _{stg}		-55 to +150	°C

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ELECTRICAL CHARACTERISTICS (Ta = 25°C)

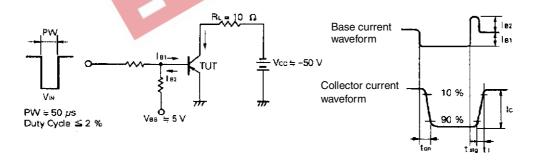
Parame	eter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit		
Collector cutoff	current	Ісво	Vcb = -100 V, IE = 0			-10	μΑ		
Emitter cutoff cu	urrent	ІЕВО	V _{EB} = -5 V, I _C = 0			-10	μΑ		
DC current gain	1	h _{FE1} *	Vce = -2 V, Ic = -0.5 A	100			-		
DC current gain	1	h _{FE2} *	Vce = -2 V, Ic = -2 A	100		400	_		
DC current gain	1	h _{FE3} *	Vce = -2 V, Ic = -6 A	60			_		
Collector satura	tion voltage	V _{CE(sat)1} *	$I_C = -6 \text{ A}, I_B = -0.3 \text{ A}$			-0.3	٧		
Collector satura	tion voltage	VCE(sat)2*	Ic = -8 A, IB = -0.4 A			-0.5	V		
Base saturation	voltage	V _{BE(sat)1} *	$I_C = -6 \text{ A}, I_B = -0.3 \text{ A}$			-1.2	V		
Base saturation	voltage	V _{BE(sat)2} *	Ic = -8 A, $IB = -0.4 A$			-1.5	V		
Gain bandwidth	product	f⊤	Vce = -10 V, Ic = -0.5 A		150		MHz		
Collector capaci	itance	Cob	Vcb = -10 V, IE = 0, f = 1 MHz		250		pF		
Turn-on time ton		ton	$I_C = -6 \text{ A}, I_{B1} = -I_{B2} = -0.3 \text{ A},$		0.3		μs		
Storage time		tstg	R _L = 8.3 Ω , Vcc = -50 V Refer to the test circuit.	.0	1.5		μs		
Fall time		tf	nelel to the test circuit.	。這所	0.4		μs		
Fall time t_i Refer to the test circuit. μ_s * Pulse test PW \leq 350 μ_s , duty cycle \leq 2% Marking M L K									
Marking	М		L						
h _{FE2}	100 to 200) 1	150 to 300 200 to 400						

^{*} Pulse test PW \leq 350 μ s, duty cycle \leq 2%

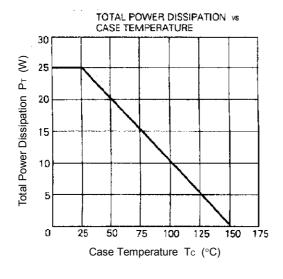
hfe CLASSIFICATION

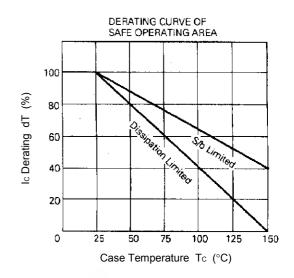
Marking	M	L	K	
h _{FE2}	100 to 200	150 to 300	200 to 400	

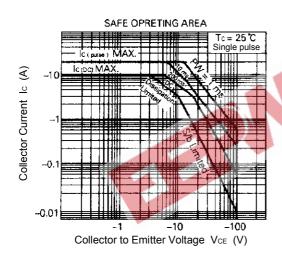
SWITCHING TIME TEST CIRCUIT

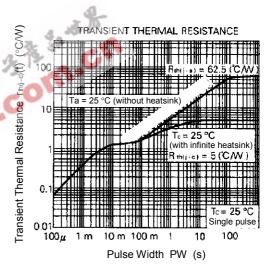


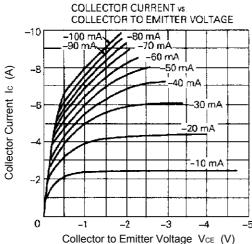
TYPICAL CHARACTERISTICS (Ta = 25°C)

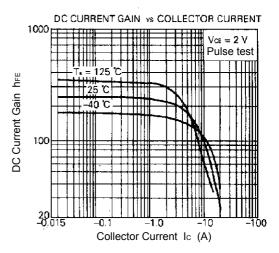






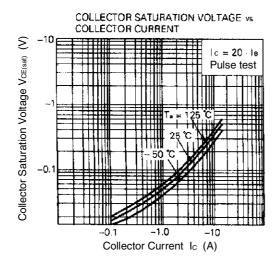


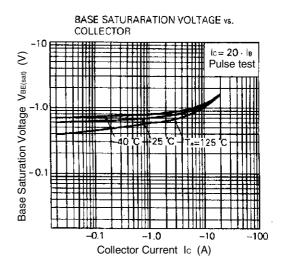


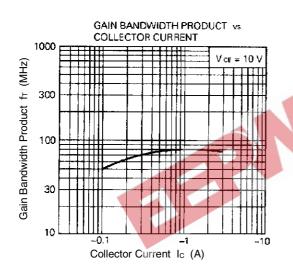


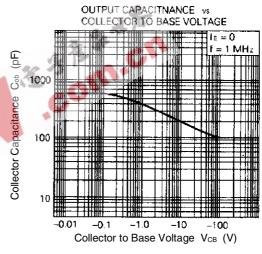
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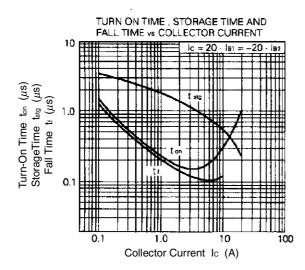
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