DATA SHEET

NEC

SILICON TRANSISTOR
2SA1714

PNP SILICON EPITAXIAL POWER TRANSISTOR (DARLINGTON CONNECTION) FOR HIGH-SPEED SWITCHING

The 2SA1714 is a high-speed darlington power transistor. This transistor is ideal for high-precision control such as PWM control for pulse mortors or blushless mortor of OA and FA equipment.

FEATURES

- High DC current amplifiers due to darlington connection
- Large current capacitance and low $V_{CE(sat)}$
- TO-126 power transistor with high power dissipation
- Complementary transistor with 2SC4342

QUALITY GRADES

- Standard

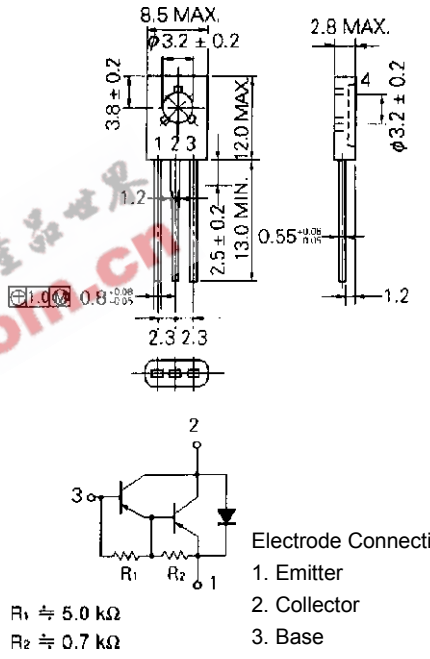
  Please refer to "Quality Grades on NEC Semiconductor Devices" (Document No. C11531E) published by NEC Corporation to know the specification of quality grade on the devices and its recommended applications.

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Parameter	Symbol	Ratings	Unit
Collector to base voltage	V_{CBO}	−100	V
Collector to emitter voltage	V_{CEO}	−100	V
Emitter to base voltage	V_{EBO}	−8.0	V
Collector current (DC)	$I_{C(DC)}$	∓3.0	A
Collector current (pulse)	$I_{C(pulse)}$*	∓6.0	A
Base current (DC)	$I_{B(DC)}$	−0.3	A
Total power dissipation	P_T (Ta = 25°C)	1.3	W
Total power dissipation	P_T (Tc = 25°C)	12	W
Junction temperature	T_j	150	°C
Storage temperature	T_{stg}	−55 to +150	°C

* PW ≤ 10 ms, duty cycle ≤ 50%

PACKAGE DRAWING (UNIT: mm)



Electrode Connection
1. Emitter
2. Collector
3. Base
4. Fin (collector)

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ELECTRICAL CHARACTERISTICS (Ta = 25°C)

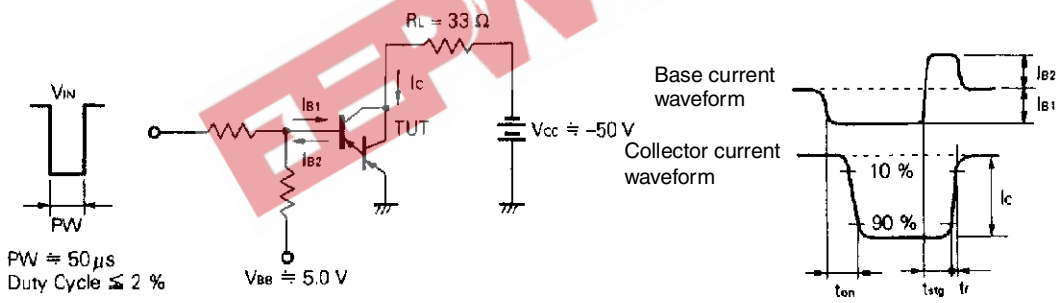
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Collector to emitter voltage	$V_{CEO(SUS)}$	I_C = –3.0 A, I_B = –3.0 mA, L = 1.0 mH	–100			V
Collector cutoff current	I_{CBO}	V_{CB} = –100 V, I_E = 0			–10	μA
Collector cutoff current	I_{CEO}	V_{CE} = –100 V, R_{BE} = ∞			–10	μA
DC current gain	h_{FE1}**	V_{CE} = –2.0 V, I_C = –1.5 A	2,000		20,000	–
DC current gain	h_{FE2}**	V_{CE} = –2.0 V, I_C = –3.0 A	1,000			–
Collector saturation voltage	$V_{CE(sat)}$**	I_C = –1.5 A, I_B = –1.5 mA		–0.9	–1.2	V
Base saturation voltage	$V_{BE(sat)}$**	I_C = –1.5 A, I_B = –1.5 mA		–1.5	–2.0	V
Turn-on time	t_{on}	I_C = –1.5 A, I_{B1} = –I_{B2} = –1.5 mA, R_L = 33 Ω, V_{CC} ≅ –50 V Refer to the test circuit.		0.15		μs
Storage time	t_{stg}			1.2		μs
Fall time	t_f			0.6		μs

** Pulse test PW ≤ 350 μs, duty cycle ≤ 2%/pulsed

h_{FE} CLASSIFICATION

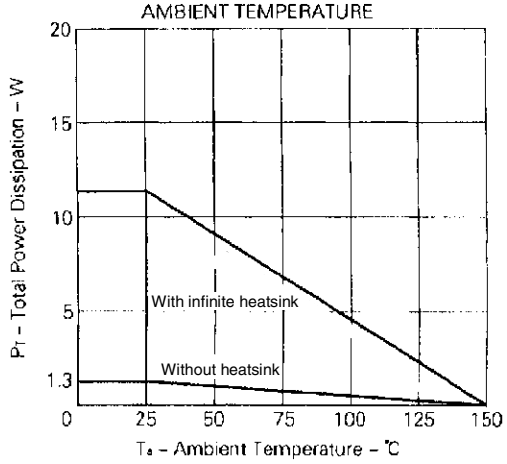
Marking	M	L	K
h_{FE1}	2,000 to 5,000	4,000 to 10,000	8,000 to 20,000

SWITCHING TIME (t_{on}, t_{stg}, t_f) TEST CIRCUIT

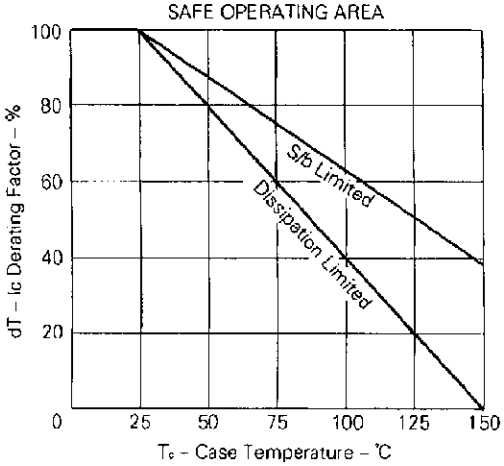


TYPICAL CHARACTERISTICS (Ta = 25°C)

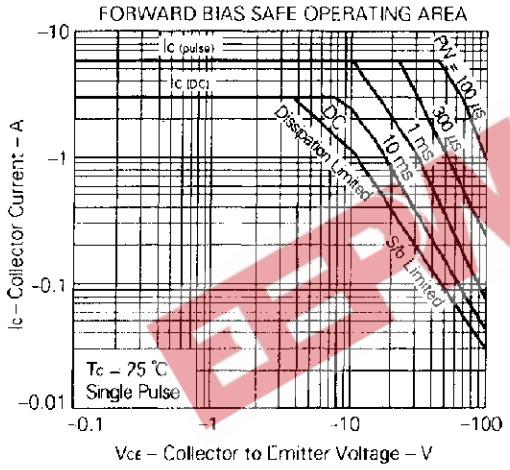
TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE



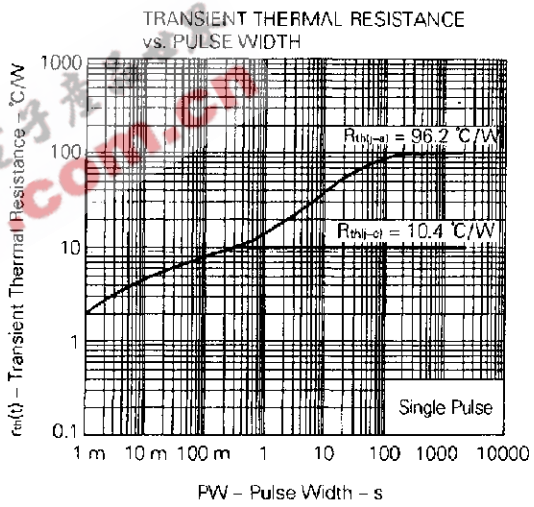
DERATING CURVE OF SAFE OPERATING AREA



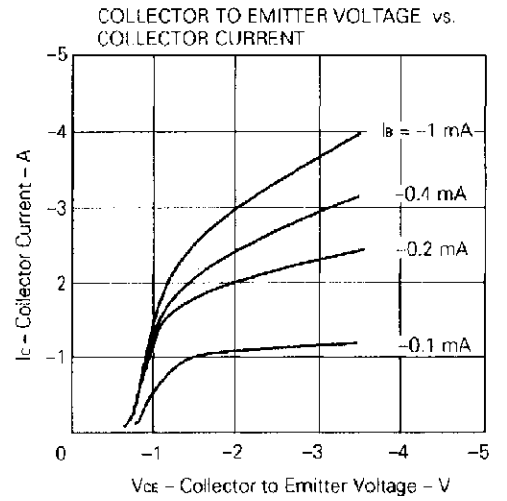
FORWARD BIAS SAFE OPERATING AREA



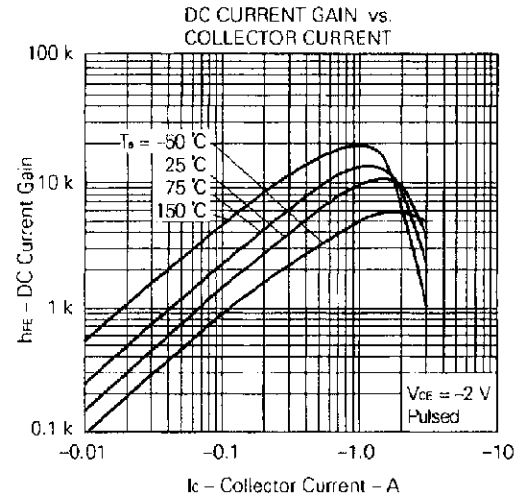
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



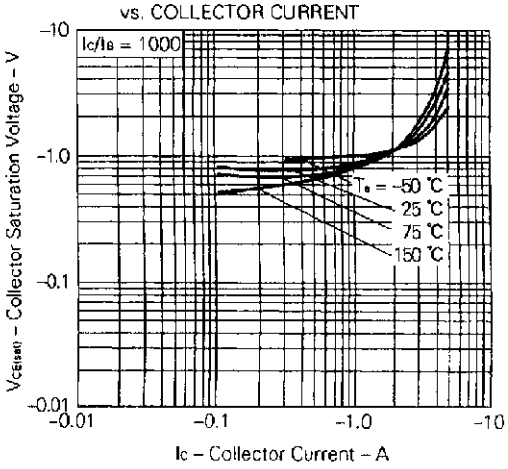
COLLECTOR TO EMITTER VOLTAGE vs. COLLECTOR CURRENT



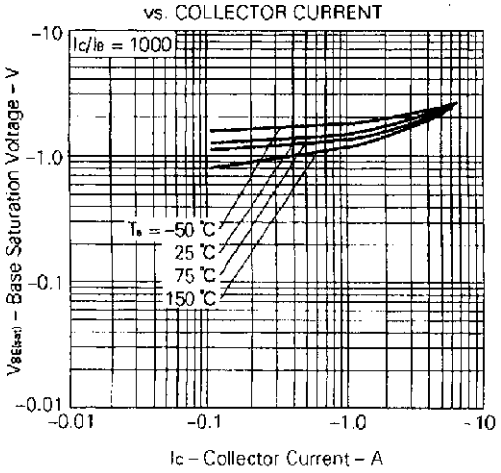
DC CURRENT GAIN vs. COLLECTOR CURRENT



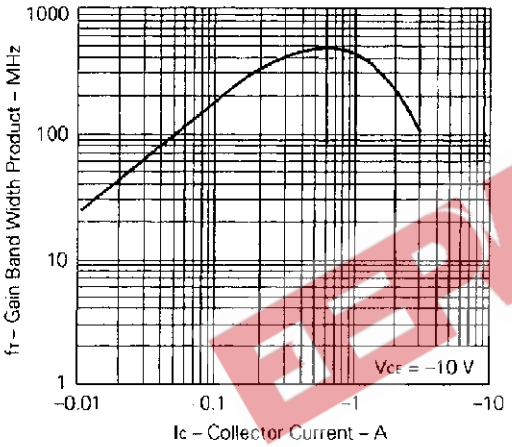
COLLECTOR SATURATION VOLTAGE
vs. COLLECTOR CURRENT



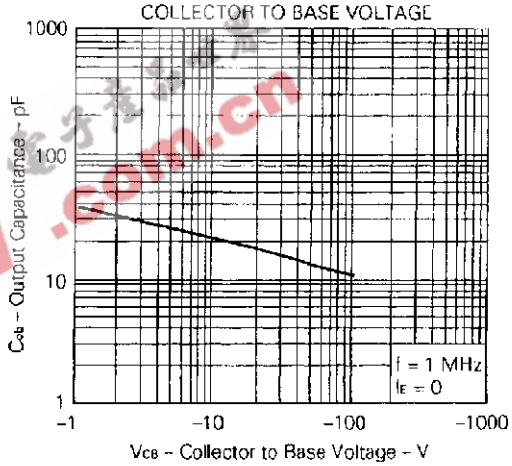
BASE SATURATION VOLTAGE
vs. COLLECTOR CURRENT



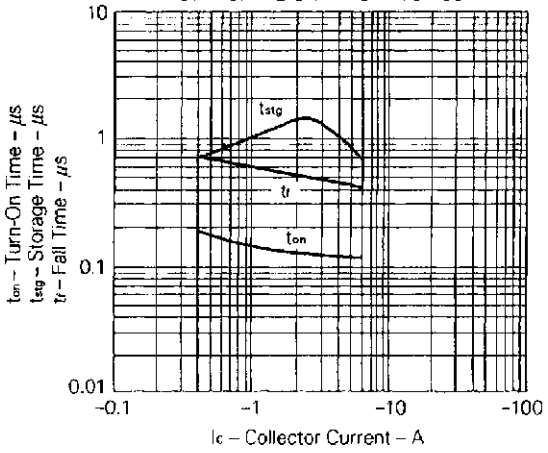
GAIN BAND WIDTH PRODUCT
vs. COLLECTOR CURRENT



OUTPUT CAPACITANCE vs.
COLLECTOR TO BASE VOLTAGE



SWITCHING CHARACTERISTICS



[MEMO]

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