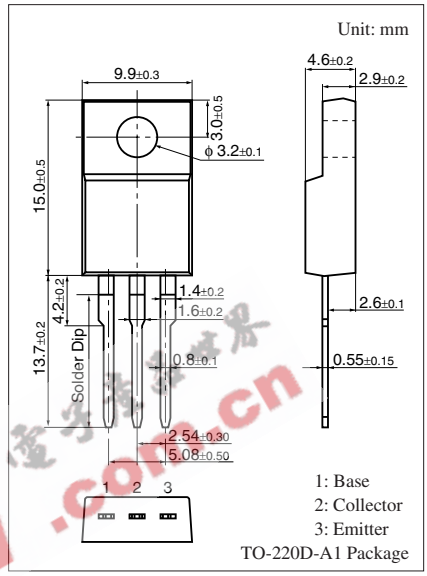
2SA2118

Silicon PNP epitaxial planar type

For power amplification
For TV vertical deflection output



■ Features

- Satisfactory linearity of forward current transfer ratio h_{FE}
- Dielectric breakdown voltage of the package: 5 kV
- Full-pack package which can be installed to the heat sink with one screw.

■ Absolute Maximum Ratings $T_C = 25°C$

Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	V_{CBO}	−200	V
Collector-emitter voltage (Base open)	V_{CEO}	−180	V
Emitter-base voltage (Collector open)	V_{EBO}	−6	V
Collector current	I_C	−2	A
Peak collector current	I_{CP}	−3	A
Collector power dissipation	P_C	25	W
($T_a = 25°C$)		2.0	
Junction temperature	T_j	150	°C
Storage temperature	T_{stg}	−55 to +150	°C

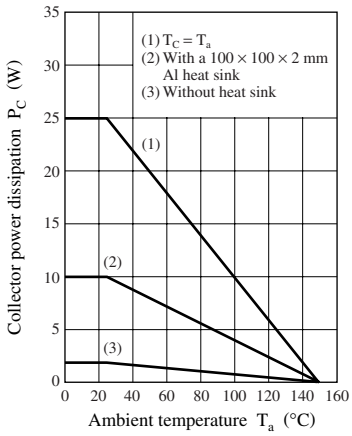
■ Electrical Characteristics $T_C = 25°C \pm 3°C$

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Collector-base voltage (Emitter open)	V_{CBO}	$I_C = -50$ µA, $I_E = 0$	−200			V
Collector-emitter voltage (Base open)	V_{CEO}	$I_C = -5$ mA, $I_B = 0$	−180			V
Emitter-base voltage (Collector open)	V_{EBO}	$I_E = -500$ µA, $I_C = 0$	−6			V
Base-emitter voltage	V_{BE}	$V_{CE} = -10$ V, $I_C = -400$ mA			−1	V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{CB} = -200$ V, $I_E = 0$			−50	µA
Emitter-base cutoff current (Collector open)	I_{EBO}	$V_{EB} = -4$ V, $I_C = 0$			−50	µA
Forward current transfer ratio	h_{FE1} *	$V_{CE} = -10$ V, $I_C = -150$ mA	60		240	—
	h_{FE2}	$V_{CE} = -10$ V, $I_C = -400$ mA	50			
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C = -500$ mA, $I_B = -50$ mA			−1	V
Transition frequency	f_T	$V_{CE} = -10$ V, $I_C = -0.5$ A, f = 10 MHz		30		MHz

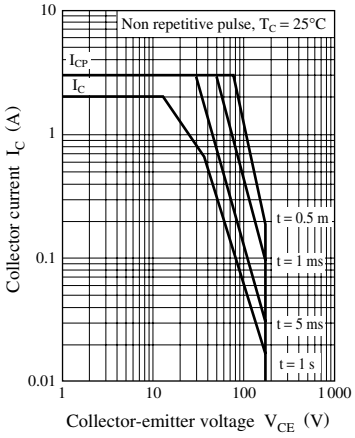
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.
2. *: Rank classification

Rank	Q	P
h_{FE1}	60 to 140	100 to 240

P_C — T_a



Safe operation area



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