Unit: mm

TOSHIBA Transistor Silicon PNP Epitaxial Type

2SA2069

High-Speed Switching Applications DC-DC Converter Applications

- High DC current gain: $h_{FE} = 200$ to 500 ($I_{C} = -0.15$ A)
- Low collector-emitter saturation voltage: $V_{CE (sat)} = -0.14 \text{ V (max)}$
- High-speed switching: $t_f = 37 \text{ ns (typ.)}$

Maximum Ratings (Ta = 25°C)

| Characteristics | | Symbol | Rating | Unit | |
|-----------------------------|----------|------------------|------------|------|--|
| Collector-base voltage | | V_{CBO} | -20 | V | |
| Collector-emitter voltage | | V _{CEO} | -20 | V | |
| Emitter-base voltage | | V _{EBO} | -7 | V | |
| Collector current | DC | Ic | -1.5 | Α | |
| | Pulse | I _{CP} | -2.5 | | |
| Base current | | I _B | -150 | mA | |
| Collector power dissipation | t = 10 s | PC | 2.0 | W | |
| | DC | (Note 1) | 1.0 | | |
| Junction temperature | | Tj | 150 | °C | |
| Storage temperature range | | T _{stg} | -55 to 150 | °C | |

Note 1: Mounted on FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm²)

1.7MAX. 0.4±0.05 0.45-0.05 0.4-0.05 1.5±0.1 1.5±0.1 1.8ase 2. Collector (heat sink) 3. Emitter JEDEC — JEITA SC-62 TOSHIBA 2-5K1A

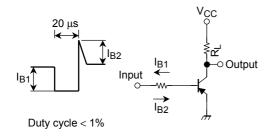
Weight: 0.05 g (typ.)

Electrical Characteristics (Ta = 25°C)

| 66 | | | | | | | |
|--------------------------------------|--|--|--|---|---|---|--|
| Characteristics | | Test Condition | Min | Тур. | Max | Unit | |
| Collector cut-off current | | V _{CB} = -20 V, I _E = 0 | _ | _ | -100 | nA | |
| Emitter cut-off current | | V _{EB} = -7 V, I _C = 0 | _ | _ | -100 | nA | |
| Collector-emitter breakdown voltage | | I _C = −10 mA, I _B = 0 | -20 | _ | _ | V | |
| DC current gain | | V _{CE} = -2 V, I _C = -0.15 A | 200 | _ | 500 | | |
| | | V _{CE} = -2 V, I _C = -0.5 A | 125 | _ | _ | | |
| Collector-emitter saturation voltage | | I _C = -0.5 A, I _B = -17 mA | _ | _ | -0.14 | V | |
| Base-emitter saturation voltage | | I _C = -0.5 A, I _B = -17 mA | _ | _ | -1.10 | V | |
| Collector output capacitance | | V _{CB} = −10 V, I _E = 0, f = 1 MHz | _ | 12 | _ | pF | |
| time | t _r | See Figure 1 circuit diagram. | _ | 40 | _ | ns | |
| age time | t _{stg} | V _{CC} ≈ −10 V, R _L = 20 Ω | _ | 135 | _ | | |
| time | t _f | -I _{B1} = I _{B2} = −17 mA | _ | 37 | _ | | |
| | n voltage Itage ce time age time | $\begin{array}{c} h_{FE} (1) \\ h_{FE} (2) \\ h_{FE} (3) $ | $\begin{array}{cccccccccccccccccccccccccccccccccccc$ | $\begin{array}{c ccccccccccccccccccccccccccccccccccc$ | $\begin{array}{c ccccccccccccccccccccccccccccccccccc$ | $ I_{EBO} V_{EB} = -7 \text{ V, } I_{C} = 0 $ — — — — — — — — — — — — — — — — — — | |

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Marking



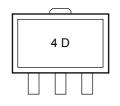
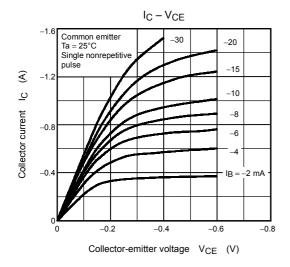
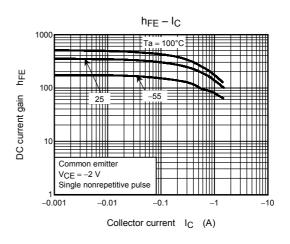
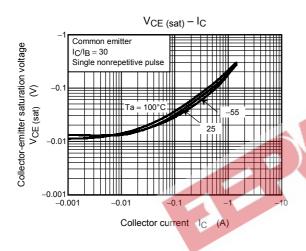


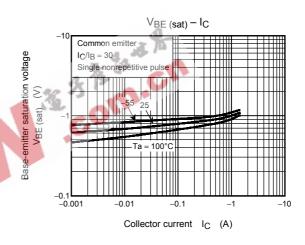
Figure 1 Switching Time Test Circuit & Timing Chart

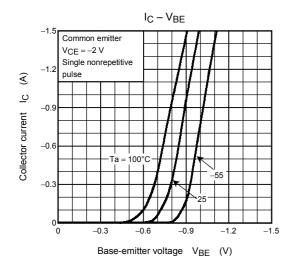




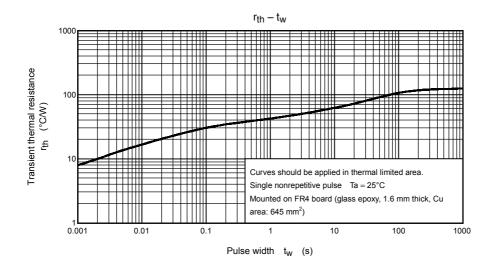


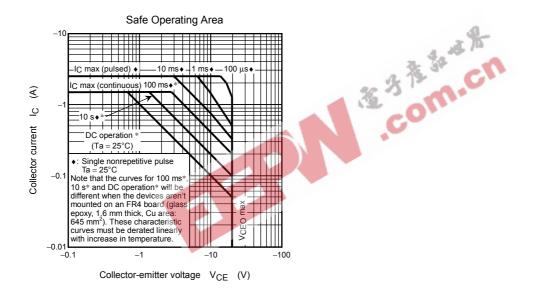






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