

DESCRIPTION

2SC2133 is a silicon NPN epitaxial planar type transistor designed for RF power amplifiers in UHF band 24 to 28 volts operation applications.

FEATURES

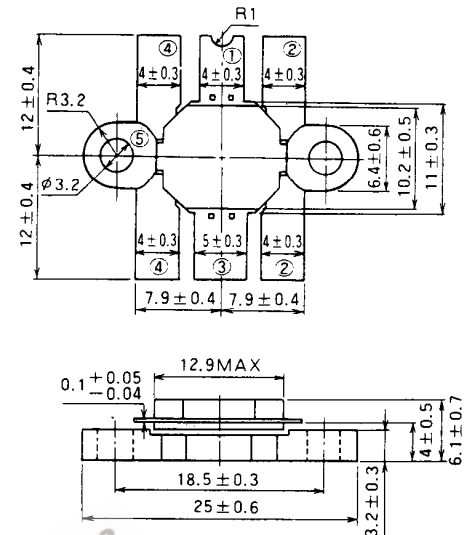
- High power gain: $G_{pe} \geq 8.2\text{dB}$
@ $V_{CC} = 28\text{V}$, $P_O = 30\text{W}$, $f = 220\text{MHz}$
- Emitter ballasted construction and gold metallization for high reliability and good performances.
- Low thermal resistance ceramic package with flange.
- Ability of withstanding more than 20:1 load VSWR when operated at $V_{CC} = 28\text{V}$, $P_O = 30\text{W}$, $f = 220\text{MHz}$, $T_C = 25^\circ\text{C}$.
- Equivalent input series impedance: $Z_{in} = 1.2 + j3.3\Omega$
@ $V_{CC} = 28\text{V}$, $P_O = 30\text{W}$, $f = 220\text{MHz}$

APPLICATION

10 to 15 watts output linear power amplifiers such as TV transposer amplifiers in VHF band.

OUTLINE DRAWING

Dimensions in mm



PIN :

- ① COLLECTOR
- ② EMITTER (FLANGE)
- ③ BASE
- ④ EMITTER (FLANGE)
- ⑤ FIN (EMITTER)

T-40E

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Ratings	Unit
V_{CB0}	Collector to base voltage		55	V
V_{EB0}	Emitter to base voltage		4	V
V_{CE0}	Collector to emitter voltage	$R_{BE} = \infty$	35	V
I_C	Collector current		5	A
P_C	Collector dissipation	$T_a = 25^\circ\text{C}$	3.75	W
		$T_C = 25^\circ\text{C}$	75	W
T_j	Junction temperature		175	$^\circ\text{C}$
T_{stg}	Storage temperature		-55 to 175	$^\circ\text{C}$
R_{th-a}	Thermal resistance	Junction to ambient	40	$^\circ\text{C/W}$
R_{th-c}		Junction to case	2	$^\circ\text{C/W}$

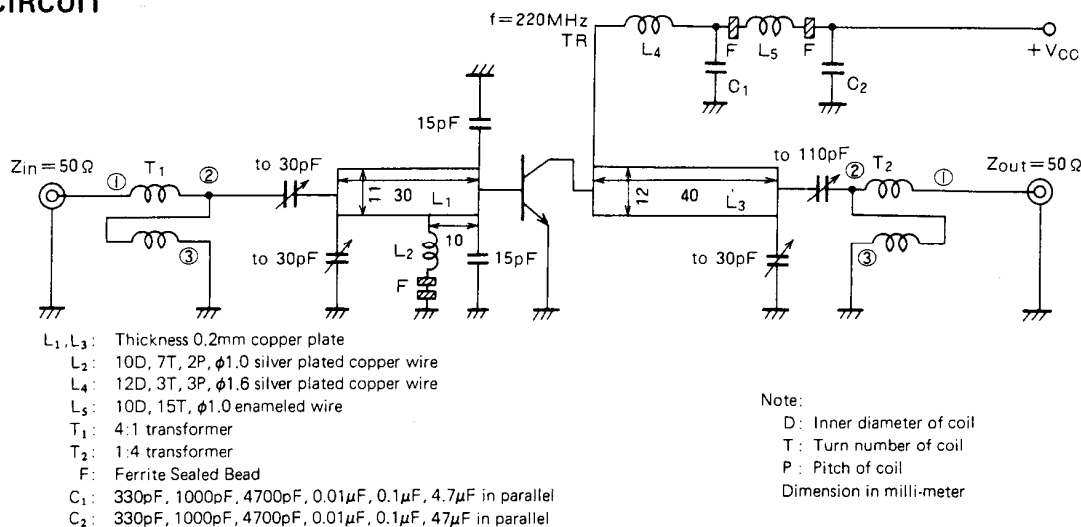
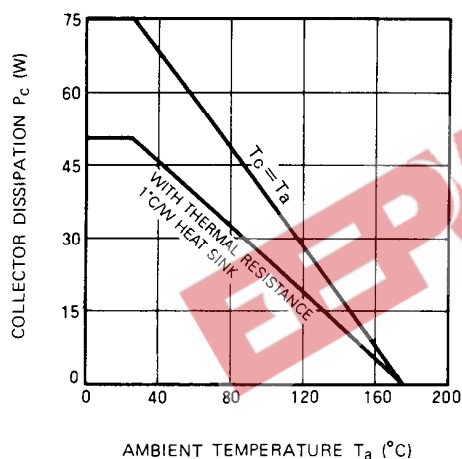
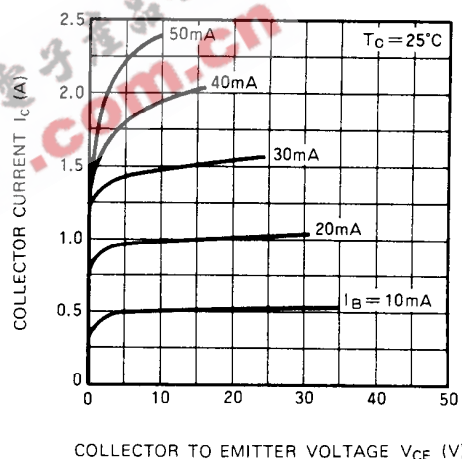
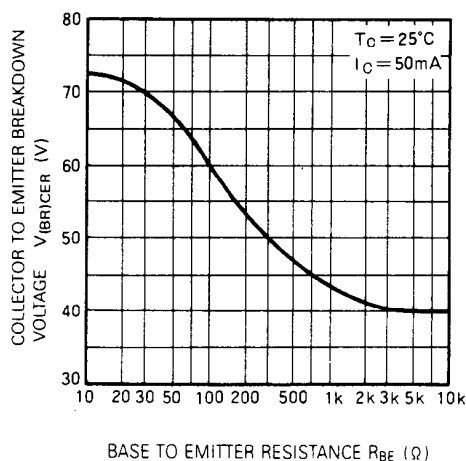
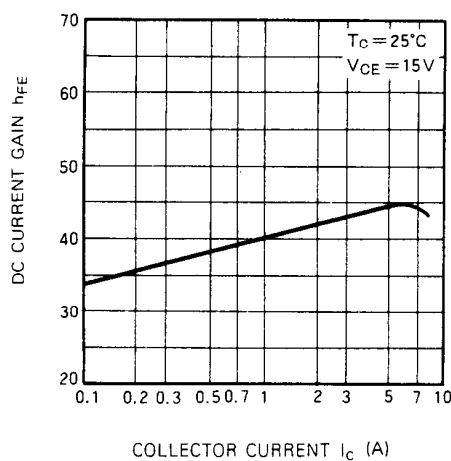
Note. Above parameters are guaranteed independently.

ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	
$V_{(BR)EB0}$	Emitter to base breakdown voltage	$I_E = 10\text{mA}$, $I_C = 0$	4			V
$V_{(BR)CB0}$	Collector to base breakdown voltage	$I_C = 10\text{mA}$, $I_E = 0$	55			V
$V_{(BR)CE0}$	Collector to emitter breakdown voltage	$I_C = 50\text{mA}$, $R_{BE} = \infty$	35			V
I_{CB0}	Collector cutoff current	$V_{CB} = 35\text{V}$, $I_E = 0$			2	mA
I_{EB0}	Emitter cutoff current	$V_{EB} = 3\text{V}$, $I_C = 0$			1	mA
h_{FE}	DC forward current gain *	$V_{CE} = 25\text{V}$, $I_C = 0.2\text{A}$	20	50	110	—
P_O	Output power	$V_{CC} = 28\text{V}$, $P_{in} = 4.5\text{W}$, $f = 220\text{MHz}$	30	34		W
η_C	Collector efficiency		55	60		%

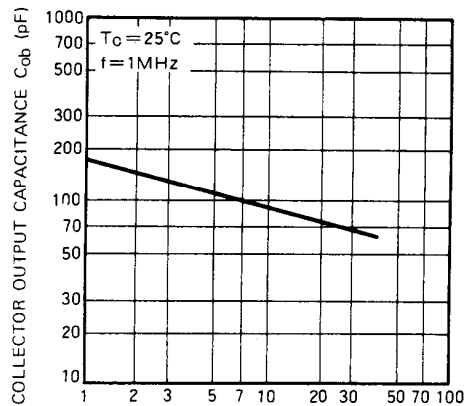
Note. * Pulse test, $P_W = 150\mu\text{s}$, duty=5%.

Above parameters, ratings, limits and conditions are subject to change.

NPN EPITAXIAL PLANAR TYPE**TEST CIRCUIT****TYPICAL PERFORMANCE DATA****COLLECTOR DISSIPATION VS. AMBIENT TEMPERATURE****COLLECTOR CURRENT VS. COLLECTOR TO EMITTER VOLTAGE****COLLECTOR TO EMITTER BREAKDOWN VOLTAGE VS. BASE TO EMITTER RESISTANCE****DC CURRENT GAIN VS. COLLECTOR CURRENT**

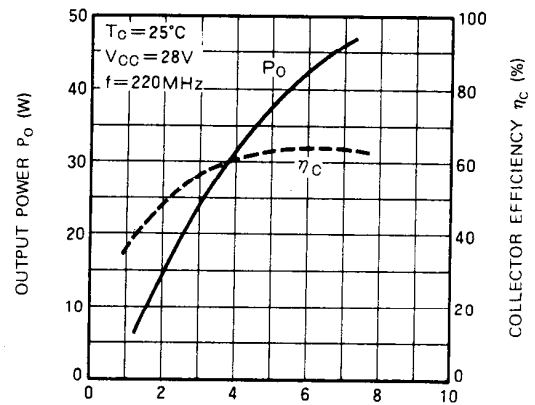
NPN EPITAXIAL PLANAR TYPE

COLLECTOR OUTPUT CAPACITANCE VS. COLLECTOR TO BASE VOLTAGE



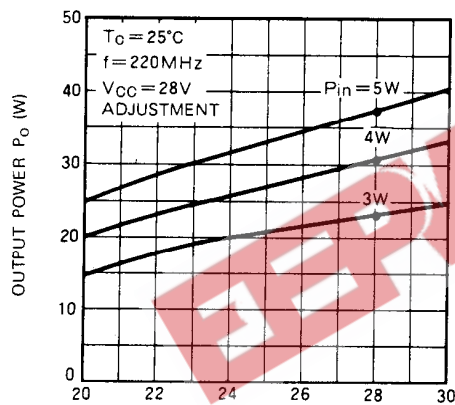
COLLECTOR TO BASE VOLTAGE V_{CB} (V)

OUTPUT POWER, COLLECTOR EFFICIENCY VS. INPUT POWER



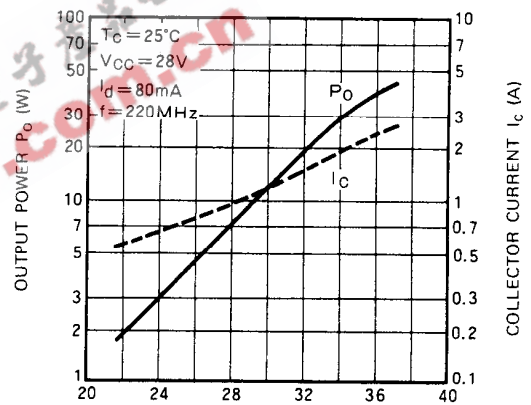
INPUT POWER P_{in} (W)

OUTPUT POWER VS. COLLECTOR SUPPLY VOLTAGE



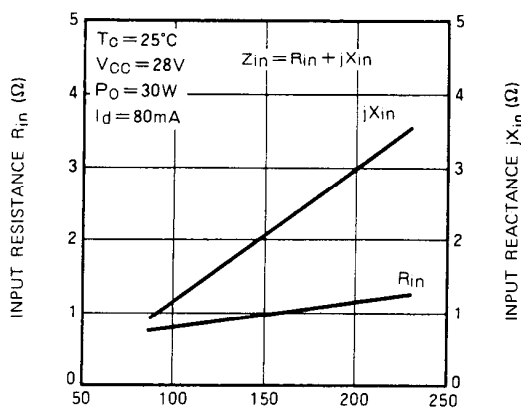
COLLECTOR SUPPLY VOLTAGE V_{CC} (V)

IN CASE AB OPERATING OUTPUT POWER COLLECTOR CURRENT VS. INPUT POWER



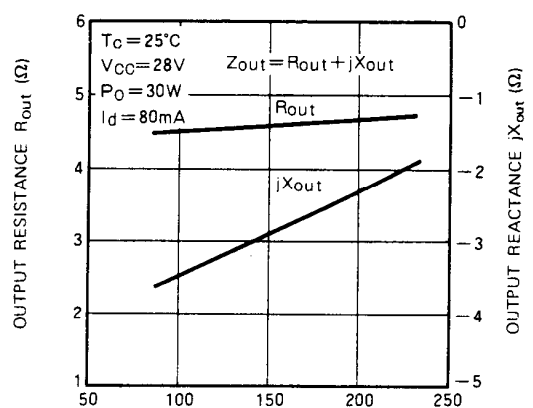
INPUT POWER P_{in} (dBm)

INPUT IMPEDANCE VS. FREQUENCY



FREQUENCY f (MHz)

OUTPUT IMPEDANCE VS. FREQUENCY



FREQUENCY f (MHz)