

TOSHIBA TRANSISTOR SILICON NPN EPITAXIAL PLANAR TYPE

2SC2290A

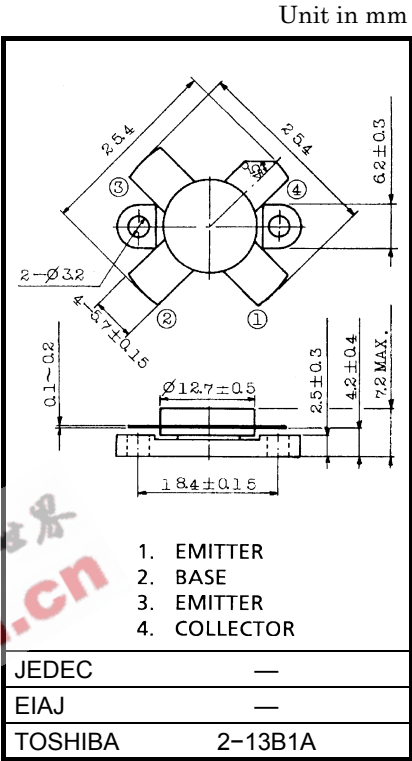
2~30MHz SSB LINEAR POWER AMPLIFIER APPLICATIONS
(LOW SUPPLY VOLTAGE USE)

- Specified 12.5V, 28MHz Characteristics
- Output Power : $P_o = 60W_{PEP}$ (Min.)
- Power Gain : $G_p = 11.8dB$ (Min.)
- Collector Efficiency : $\eta_C = 35\%$ (Min.)
- Intermodulation Distortion: $IMD = -30dB$ (Max.)

ABSOLUTE MAXIMUM RATINGS (Tc = 25°C)

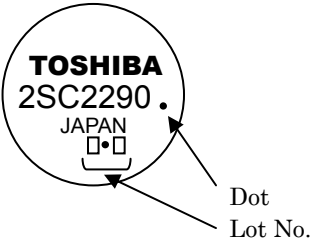
CHARACTERISTIC	SYMBOL	RATING	UNIT
Collector-Base Voltage	V_{CBO}	45	V
Collector-Emitter Voltage	V_{CES}	45	V
Collector-Emitter Voltage	V_{CEO}	18	V
Emitter-Base Voltage	V_{EBO}	4	V
Collector Current	I_C	20	A
Collector Power Dissipation	P_C	175	W
Junction Temperature	T_j	175	°C
Storage Temperature Range	T_{stg}	-65~175	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Weight: 5.2g

MARKING



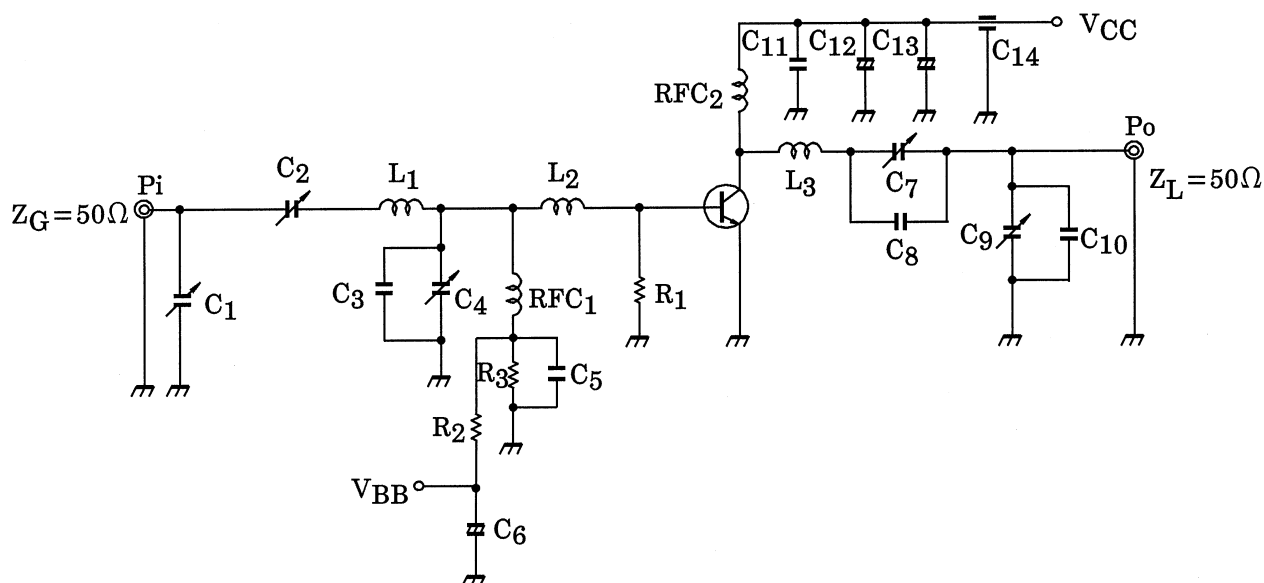
ELECTRICAL CHARACTERISTICS ($T_c = 25^\circ\text{C}$)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Collector-Emitter Breakdown Voltage	$V_{(BR) CEO}$	$I_C = 100\text{mA}$, $I_B = 0$	18	—	—	V
Collector-Emitter Breakdown Voltage	$V_{(BR) CES}$	$I_C = 100\text{mA}$, $V_{EB} = 0$	45	—	—	V
Emitter-Base Breakdown Voltage	$V_{(BR) EBO}$	$I_E = 1\text{mA}$, $I_C = 0$	4	—	—	V
DC Current Gain	h_{FE}	$V_{CE} = 5\text{V}$, $I_C = 10\text{A}^*$	10	—	150	—
Collector Output Capacitance	C_{ob}	$V_{CB} = 12.5\text{V}$, $I_E = 0$ $f = 1\text{MHz}$	—	—	500	pF
Power Gain	G_p	$V_{CC} = 12.5\text{V}$, $f_1 = 28.000\text{MHz}$, $f_2 = 28.001\text{MHz}$ $I_{idle} = 50\text{mA}$ $P_o = 60\text{W}_{PEP}$ (Fig.)	11.8	13.8	—	dB
Input Power	P_i		—	2.5	4	W_{PEP}
Collector Efficiency	η_C		35	—	—	%
Intermodulation Distortion	IMD		—	—	-30	dB
Series Equivalent Input Impedance	Z_{in}	$V_{CC} = 12.5\text{V}$, $f_1 = 28.000\text{MHz}$, $f_2 = 28.001\text{MHz}$ $P_o = 60\text{W}_{PEP}$	—	1.02 -j0.17	—	Ω
Series Equivalent Output Impedance	Z_{out}		—	0.86 -j0.21	—	Ω

* Pulse Test: Pulse Width $\leq 100\mu\text{s}$, Duty Cycle $\leq 3\%$

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Fig. Pi TEST CIRCUIT



C₁, C₂, C₄, C₇ : 7~150pF

C₃ : 250pF

C₅ : 0.4μF

C₆ : 100μF 10WV

C₈ : 150pF

C₉ : 10~200pF

C₁₀ : 600pF

C₁₁ : 0.4μF

C₁₂, C₁₃ : 22μF 35WV

C₁₄ : 1000pF

(FEED THROUGH)

L₁ : φ0.8 ENAMEL COATED COPPER WIRE, 9ID, 6T

L₂ : φ1 SILVER PLATED COPPER WIRE, 9ID, 2T

L₃ : φ1.5 ENAMEL COATED COPPER WIRE, 9ID, 5T

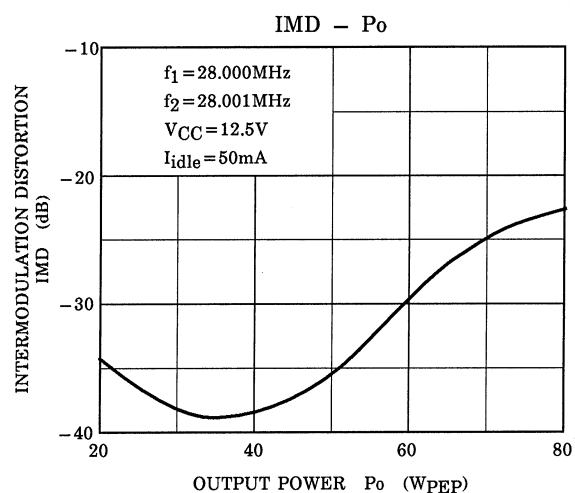
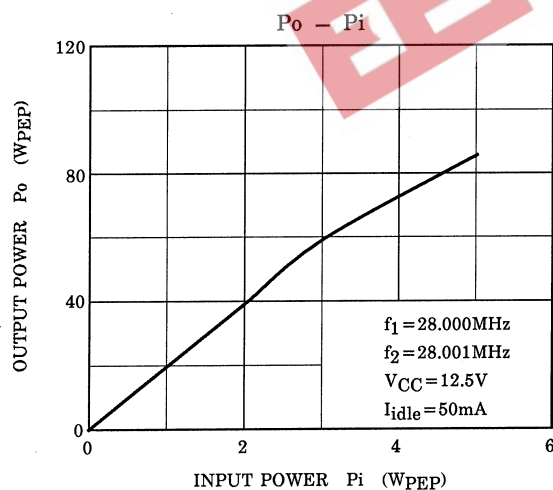
RFC₁ : φ0.8 ENAMEL COATED COPPER WIRE, 9ID, 20T

RFC₂ : φ1.5 ENAMEL COATED COPPER WIRE, 12ID, 15T

R₁ : 5.6Ω (1/2W)

R₂ : 5Ω (5W)

R₃ : 1.5Ω (10W)



CAUTION

These are only typical curves and devices are not necessarily guaranteed at these curves.

RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

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