

No.3810

2SJ225

**SANYO**

P-Channel MOS Silicon FET

Very High-Speed Switching Applications

**Features**

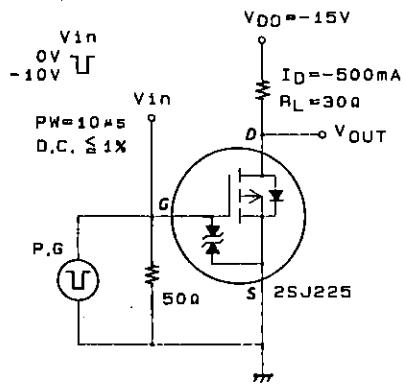
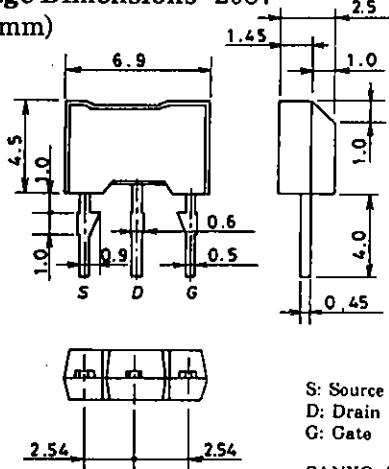
- Small ON resistance.
- Very high-speed switching.
- Low-voltage drive.
- Meets radial taping.

**Absolute Maximum Ratings at Ta = 25°C**

		unit
Drain to Source Voltage	V <sub>DSS</sub>	-30 V
Gate to Source Voltage	V <sub>GSS</sub>	±15 V
Drain Current (DC)	I <sub>D</sub>	-1 A
Drain Current (Pulse)	I <sub>DP</sub>	PW ≤ 10 μs, duty cycle ≤ 1% -4 A
Allowable Power Dissipation	P <sub>D</sub>	1 W
Channel Temperature	T <sub>ch</sub>	150 °C
Storage Temperature	T <sub>stg</sub>	-55 to +150 °C

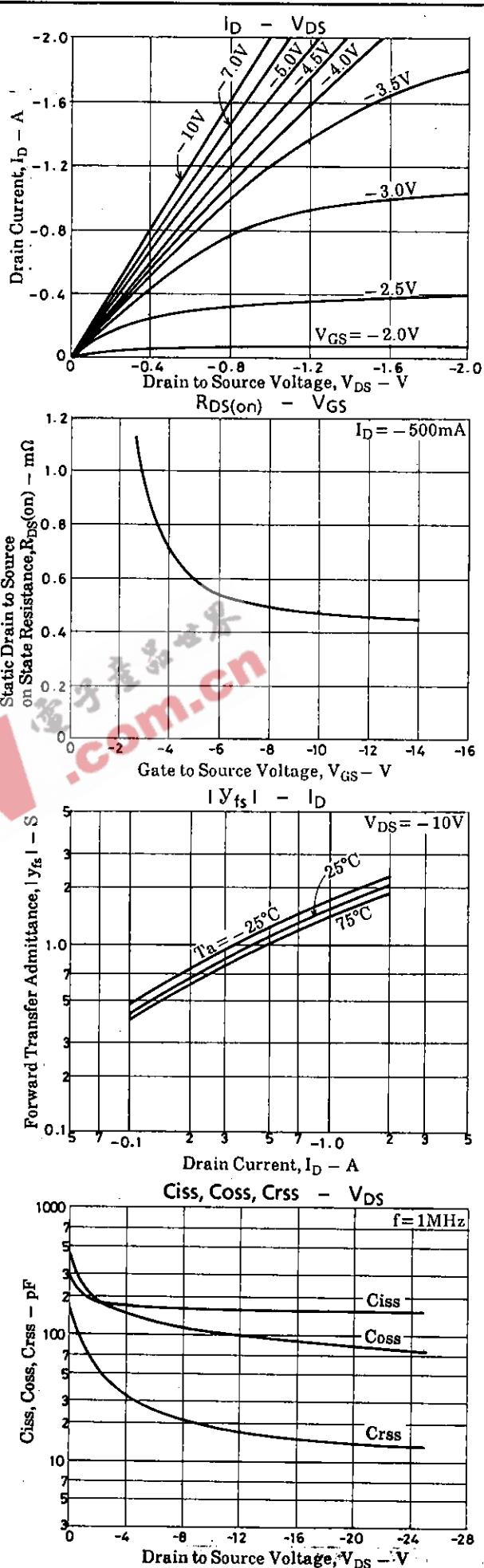
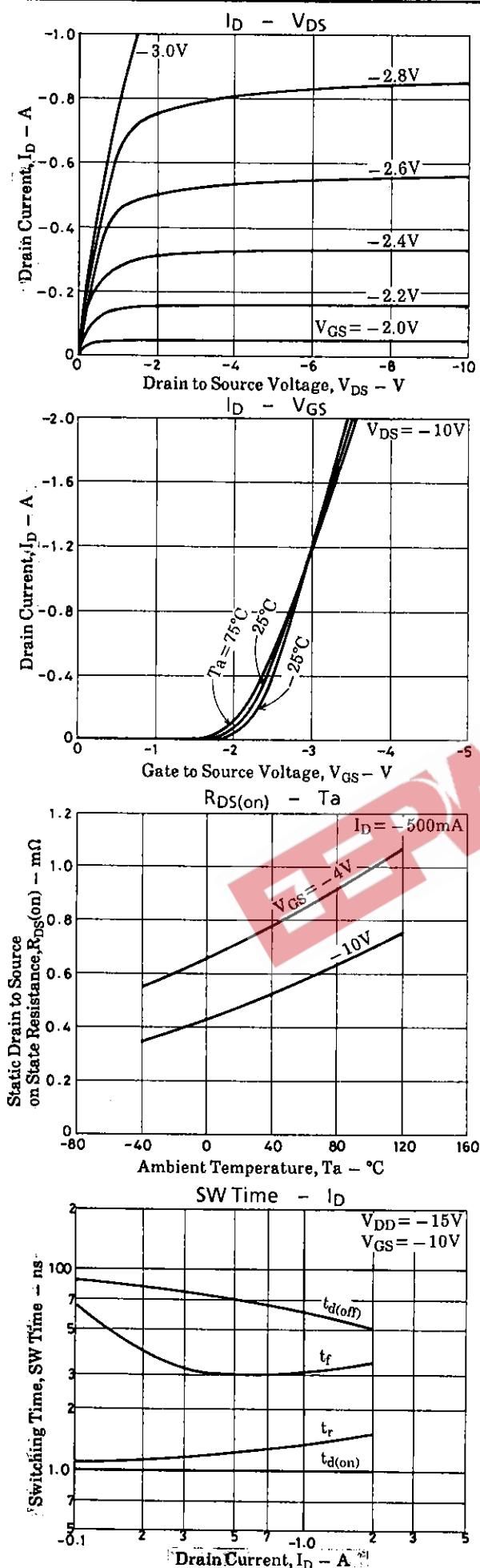
**Electrical Characteristics at Ta = 25°C**

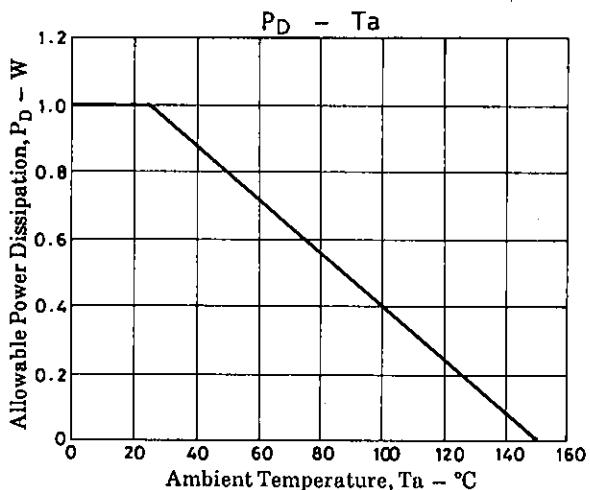
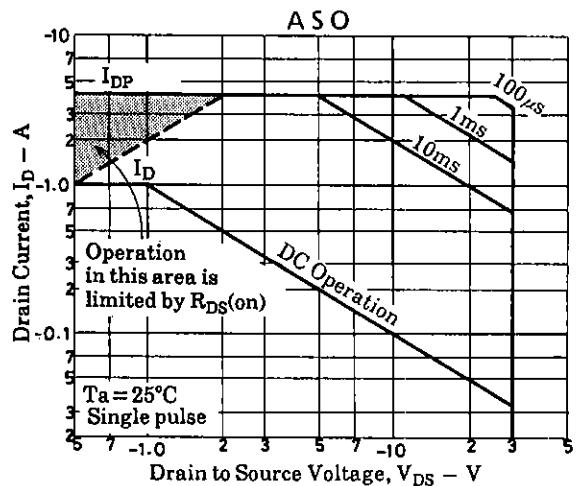
		min	typ	max	unit
D-S Breakdown Voltage	V <sub>(BR)DSS</sub>	-30			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	-100			μA
Gate to Source Leakage Current	I <sub>GSS</sub>	±10			μA
Cutoff Voltage	V <sub>GS(off)</sub>	-10V, I <sub>D</sub> = -1mA	-2.0		V
Forward Transfer Admittance	Y <sub>fs</sub>	0.6	1.0		S
Static Drain to Source on State Resistance	R <sub>DS(on)</sub>	0.5	0.75		Ω
Input Capacitance	C <sub>iss</sub>	170			pF
Output Capacitance	C <sub>oss</sub>	110			pF
Reverse Transfer Capacitance	C <sub>rss</sub>	20			pF
Turn-ON Delay Time	t <sub>d(on)</sub>	See specified Test Circuit.	10		ns
Rise Time	t <sub>r</sub>	"	13		ns
Turn-OFF Delay Time	t <sub>d(off)</sub>	"	70		ns
Fall Time	t <sub>f</sub>	"	30		ns
Diode Forward Voltage	V <sub>SD</sub>	-0.9			V
I <sub>S</sub> = -1A, V <sub>GS</sub> = 0.					

**Switching Time Test Circuit****Package Dimensions 2087  
(unit : mm)**

S: Source  
D: Drain  
G: Gate  
SANYO: NMP

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