

# 2SJ294

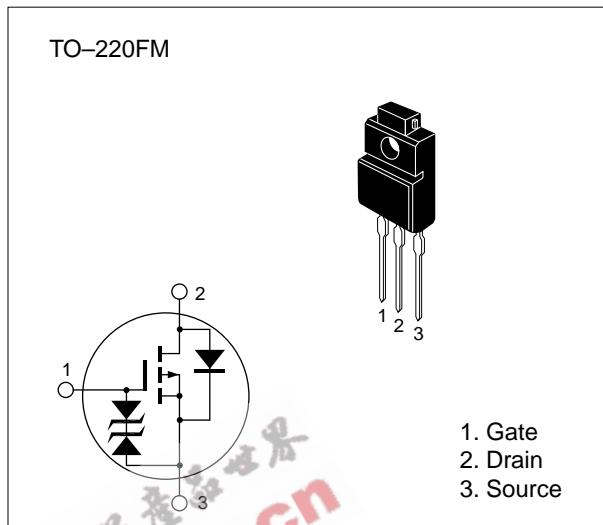
## Silicon P Channel MOS FET

### Application

High speed power switching

### Features

- Low on-resistance
- High speed switching
- Low drive current
- 4 V gate drive device can be driven from 5 V source
- Suitable for Switching regulator, DC – DC converter
- Avalanche Ratings



**Table 1 Absolute Maximum Ratings (Ta = 25°C)**

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	-60	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	-20	A
Drain peak current	I <sub>D(pulse)*</sub>	-80	A
Body-drain diode reverse drain current	I <sub>DR</sub>	-20	A
Avalanche current	I <sub>AP***</sub>	-20	A
Avalanche energy	E <sub>AR***</sub>	34	mJ
Channel dissipation	P <sub>ch**</sub>	35	W
Channel temperature	T <sub>ch</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C

\* PW ≤ 10 µs, duty cycle ≤ 1 %

\*\* Value at T<sub>c</sub> = 25 °C

\*\*\* Value at T<sub>ch</sub> = 25 °C, R<sub>g</sub> ≥ 50 Ω

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**Table 2 Electrical Characteristics (Ta = 25°C)**

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	-60	—	—	V	I <sub>D</sub> = -10 mA, V <sub>GS</sub> = 0
Gate to source breakdown voltage	V <sub>(BR)GSS</sub>	±20	—	—	V	I <sub>G</sub> = ±100 µA, V <sub>DS</sub> = 0
Gate to source leak current	I <sub>GSS</sub>	—	—	±10	µA	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0
Zero gate voltage drain current	I <sub>DSS</sub>	—	—	-250	µA	V <sub>DS</sub> = -50 V, V <sub>GS</sub> = 0
Gate to source cutoff voltage	V <sub>GS(off)</sub>	-1.0	—	-2.25	V	I <sub>D</sub> = -1 mA, V <sub>DS</sub> = -10 V
Static drain to source on state resistance	R <sub>DS(on)</sub>	—	0.05	0.065	Ω	I <sub>D</sub> = -10 A V <sub>GS</sub> = -10 V *
		—	0.07	0.095	Ω	I <sub>D</sub> = -10 A V <sub>GS</sub> = -4 V *
Forward transfer admittance	y <sub>fsl</sub>	10	16	—	S	I <sub>D</sub> = -10 A V <sub>DS</sub> = -10 V *
Input capacitance	C <sub>iss</sub>	—	2200	—	pF	V <sub>DS</sub> = -10 V
Output capacitance	C <sub>oss</sub>	—	1000	—	pF	V <sub>GS</sub> = 0
Reverse transfer capacitance	C <sub>rss</sub>	—	300	—	pF	f = 1 MHz
Turn-on delay time	t <sub>d(on)</sub>	—	25	—	ns	I <sub>D</sub> = -10 A
Rise time	t <sub>r</sub>	—	130	—	ns	V <sub>GS</sub> = -10 V
Turn-off delay time	t <sub>d(off)</sub>	—	320	—	ns	R <sub>L</sub> = 3 Ω
Fall time	t <sub>f</sub>	—	210	—	ns	
Body-drain diode forward voltage	V <sub>DF</sub>	—	-1.1	—	V	I <sub>F</sub> = -20 A, V <sub>GS</sub> = 0
Body-drain diode reverse recovery time	t <sub>rr</sub>	—	160	—	ns	I <sub>F</sub> = -20 A, V <sub>GS</sub> = 0, diF / dt = 50 A / µs

\* Pulse Test

See characteristic curves of 2SJ291

