

# 2SJ317

# Silicon P Channel MOS FET

REJ03G0857-0200

(Previous: ADE-208-1191)

Rev.2.00 Sep 07, 2005

### **Description**

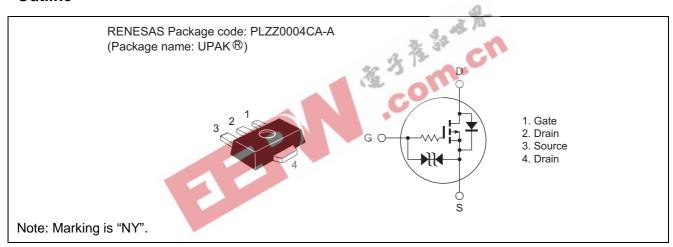
High speed power switching

Low voltage operation

#### **Features**

- Very low on-resistance
- High speed switching
- Suitable for camera or VTR motor drive circuit, power switch, solenoid drive and etc.

#### **Outline**



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# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	-12	V
Gate to source voltage	V <sub>GSS</sub>	-7	V
Drain current	I <sub>D</sub>	±2	A
Drain peak current	I <sub>D (pulse)</sub> Note 1	±4	А
Body to drain diode reverse drain current	I <sub>DR</sub>	2	А
Channel dissipation	Pch Note 2	1	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  100  $\mu$ s, duty cycle  $\leq$  10%

2. Value on the alumina ceramic board (12.5  $\times$  20  $\times$  0.7 mm)

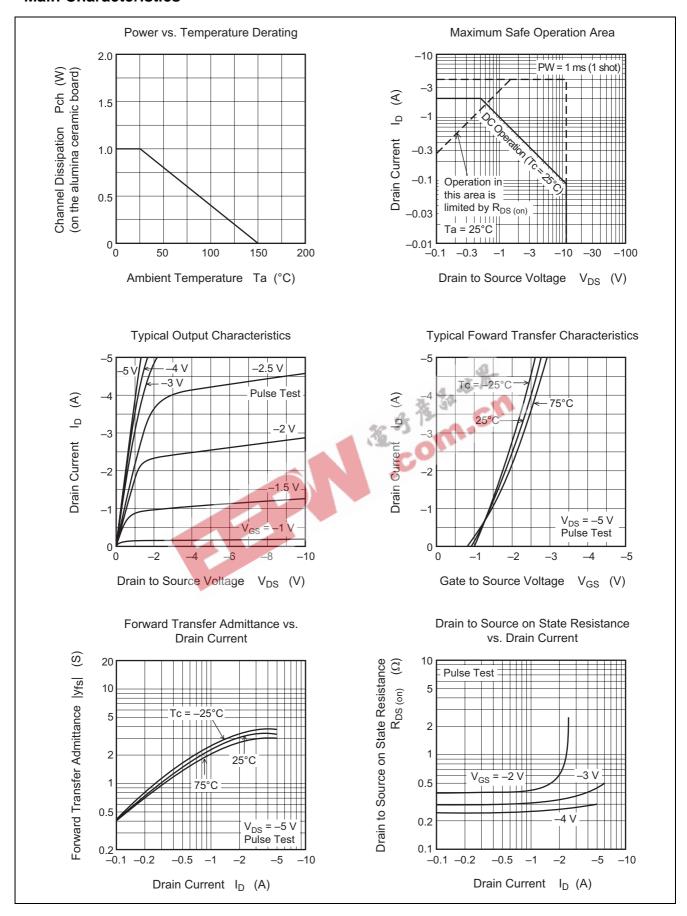
### **Electrical Characteristics**

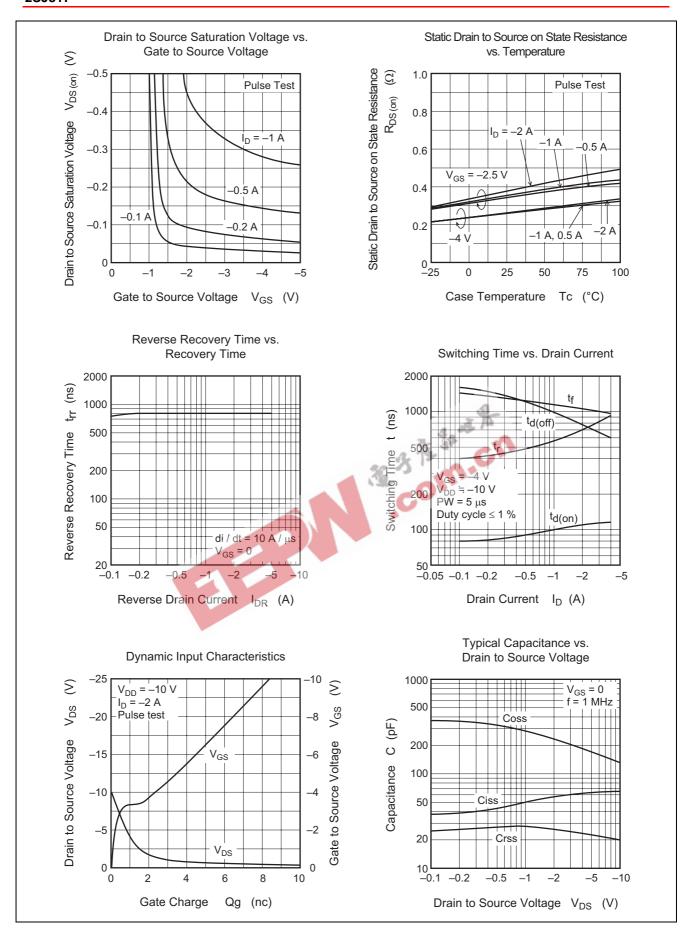
 $(Ta = 25^{\circ}C)$ 

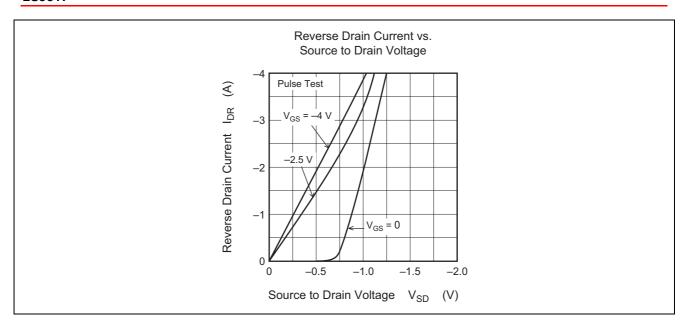
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR) DSS</sub>	-12	_	_	V	$I_D = -1 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V <sub>(BR) GSS</sub>	±7	_	_	V	$I_G = \pm 10 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>		—	±5	μΑ	$V_{GS} = \pm 6.5 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>			-1	μΑ	$V_{DS} = -8 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS (off)</sub>	-0.4	_	-1.4	V	$I_D = -100 \ \mu\text{A}, \ V_{DS} = -5 \ \text{V}$
Static drain to source on state resistance	R <sub>DS (on) 1</sub>		0.4	0.7	Ω	$I_D = -0.5 \text{ A}, V_{GS} = -2.2 \text{ V}^{\text{Note 3}}$
	R <sub>DS (on) 2</sub>	_	0.28	0.35	Ω	$I_D = -1 A$ , $V_{GS} = -4 V^{Note 3}$
Forward transfer admittance	y <sub>fs</sub>	1.0	2.3	O2.	S	$I_D = -1 A, V_{DS} = -5 V^{\text{Note 3}}$
Input capacitance	Ciss	+	63	_	pF	$V_{DS} = -5 V$
Output capacitance	Coss	Į	180	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	1	23	_	pF	f = 1 MHz
Turn-on delay time	t <sub>d (on)</sub>		500	_	ns	$I_D = -0.2 \text{ A}$
Turn-off delay time	t <sub>d (off)</sub>	_	2860	_	ns	Vin = –4 V, $R_L$ = 51 $\Omega$

Note: 3. Pulse test

#### **Main Characteristics**

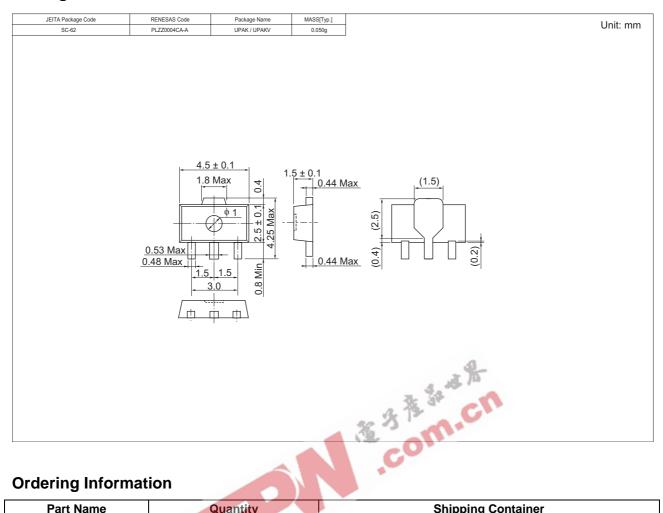








# **Package Dimensions**



### **Ordering Information**

Part Name	Quantity	Shipping Container
2SJ317NYTL-E	1000 pcs	Taping
2SJ317NYTR-E	1000 pcs	Taping

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