

No.4236

**2SJ266**

P-Channel MOS Silicon FET

Very High-Speed  
Switching Applications**SANYO****Features**

- Low ON resistance.
- Very high-speed switching.
- Low-voltage drive.
- Surface mount type device making the following possible.
  - Reduction in the number of manufacturing processes for 2SJ266-applied equipment.
  - High density surface mount applications.
  - Small size of 2SJ266-applied equipment.

**Absolute Maximum Ratings at Ta = 25°C**

			unit
Drain to Source Voltage	V <sub>DSS</sub>	- 60	V
Gate to Source Voltage	V <sub>GSS</sub>	± 15	V
Drain Current(DC)	I <sub>D</sub>	- 8	A
Drain Current(Pulse)	I <sub>DP</sub>	PW ≤ 10μs, duty cycle ≤ 1% - 32	A
Allowable Power Dissipation	P <sub>D</sub>	1.65	W
Channel Temperature	T <sub>ch</sub>	50	W
Storage Temperature	T <sub>stg</sub>	150	°C
		- 55 to + 150	°C

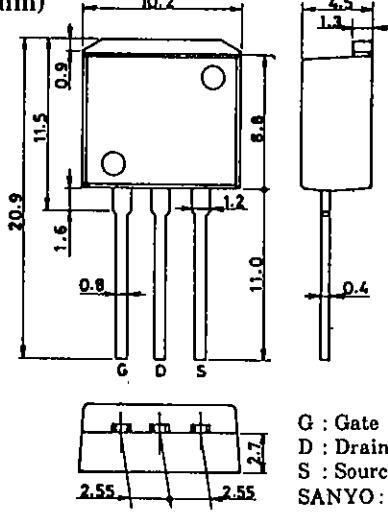
**Electrical Characteristics at Ta = 25°C**

			min	typ	max	unit
D-S Breakdown Voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = - 1mA, V <sub>GS</sub> = 0	- 60			V
G-S Breakdown Voltage	V <sub>(BR)GSS</sub>	I <sub>G</sub> = ± 100μA, V <sub>DS</sub> = 0	± 15			V
Zero Gate Voltage	I <sub>DSS</sub>	V <sub>DS</sub> = - 60V, V <sub>GS</sub> = 0			- 100	μA
Drain Current						
Gate to Source Leakage Current	I <sub>CSS</sub>	V <sub>GS</sub> = ± 12V, V <sub>DS</sub> = 0			± 10	μA
Cutoff Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = - 10V, I <sub>D</sub> = - 1mA	- 1.0		- 2.0	V
Forward Transfer Admittance	Y <sub>fs</sub>	V <sub>DS</sub> = - 10V, I <sub>D</sub> = - 4A	3.5	6		S
Static Drain to Source on State Resistance	R <sub>DS(on)</sub>	I <sub>D</sub> = - 4A, V <sub>GS</sub> = - 10V	0.15	0.2		Ω
	R <sub>DS(on)</sub>	I <sub>D</sub> = - 4A, V <sub>GS</sub> = - 4V	0.2	0.27		Ω

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**Package Dimensions 2093**

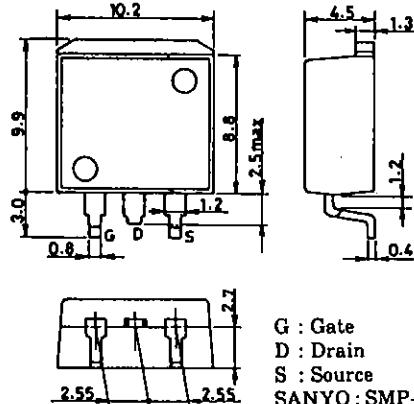
(unit : mm)



G : Gate  
D : Drain  
S : Source  
SANYO : SMP

**Package Dimensions 2090**

(unit : mm)



G : Gate  
D : Drain  
S : Source  
SANYO : SMP-FD

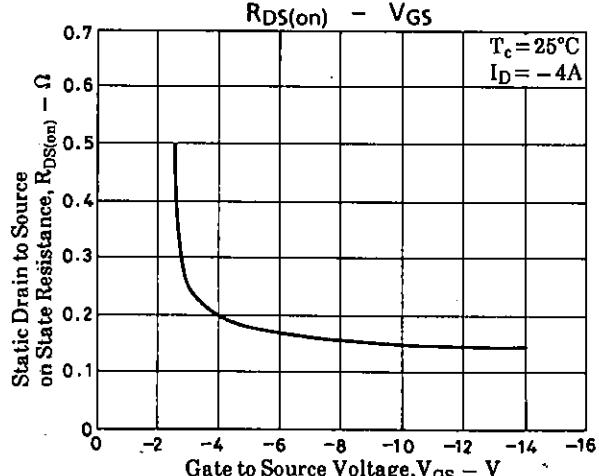
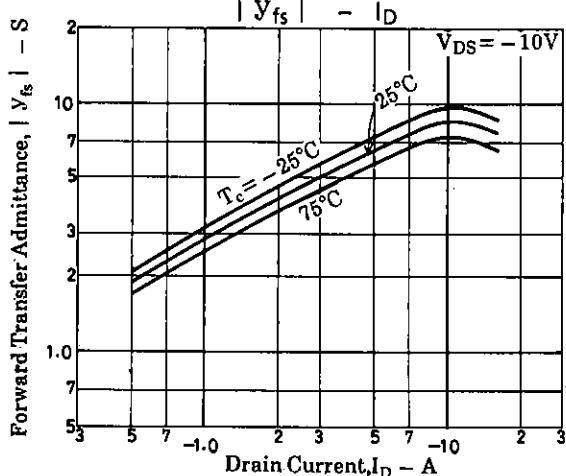
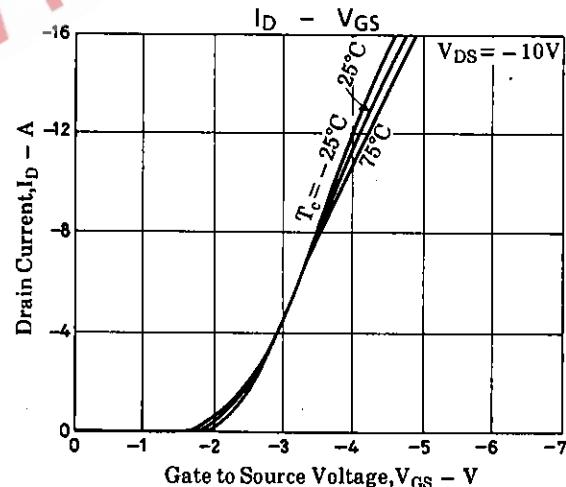
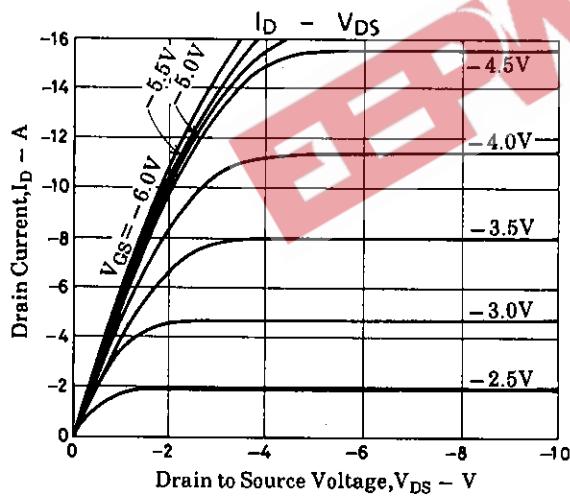
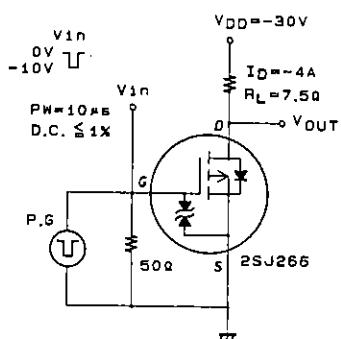
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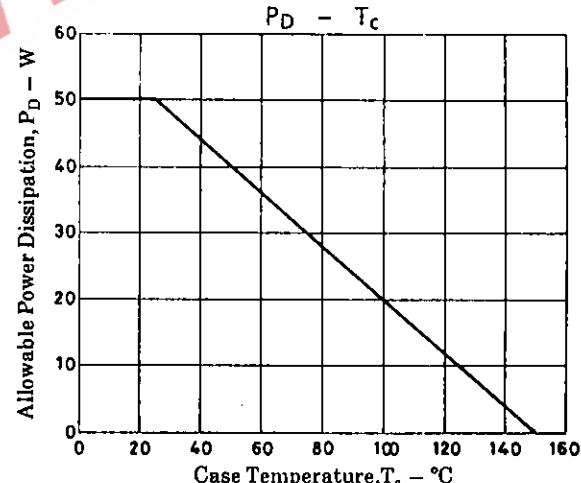
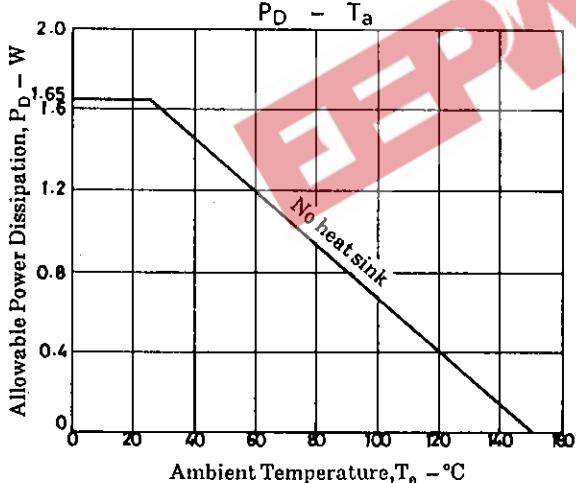
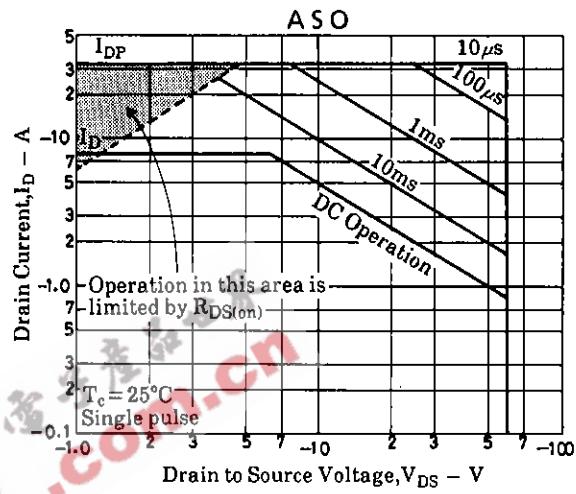
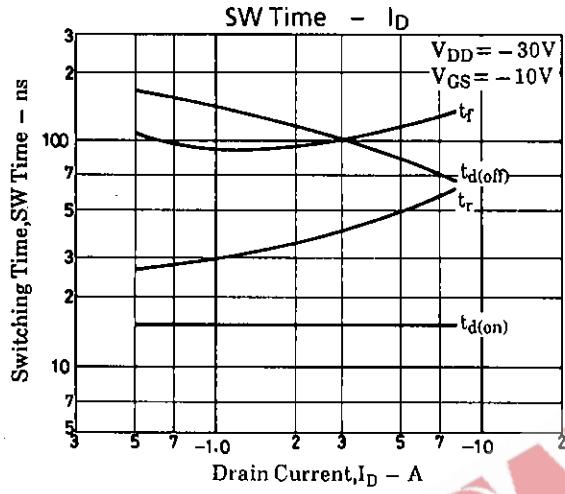
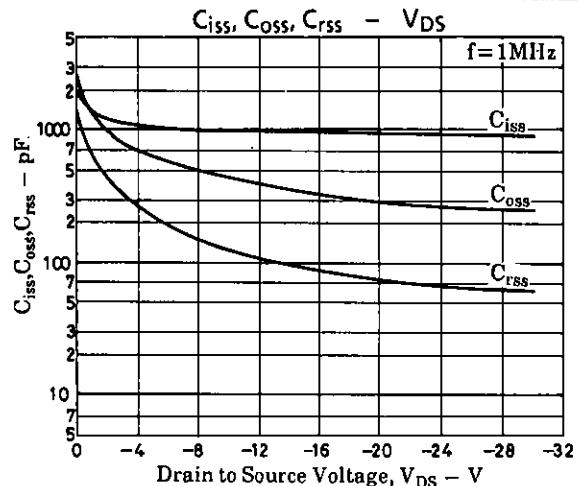
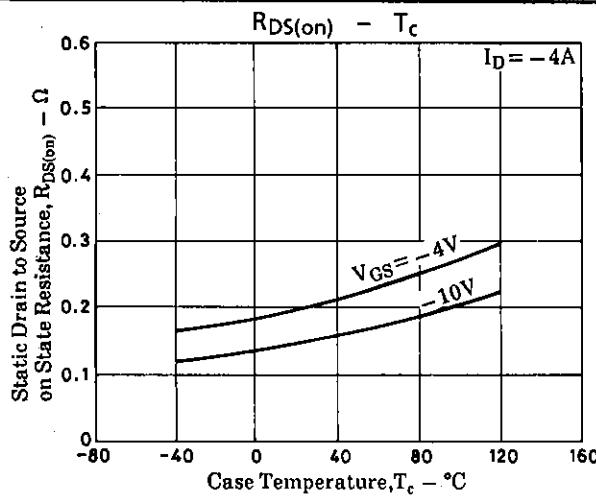
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			min	typ	max	unit
Input Capacitance	$C_{iss}$	$V_{DS} = -20V, f = 1MHz$		950		pF
Output Capacitance	$C_{oss}$	$V_{DS} = -20V, f = 1MHz$		300		pF
Reverse Transfer Capacitance	$C_{rss}$	$V_{DS} = -20V, f = 1MHz$		75		pF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.		15		ns
Rise Time	$t_r$	"		45		ns
Turn-OFF Delay Time	$t_{d(off)}$	"		90		ns
Fall Time	$t_f$	"		110		ns
Diode Forward Voltage	$V_{SD}$	$I_S = -8A, V_{GS} = 0$		-1.0	-1.5	V

### Switching Time Test Circuit





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