

# MOS FIELD EFFECT TRANSISTOR

# 2SJ463A

# P-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR HIGH SPEED SWITCHING

#### **DESCRIPTION**

The 2SJ463A is a switching device which can be driven directly by a 2.5 V power source.

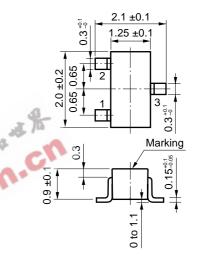
The 2SJ463A has excellent switching characteristics, and is suitable for use as a high-speed switching device in digital circuits.

#### **FEATURES**

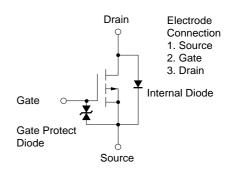
FEATURES				
• Can be driven by a 2.5 V	power sour	ce.		3 %
<ul> <li>Low Gate Cut-off Voltage</li> </ul>	<del>)</del> .		20	4.0
			27	5 " C
ABSOLUTE MAXIMUM	<b>RATINGS</b>	$(T_A = 25  ^{\circ}C)$	36	±0.1
Drain to Source Voltage	VDSS	-30	V	6.0
Gate to Source Voltage	Vgss	∓20	V	
Drain Current (DC)	ID(DC)	∓0.1	A	
Drain Current (pulse)	ID(pulse)	∓0.4 Note	Α	
Total Power Dissipation	Pt	150	mW	E
Channel Temperature	Tch	150	°C	
Storage Temperature	Tstg	-55 to +150	°C	

**Note** PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1 %

#### Package Drawings (unit: mm)



### **Equivalent Circuit**



Marking: H21

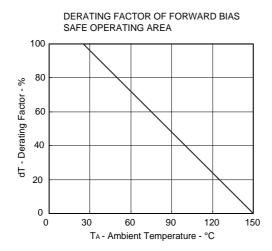
The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device is actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

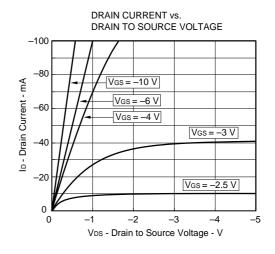


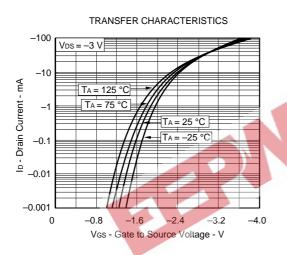
## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C)

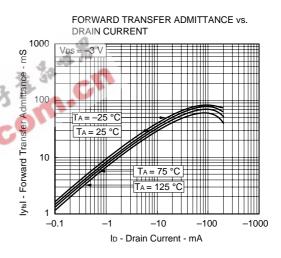
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain Cut-off Current	Ipss			-1	μΑ	V <sub>DS</sub> = -30 V, V <sub>GS</sub> = 0
Gate Leakage Current	Igss			<b>∓</b> 10	μΑ	Vgs = ∓20 V, Vps = 0
Gate Cut-off Voltage	VGS(off)	-1.0	-1.4	-1.7	V	$V_{DS} = -3 \text{ V}, \text{ ID} = -10 \mu \text{A}$
Forward Transfer Admittance	yfs	20			mS	$V_{DS} = -3 \text{ V}, I_{D} = -10 \text{ mA}$
Drain to Source On-State Resistance	RDS(on)1		23	60	Ω	Vgs = -2.5 V, Ib = -1 mA
Drain to Source On-State Resistance	RDS(on)2		11	23	Ω	Vgs = -4 V, Ib = -10 mA
Drain to Source On-State Resistance	RDS(on)3		6	13	Ω	Vgs = -10 V, ID = -10 mA
Input Capacitance	Ciss		5		pF	V <sub>DS</sub> = −3 V
Output Capacitance	Coss		15		pF	Vgs = 0
Reverse Transfer Capacitance	Crss		1.3		pF	f = 1 MHz
Turn-on Delay Time	td(on)		140		ns	$V_{DD} = -3 \text{ V, } I_{D} = -10 \text{ mA}$
Rise Time	tr		330	1	ns	$V_{GS(on)}$ = -4 V, Rg = 10 $\Omega$
Turn-off Delay Time	td(off)		220	26 1	ns	$R_L = 300 \Omega$
Fall Time	tf		320		ns	
		3				

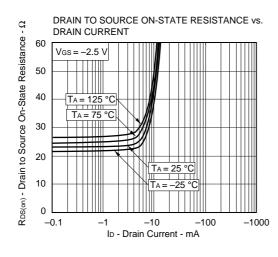
# **NEC**

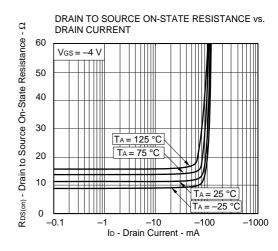




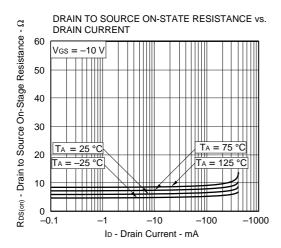


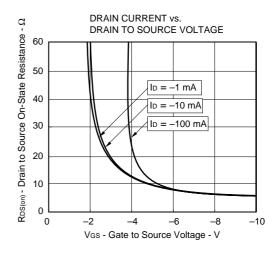


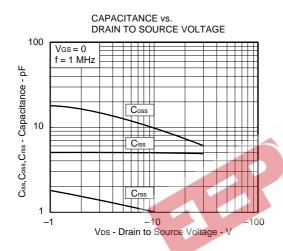


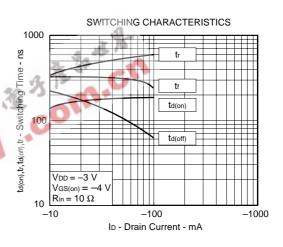


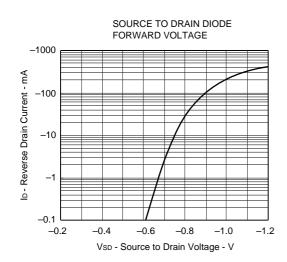














### **REFERENCE**

Document Name	Document No.
NEC semiconductor device reliability/quality control system	TEI-1202
Quality grade on NEC semiconductor devices	C11531E
Semiconductor device mounting technology manual	C10535E
Guide to quality assurance for semiconductor devices	MEI-1202
Semiconductor selection guide	X10679E



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Anti-radioactive design is not implemented in this product.