

# 2SJ535 Silicon P Channel MOS FET

REJ03G0885-0400 (Previous: ADE-208-627B) Rev.4.00 Sep 07, 2005

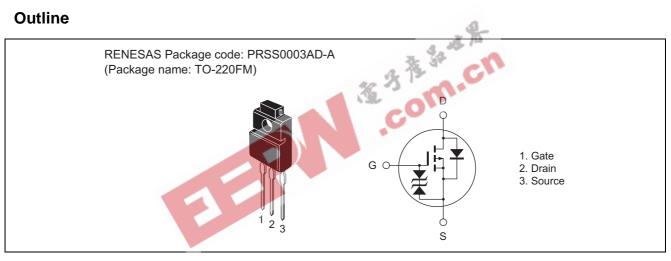
### Description

High speed power switching

## **Features**

- Low on-resistance
- $R_{DS (on)} = 0.028 \Omega$  typ. • Low drive current.
- 4 V gate drive devices.
- High speed switching.

### Outline





## Absolute Maximum Ratings

			$(Ta = 25^{\circ}C)$
Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	-60	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	ID	-30	A
Drain peak current	I <sub>D (pulse)</sub> Note 1	-120	A
Body to drain diode reverse drain current	I <sub>DR</sub>	-30	A
Avalanche current	I <sub>AP</sub> Note 3	-30	A
Avalanche energy	E <sub>AR</sub> Note 3	77	mJ
Channel dissipation	Pch Note 2	35	W
Channel temperature	Tch	150	٥C
Storage temperature	Tstg	-55 to +150	٦°

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Value at Tc = 25°C

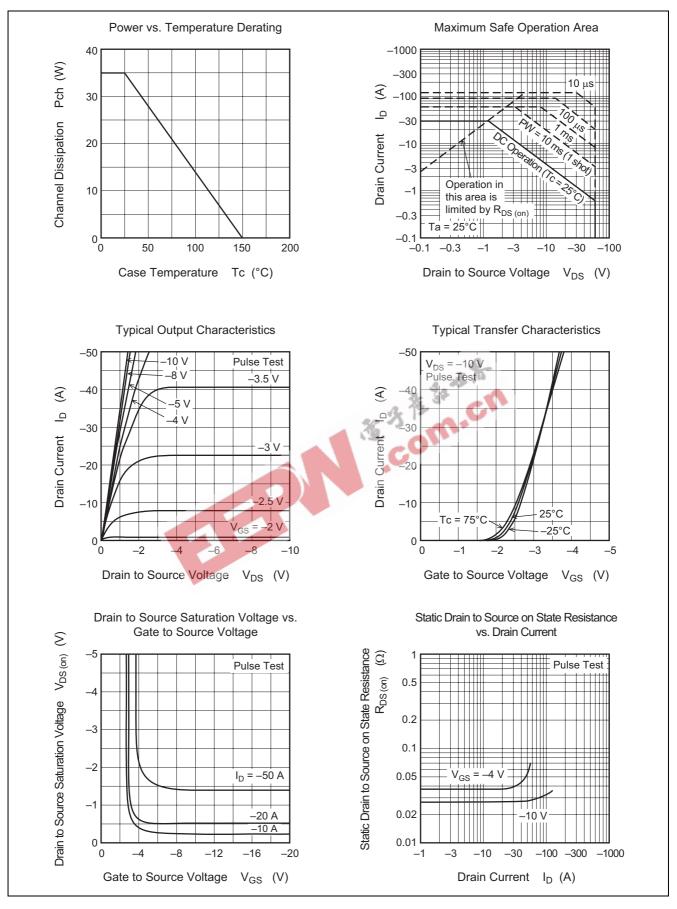
3. Value at Tch =  $25^{\circ}$ C, Rg  $\geq 50 \Omega$ 

## **Electrical Characteristics**

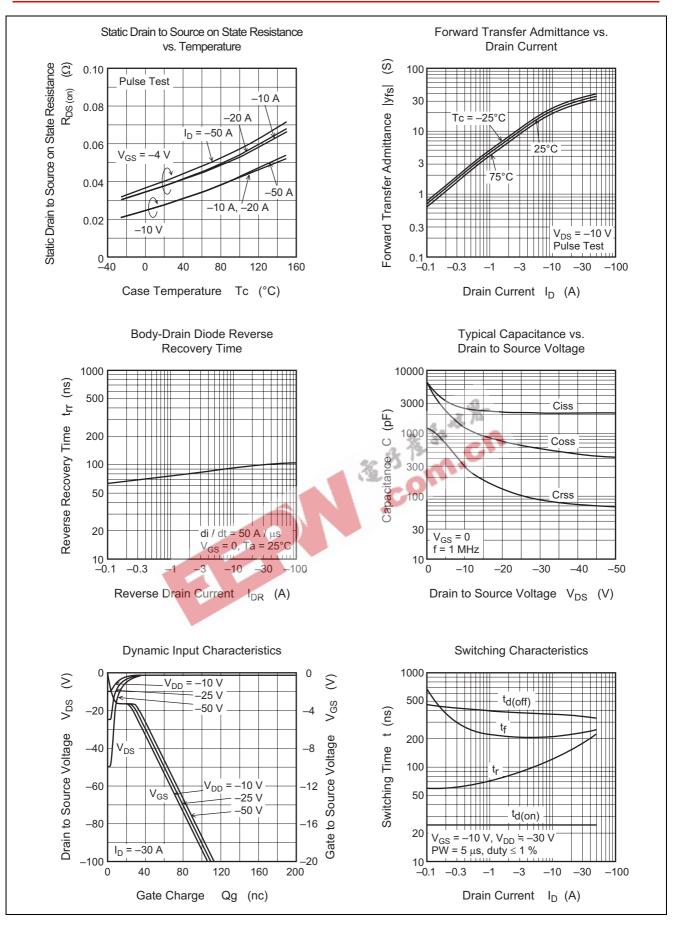
						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V (BR) DSS	-60			- V-	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V (BR) GSS	±20	_	40	V	$I_{\rm G} = \pm 100 \ \mu {\rm A}, \ {\rm V}_{\rm DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	a	-10	μA	$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>		<u>%</u> _)	±10	μA	$V_{GS} = \pm 16 V, V_{DS} = 0$
Gate to source cutoff voltage	V <sub>GS (off)</sub>	-1.0		-2.0	V	$I_D = -1 \text{ mA}, V_{DS} = -10 \text{ V}$
Static drain to source on state resistance	R <sub>DS</sub> (on)	+	0.028	0.037	Ω	$I_D = -15 \text{ A}, V_{GS} = -10 \text{ V}^{Note 4}$
	R <sub>DS (on)</sub>	-	0.038	0.055	Ω	$I_D = -15 \text{ A}, V_{GS} = -4 \text{ V}^{Note 4}$
Forward transfer admittance	y <sub>fs</sub>	15	25	—	S	$I_D = -15 \text{ A}, V_{DS} = -10 \text{ V}^{Note 4}$
Input capacitance	Ciss	_	2500	—	pF	$V_{DS} = -10 \text{ V}$
Output capacitance	Coss	_	1300	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	300	—	pF	f = 1 MHz
Turn-on delay time	t <sub>d (on)</sub>	_	25	—	ns	$V_{GS} = -10 \text{ V}$
Rise time	tr	_	150	—	ns	$I_{\rm D} = -15 \ {\rm A}$
Turn-off delay time	t <sub>d (off)</sub>	_	350	—	ns	$R_L = 2 \Omega$
Fall time	t <sub>f</sub>	_	220	—	ns	
Body to drain diode forward voltage	V <sub>DF</sub>	_	-0.95		V	$I_F = -30 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	t <sub>rr</sub>		100	_	ns	$I_F = -30 \text{ A}, V_{GS} = 0$
						$di_F/dt = 50 A/\mu s$

Note: 4. Pulse test

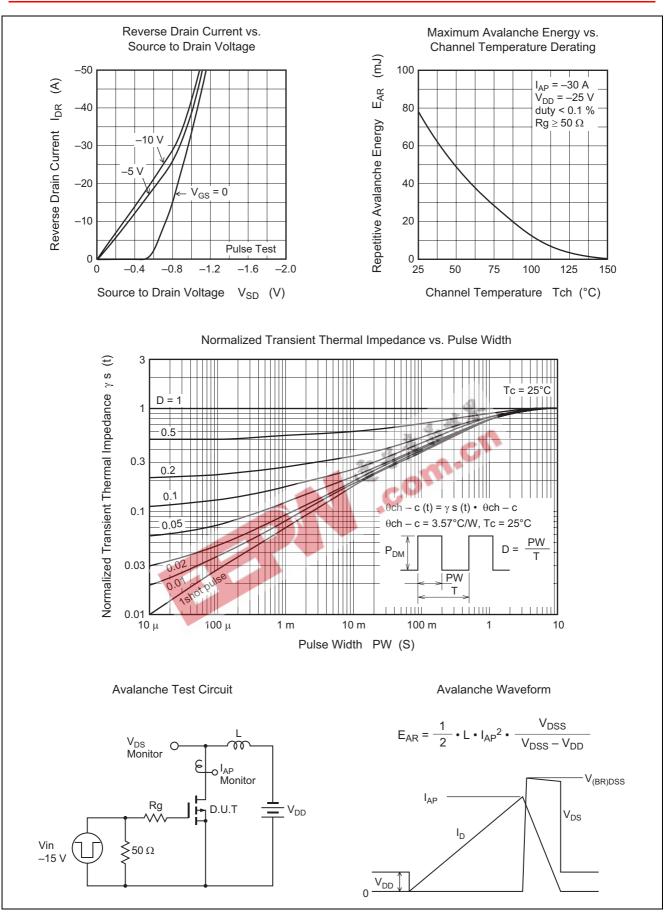
## **Main Characteristics**



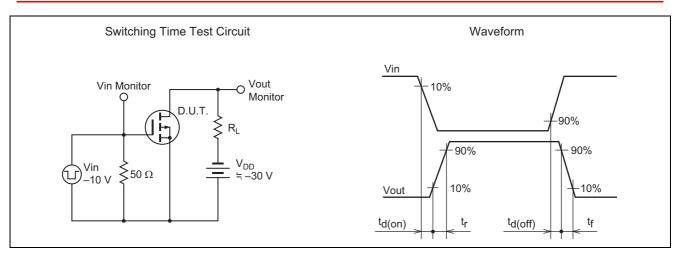








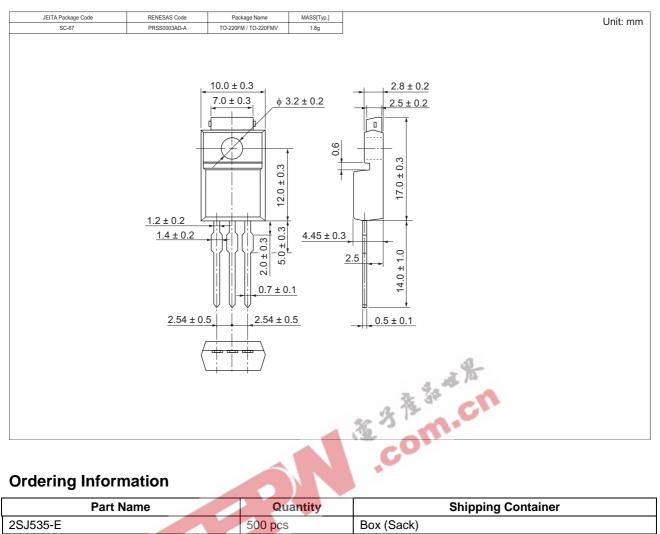








## **Package Dimensions**



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