

Silicon N Channel MOS FET

REJ03G0949-0200 (Previous: ADE-208-1289) Rev.2.00 Sep 07, 2005

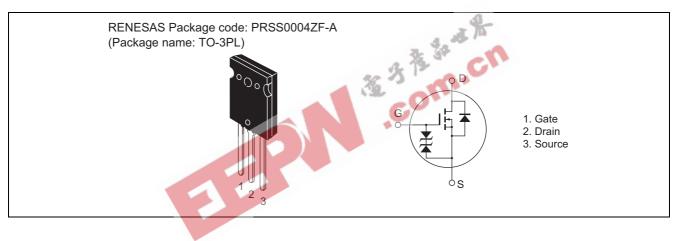
Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- Built-in fast recovery diode ($t_{rr} = 120 \text{ ns}$)
- Suitable for motor control, switching regulator, DC-DC converter

Outline





Absolute Maximum Ratings

| | | | | $(1a = 25^{\circ}C)$ | |
|---|---------|--------------------------------------|-------------|----------------------|--|
| Item | | Symbol | Ratings | Unit | |
| Drain to source voltage | 2SK1521 | V _{DSS} | 450 | V | |
| | 2SK1522 | | 500 | | |
| Gate to source voltage | | V _{GSS} | ±30 | V | |
| Drain current | | ID | 50 | А | |
| Drain peak current | | I _{D(pulse)} * ¹ | 200 | А | |
| Body to drain diode reverse drain current | | I _{DR} | 50 | А | |
| Channel dissipation | | Pch* ² | 250 | W | |
| Channel temperature | | Tch | 150 | °C | |
| Storage temperature | | Tstg | -55 to +150 | °C | |
| | | - | | | |

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Value at $T_C = 25^{\circ}C$

Electrical Characteristics

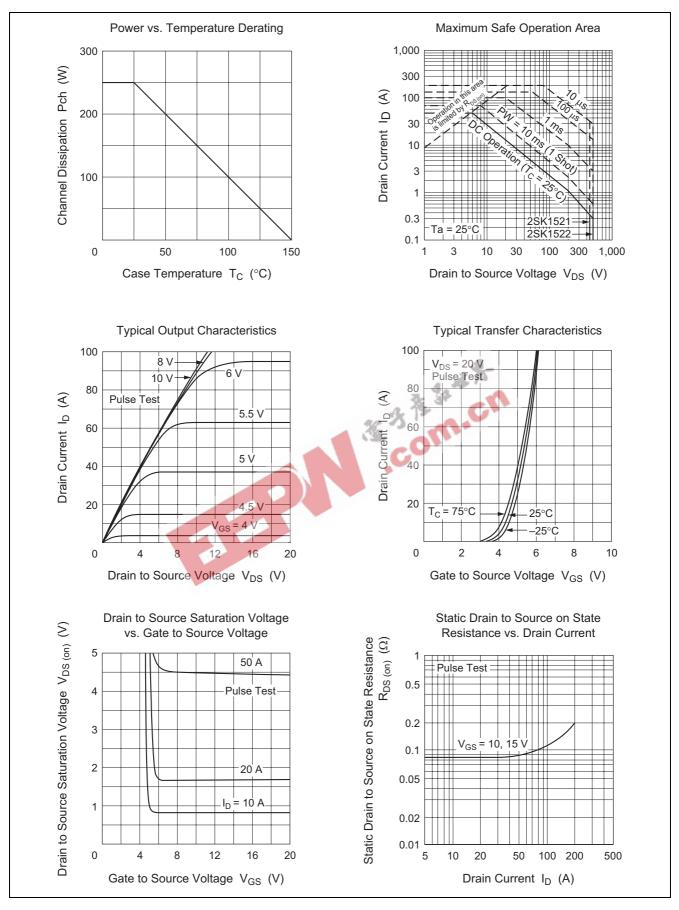
| | | | | | | | $(Ta = 25^{\circ}C)$ |
|--------------------------------------|---------|----------------------|---------------|--------------|------|------|--|
| Item | | Symbol | Min | Тур | Max | Unit | Test conditions |
| Drain to source | 2SK1521 | V _{(BR)DSS} | 450 | | _ | V | $I_D = 10 \text{ mA}, V_{GS} = 0$ |
| breakdown voltage | 2SK1522 | | 500 | | | | |
| Gate to source breakdown voltage | | $V_{(BR)GSS}$ | ±30 | | | - V | $I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$ |
| Gate to source leak current | | I _{GSS} | _ | | ±10 | μΑ | $V_{GS} = \pm 25 \text{ V}, \text{ V}_{DS} = 0$ |
| Zero gate voltage drain | 2SK1521 | I _{DSS} | — | _ | 250 | μA | $V_{DS} = 360 \text{ V}, \text{ V}_{GS} = 0$ |
| current | 2SK1522 | | | 26 | | | $V_{DS} = 400 V, V_{GS} = 0$ |
| Gate to source cutoff voltage | | V _{GS(off)} | 2.0 | | 3.0 | V | $I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$ |
| Static drain to source on | 2SK1521 | R _{DS(on)} | | 0 .08 | 0.10 | Ω | $I_D = 25 \text{ A}, V_{GS} = 10 \text{ V}^{*3}$ |
| state resistance | 2SK1522 | | \mathcal{H} | 0.085 | 0.11 | | |
| Forward transfer admittance | | y _{fs} | 22 | 35 | — | S | $I_D = 25 \text{ A}, V_{DS} = 10 \text{ V}^{*3}$ |
| Input capacitance | | Ciss | | 8700 | — | pF | $V_{DS} = 10 V, V_{GS} = 0,$ |
| Output capacitance | | Coss | — | 2400 | — | рF | f = 1 MHz |
| Reverse transfer capacitance | | Crss | _ | 235 | _ | рF | |
| Turn-on delay time | | t _{d(on)} | — | 85 | — | ns | $I_D = 25 \text{ A}, V_{GS} = 10 \text{ V},$ |
| Rise time | | t _r | — | 250 | — | ns | R _L = 1.2 Ω |
| Turn-off delay time | | t _{d(off)} | — | 600 | — | ns | |
| Fall time | | t _f | — | 250 | — | ns | |
| Body to drain diode forward voltage | | V_{DF} | — | 1.1 | — | V | $I_F = 50 \text{ A}, V_{GS} = 0$ |
| Body to drain diode reverse recovery | | t _{rr} | — | 120 | — | ns | $I_F = 50 \text{ A}, V_{GS} = 0,$ |
| time | | | | | | | di _F /dt = 100 A/μs |

Note: 3. Pulse test

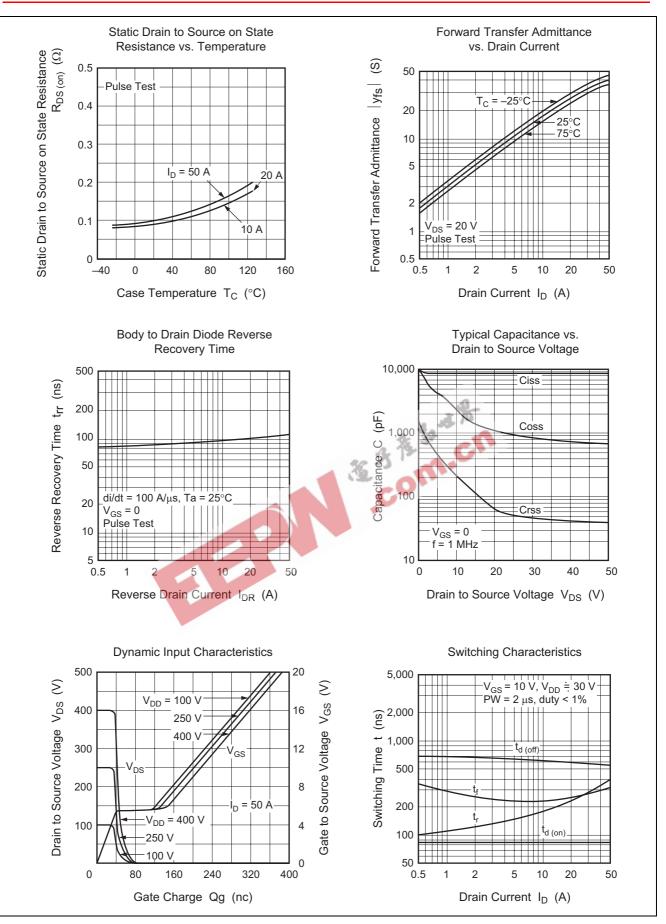


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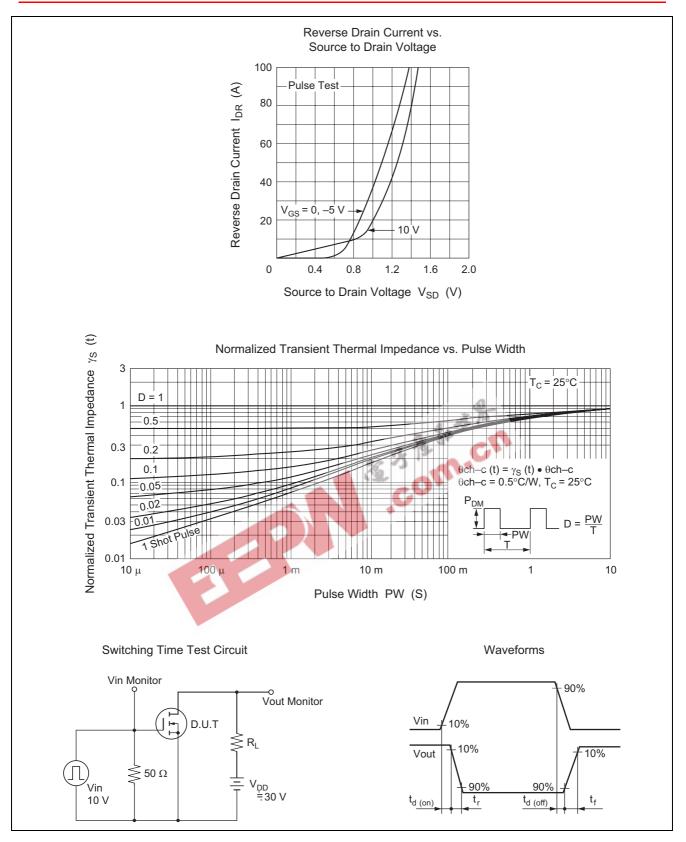
Main Characteristics



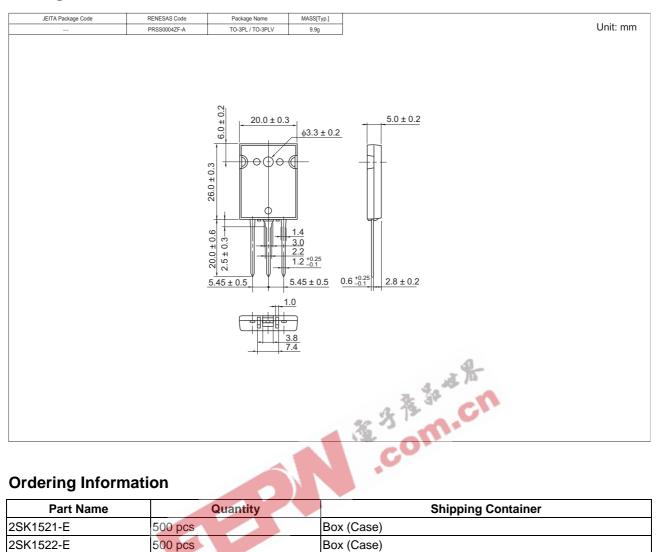








Package Dimensions



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