

SANYO

No.3568

2SK1430

N-Channel MOS Silicon FET

Very High-Speed
Switching Applications**Features**

- Low ON-state resistance.
- Very high-speed switching.
- Converters.
- Micaless package facilitating easy mounting.

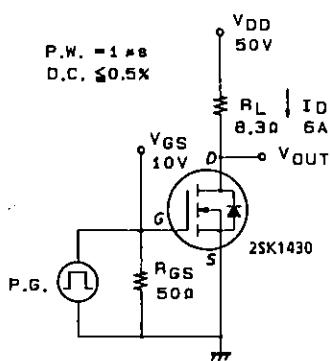
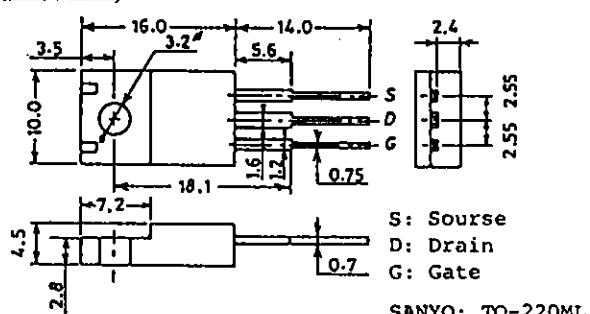
Absolute Maximum Ratings at Ta = 25°C

		unit
Drain to Source Voltage	V _{DSS}	100 V
Gate to Source Voltage	V _{GSS}	±20 V
Drain Current(DC)	I _D	10 A
Drain Current(Pulse)	I _{DP}	PW ≤ 10 μs, duty cycle ≤ 1% 40 A
Allowable Power Dissipation	P _D	T _c = 25°C 25 W 2.0 W
Channel Temperature	T _{ch}	150 °C
Storage Temperature	T _{stg}	-55 to +150 °C

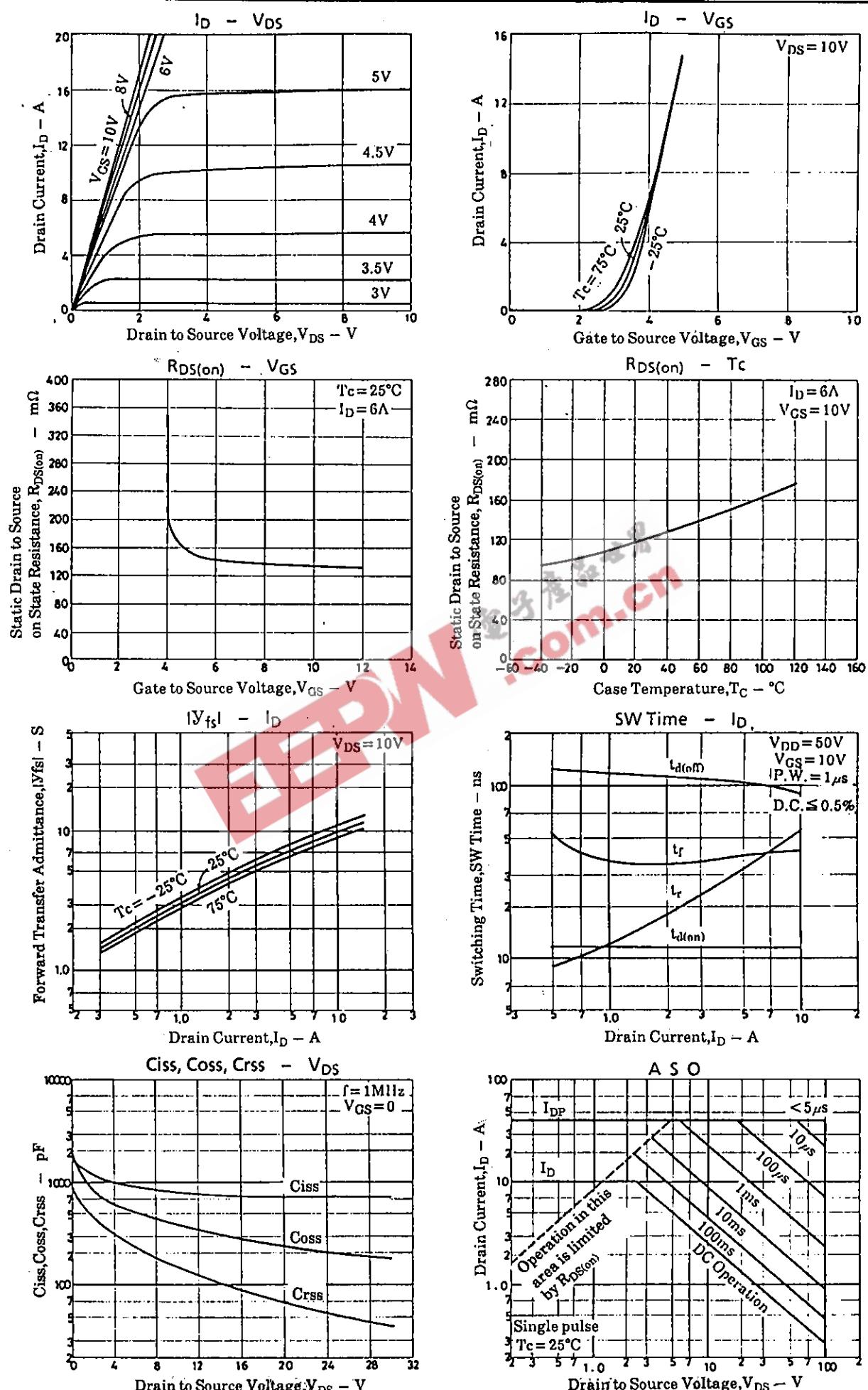
Electrical Characteristics at Ta = 25°C

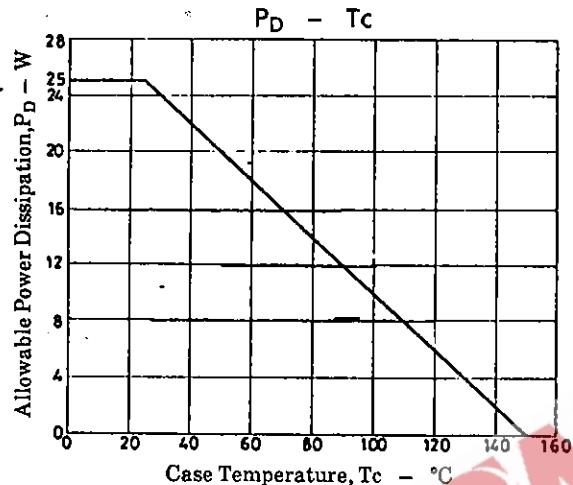
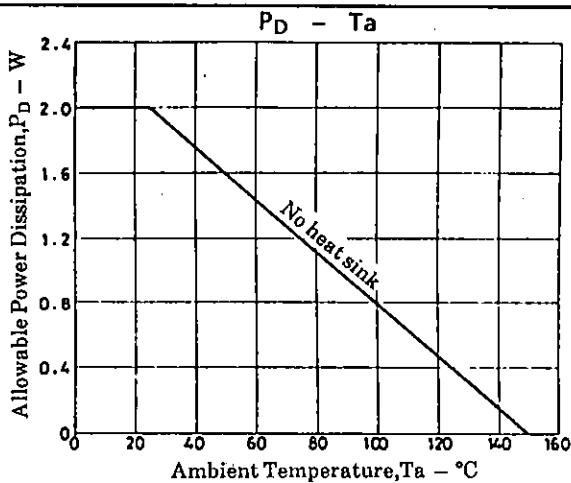
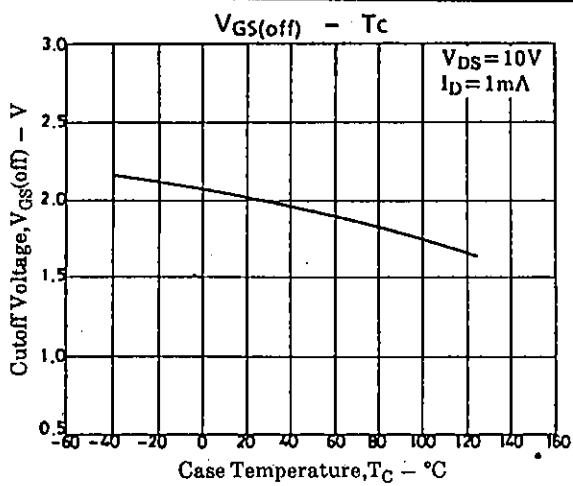
		min	typ	max	unit
D-S Breakdown Voltage	V _{(BR)DSS}	100			V
Zero Gate Voltage Drain Current	I _{DSS}			100	μA
Gate to Source Leakage Current	I _{GSS}	±100			nA
Cutoff Voltage	V _{GS(off)}	1.5		2.5	V
Forward Transfer Admittance	Y _{fs}	5.0	8.0		S
Static Drain to Source on State Resistance	R _{DS(on)}	0.12	0.16		Ω
Input Capacitance	C _{iss}	750			pF
Output Capacitance	C _{oss}	230			pF
Reverse Transfer Capacitance	C _{rss}	70			pF
Turn-ON Delay Time	t _{d(on)}	12			ns
Rise Time	t _r	38			ns
Turn-OFF Delay Time	t _{d(off)}	100			ns
Fall Time	t _f	40			ns
Diode Forward Voltage	V _{SD}	I _S = 10A, V _{GS} = 0 1.8			V

(Note) Be careful in handling the 2SK1430 because it has no protection diode between gate and source.

Switching Time Test Circuit**Package Dimensions 2063**
(unit : mm)

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