Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (L^2 - π -MOSV)

2SK2507

Chopper Regulator, DC-DC Converter and Motor Drive Applications

• 4-V gate drive

• Low drain-source ON resistance : RDS (ON) = 0.034Ω (typ.)

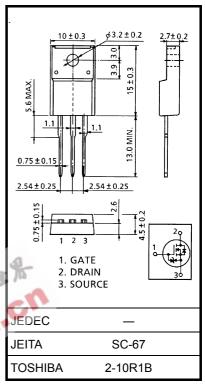
• High forward transfer admittance $|Y_{fs}| = 16 \text{ S (typ.)}$

Low leakage current : $IDSS = 100 \mu A (max) (VDS = 50 V)$

• Enhancement mode $V_{th} = 0.8 \sim 2.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA}$

Absolute Maximum Ratings (Ta = 25°C)

Characteri	stics	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	50	V	
Drain-gate voltage (R	_{GS} = 20 kΩ)	V_{DGR}	50	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1)	ΙD	25	A 38e	
	Pulse (Note 1)	I_{DP}	75	文学	
Drain power dissipatio	n (Tc = 25°C)	P_{D}	30	W	
Single pulse avalanche	e energy (Note 2)	E _{AS}	138	mJ	
Avalanche current		I _{AR}	25	A	
Repetitive avalanche	energy (Note 3)	E _{AR}	3	mJ	
Channel temperature	1	T _{ch}	150	°C	
Storage temperature r	ange	T _{stg}	-55~150	°C	



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	4.17	°C / W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 25 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), $L = 272 \mu\text{H}$, $R_G = 25 \Omega$, $I_{AR} = 25 \text{ A}$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.



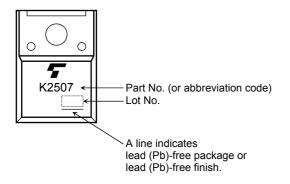
Electrical Characteristics (Ta = 25°C)

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	irrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 50 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	50	_	_	V
Gate threshold v	oltage/	V_{th}	V _{DS} = 10 V, I _D = 1 mA	0.8	_	2.0	V
Drain-source ON resistance		R _{DS (ON)}	V _{GS} = 4 V, I _D = 6 A	_	0.058	0.08	Ω
			V _{GS} = 10 V, I _D = 12 A	1	0.034	0.046	
Forward transfer	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 12 A	8.0	16	_	S
Input capacitano	e	C _{iss}			900	_	
Reverse transfe	r capacitance	C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	130	_	pF
Output capacitance		Coss		_	370	_	
Switching time	Rise time	t _r	$V_{GS} \stackrel{10 \text{ V}}{\text{0 V}} \prod \stackrel{I_D = 12 \text{ A}}{\text{1 V}_{out}}$	_	15	_	
	Turn-on time	t _{on}	0 V E R_L $= 2.5 \Omega$ $V_{DD} = 30 V$	ı	25	_	ns
	Fall time	t _f		_	30		115
	Turn-off time	t _{off}	Duty \leq 1%, $t_{\rm W} = 10~\mu \rm s$	7	110	_	
Total gate charge (Gate-source plus gate-drain)		Q_{g}	A COM	_	25	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 40 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 25 \text{ A}$	_	19	_	nC
Gate-drain ("miller") charge		Q _{gd}		_	6	_	

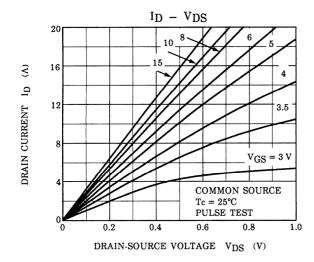
Source-Drain Ratings and Characteristics (Ta = 25°C)

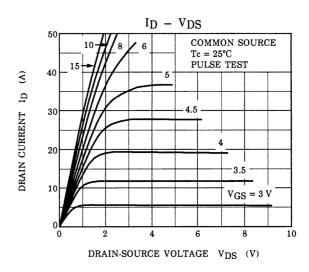
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	25	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	-	-	75	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 25 A, V _{GS} = 0 V	_	_	-1.6	V
Reverse recovery time	t _{rr}	I _{DR} = 25 A, V _{GS} = 0 V, dI _{DR} / dt = 50 A / μs	1	60	1	ns
Reverse recovery charge	Q _{rr}	TDR - 25 A, VGS - 0 V, αιDR / αι - 50 A / μs	_	45	_	μC

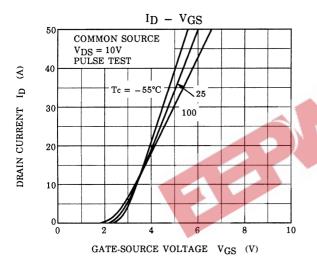
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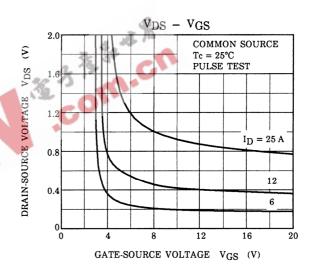


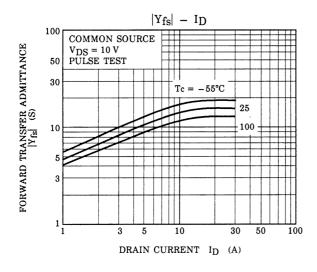
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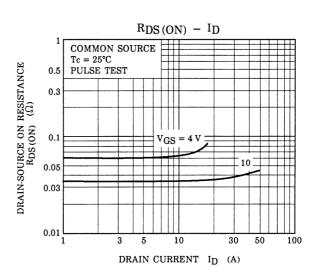




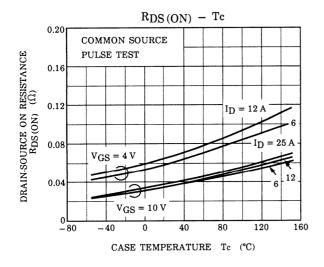


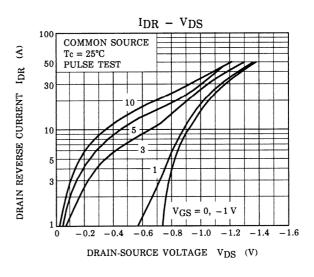


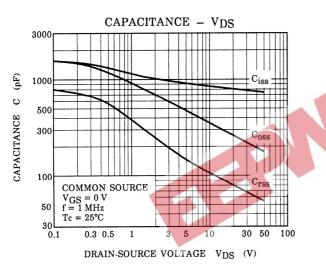


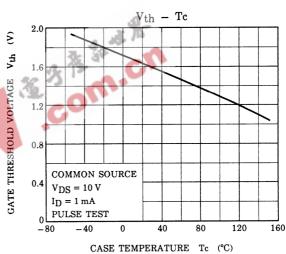


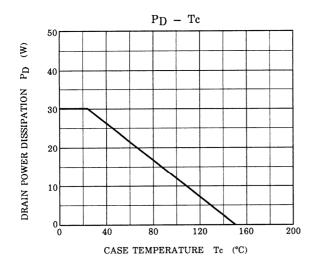
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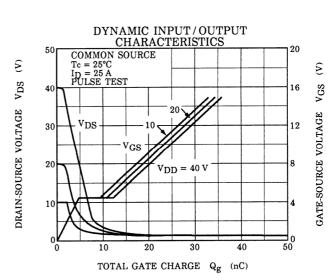


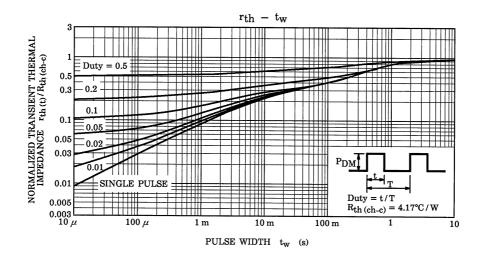


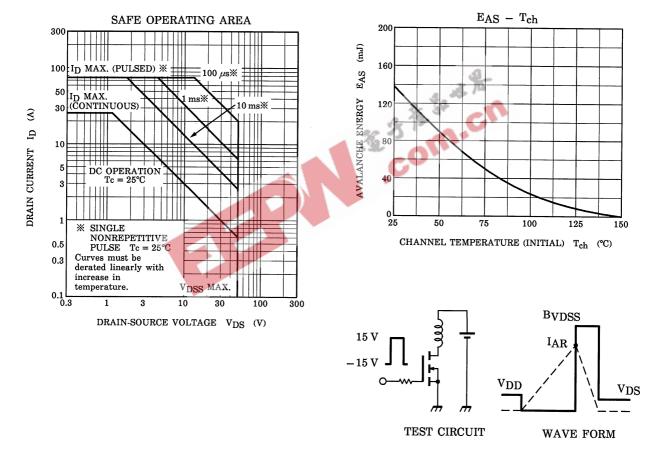












$$\begin{aligned} R_G &= 25~\Omega \\ V_{DD} &= 25~V,~L = 272~\mu H \end{aligned} \qquad EAS &= \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - VDD} \right) \end{aligned}$$

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