Silicon N-Channel MOS FET

# **HITACHI**

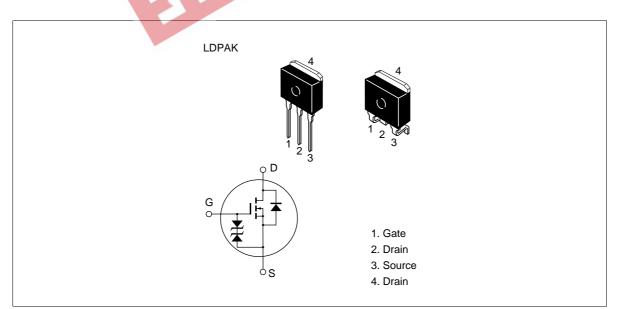
## Application

High speed power switching

### **Features**

- Low on-resistance
- High speed switching •
- Low drive current •
- No secondary breakdown •
- ·Com.cn Suitable for switching regulator, DC-DC converter and motor driver ٠

### Outline





### **Absolute Maximum Ratings** ( $Ta = 25^{\circ}C$ )

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	150	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	10	А
Drain peak current	I *1 D(pulse)	40	А
Body to drain diode reverse drain current	I <sub>DR</sub>	10	А
Channel dissipation	Pch*2	50	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C
2. Value at $T_c = 25^{\circ}C$	36 B.	om.cn	

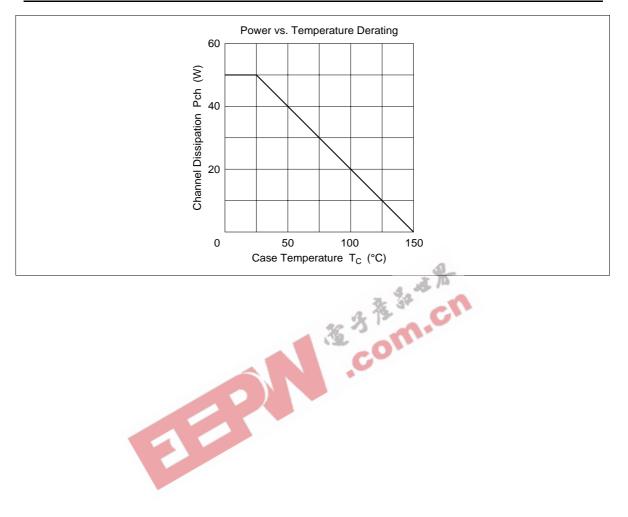
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<sup>2.</sup> Value at  $T_c = 25^{\circ}C$ 

# **Electrical Characteristics** (Ta = $25^{\circ}$ C)

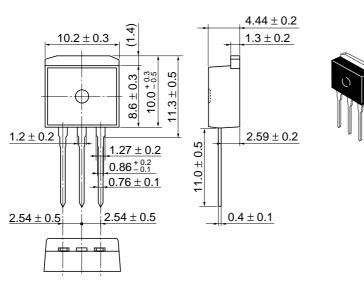
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{\rm (BR)DSS}$	150	_	_	V	$I_{\rm D} = 10 \text{ mA}, V_{\rm GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_{g} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	—	—	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	—	—	250	μΑ	$V_{\rm DS} = 120 \ V, \ V_{\rm GS} = 0$
Gate to source cutoff voltage	$V_{\text{GS(off)}}$	2.0	—	4.0	V	$I_{\rm D} = 1 \text{ mA}, V_{\rm DS} = 10 \text{ V}$
Static Drain to source on state resistance	$R_{DS(on)}$	—	0.12	0.15	Ω	$I_{D} = 5 \text{ A}, \text{ V}_{GS} = 10 \text{ V}^{*1}$
Forward transfer admittance	yfs	4.0	7.0	—	S	$I_{\rm D} = 5 \text{ A}, V_{\rm DS} = 10 \text{ V}^{*1}$
Input capacitance	Ciss	—	1200	_	pF	$V_{\rm DS} = 10 \text{ V}, \text{ V}_{\rm GS} = 0,$
Output capacitance	Coss	—	550	- 3:	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	85	3	рF	
Turn-on delay time	t <sub>d(on)</sub>	-	20	L0	ns	$I_{\rm D} = 5 \text{ A}, V_{\rm GS} = 10 \text{ V},$
Rise time	t,	-	50	5	ns	$R_L = 6 \Omega$
Turn-off delay time	t <sub>d(off)</sub>		70	_	ns	_
Fall time	t <sub>f</sub>	ZQ	40		ns	
Body to drain diode forward voltage	V <sub>DF</sub>	-	1.2	_	V	$I_{\rm F} = 10$ A, $V_{\rm GS} = 0$
Body to drain diode reverse recovery time	t <sub>rr</sub>	_	220	_	ns	$I_{F} = 10 \text{ A}, V_{GS} = 0,$ $di_{F}/dt = 50 \text{ A}/\mu \text{s}$
Note 1. Pulse test						

See characteristic curves of 2SK740.









Hitachi Code	LDPAK (L)
JEDEC	
EIAJ	
Weight (reference value)	1.4 g

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