

## 2SK1880(L), 2SK1880(S)

Silicon N Channel MOS FET

REJ03G0983-0200  
(Previous: ADE-208-1331)

Rev.2.00

Sep 07, 2005

### Application

High speed power switching

### Features

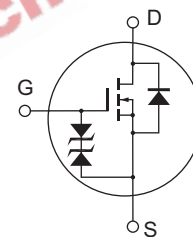
- Low on-resistance
- High speed switching
- No secondary breakdown
- Suitable for switching regulator

### Outline

RENESAS Package code: PRSS0004ZD-A  
(Package name: DPAK(L)-(1))



RENESAS Package code: PRSS0004ZD-C  
(Package name: DPAK(S))



1. Gate
2. Drain
3. Source
4. Drain

## Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	600	V
Gate to source voltage	V <sub>GSS</sub>	±30	V
Drain current	I <sub>D</sub>	1.5	A
Drain peak current	I <sub>D(pulse)</sub> * <sup>1</sup>	3.0	A
Body to drain diode reverse drain current	I <sub>DR</sub>	1.5	A
Channel dissipation	P <sub>ch</sub> * <sup>2</sup>	20	W
Channel temperature	T <sub>ch</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C

Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1 %  
 2. Value at Tc = 25°C

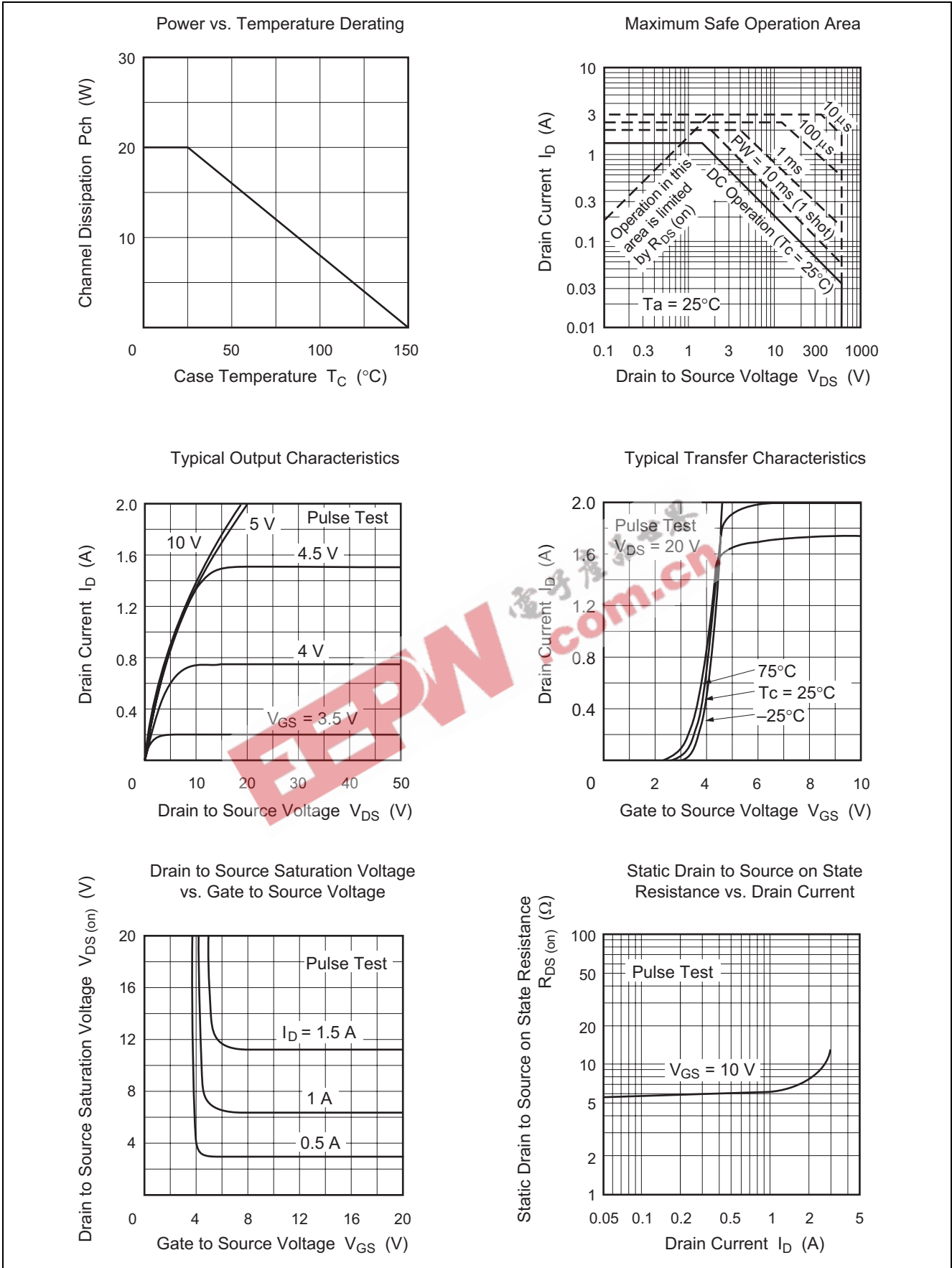
## Electrical Characteristics

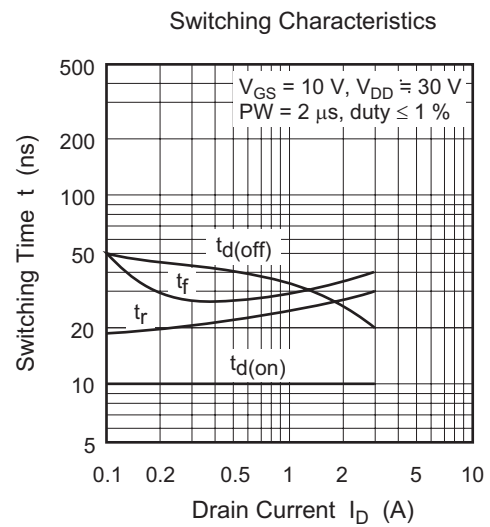
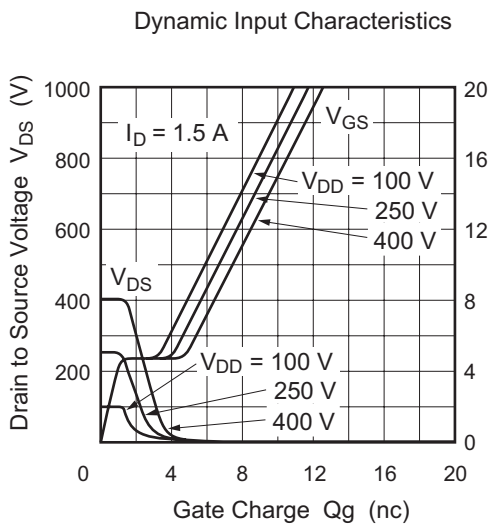
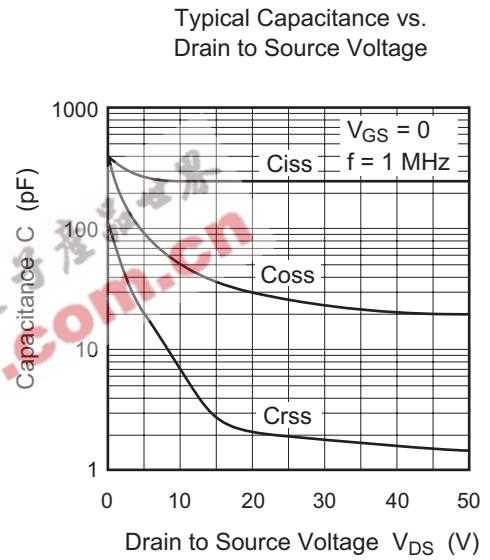
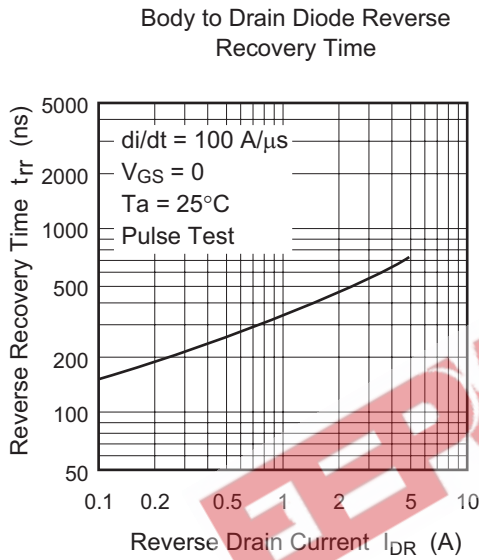
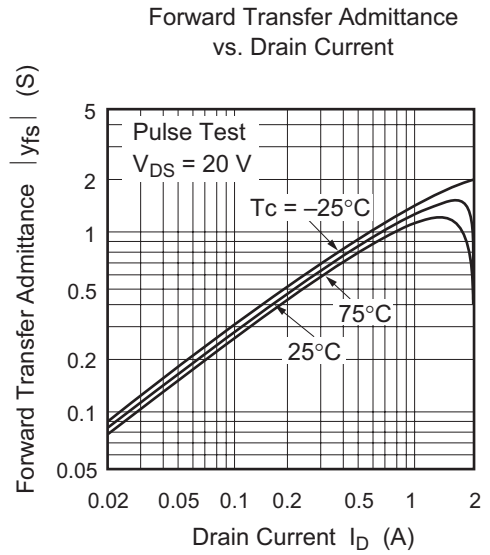
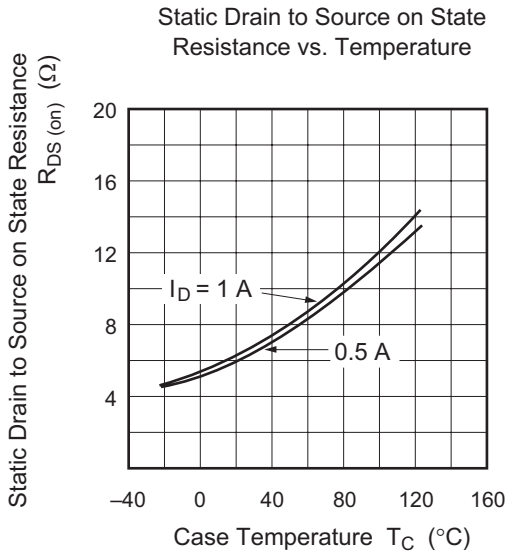
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	600	—	—	V	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0
Gate to source breakdown voltage	V <sub>(BR)GSS</sub>	±30	—	—	V	I <sub>G</sub> = ±100 μA, V <sub>DS</sub> = 0
Gate to source leak current	I <sub>GSS</sub>	—	—	±10	μA	V <sub>GS</sub> = ±25 V, V <sub>DS</sub> = 0
Zero gate voltage drain current	I <sub>DSS</sub>	—	—	100	μA	V <sub>DS</sub> = 500 V, V <sub>GS</sub> = 0
Gate to source cutoff voltage	V <sub>GS(off)</sub>	2.0	—	3.0	V	I <sub>D</sub> = 1 mA, V <sub>DS</sub> = 10 V
Static drain to source on state resistance	R <sub>DS(on)</sub>	—	6.5	8.0	Ω	I <sub>D</sub> = 1 A, V <sub>GS</sub> = 10 V* <sup>3</sup>
Forward transfer admittance	y <sub>fs</sub>	0.85	1.4	—	S	I <sub>D</sub> = 1 A, V <sub>DS</sub> = 20 V* <sup>3</sup>
Input capacitance	C <sub>iss</sub>	—	250	—	pF	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0, f = 1 MHz
Output capacitance	C <sub>oss</sub>	—	55	—	pF	
Reverse transfer capacitance	C <sub>rss</sub>	—	8	—	pF	
Turn-on delay time	t <sub>d(on)</sub>	—	10	—	ns	I <sub>D</sub> = 1 A, V <sub>GS</sub> = 10 V, R <sub>L</sub> = 30 Ω
Rise time	t <sub>r</sub>	—	25	—	ns	
Turn-off delay time	t <sub>d(off)</sub>	—	35	—	ns	
Fall time	t <sub>f</sub>	—	30	—	ns	
Body to drain diode forward voltage	V <sub>DF</sub>	—	0.95	—	V	I <sub>F</sub> = 1.5 A, V <sub>GS</sub> = 0
Body to drain diode reverse recovery time	t <sub>rr</sub>	—	350	—	μs	I <sub>F</sub> = 1.5 A, V <sub>GS</sub> = 0, di <sub>F</sub> /dt = 100 A/μs

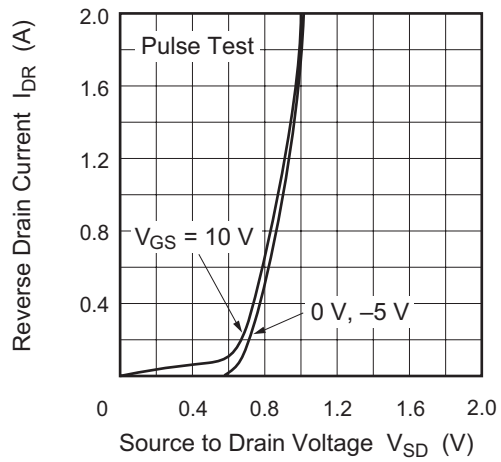
Note: 3. Pulse Test

Main Characteristics

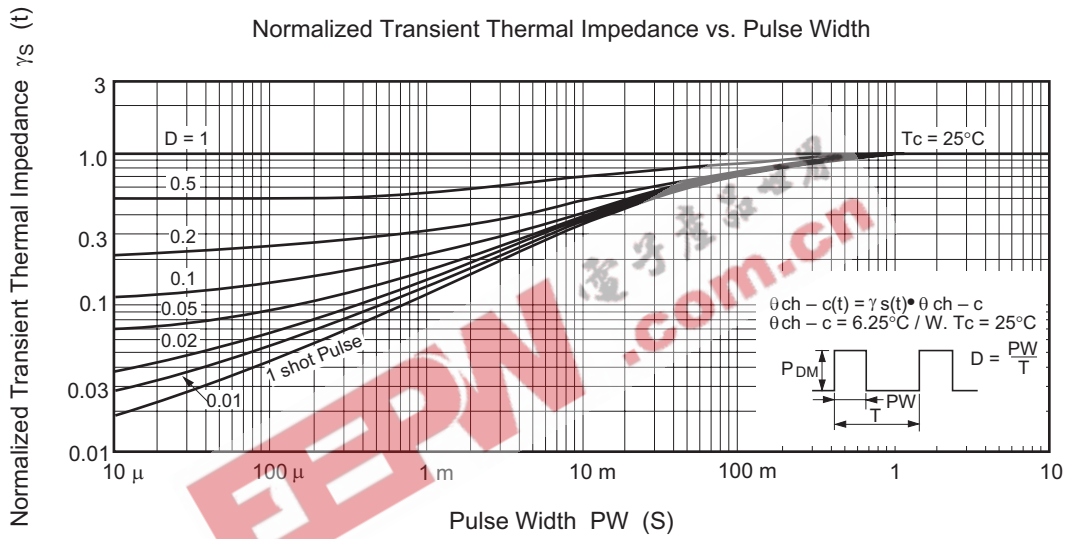




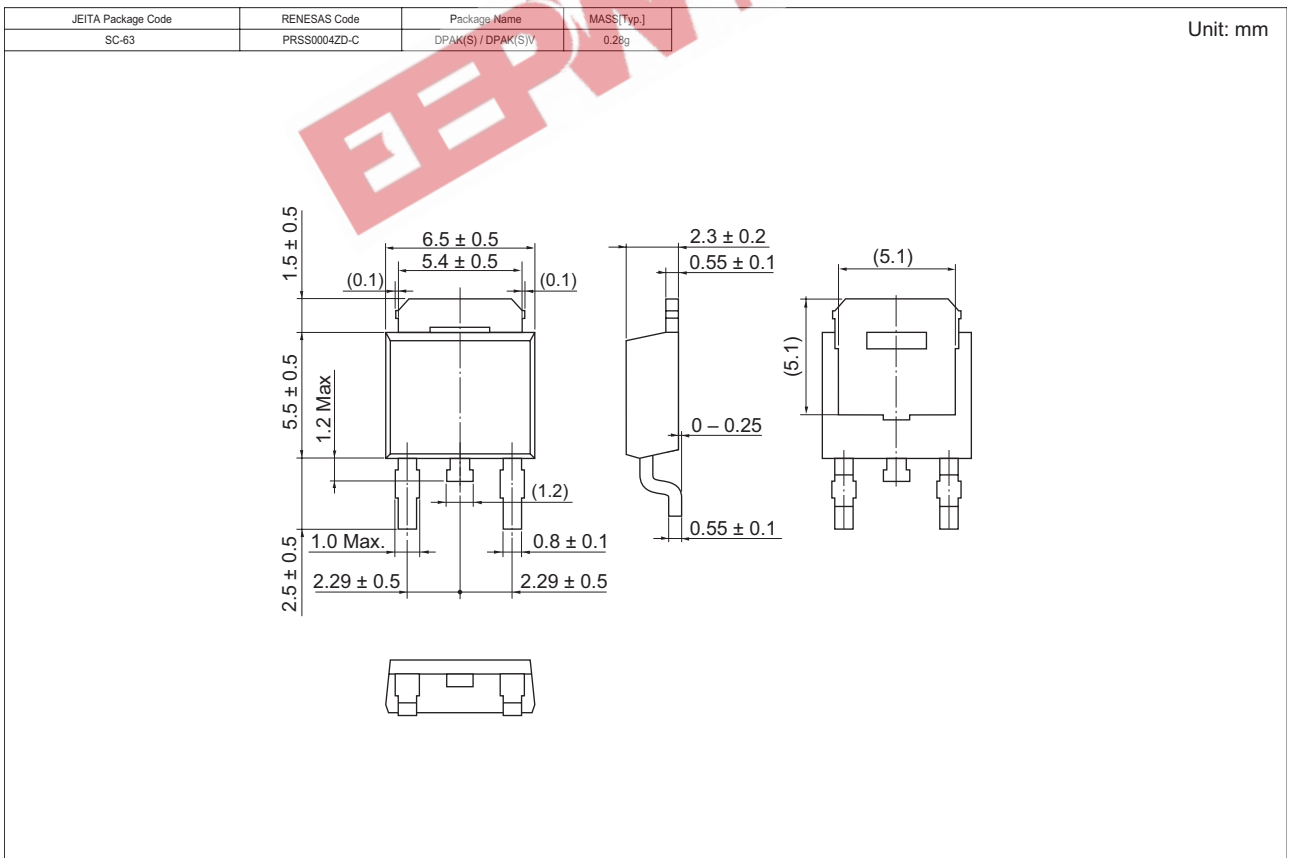
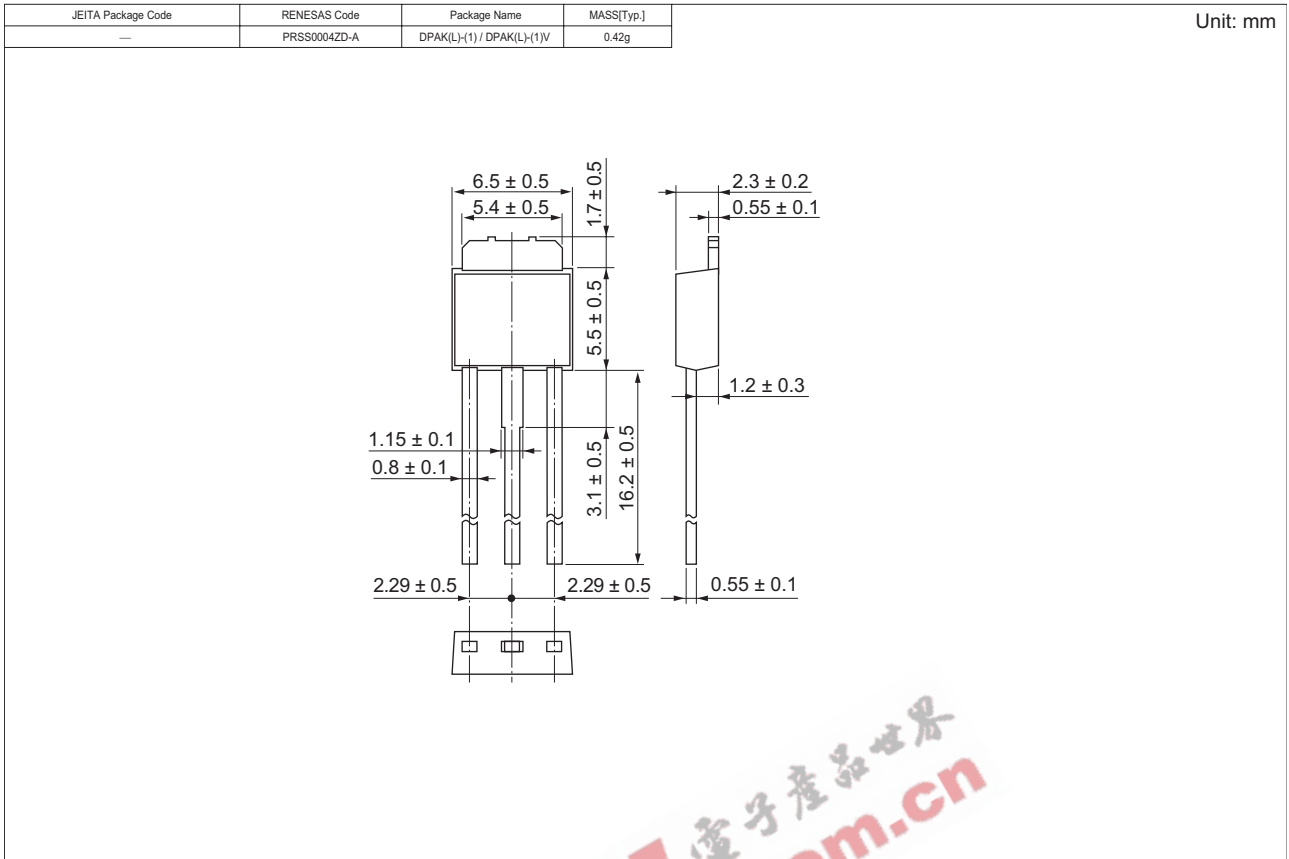
Reverse Drain Current vs. Source to Drain Voltage



Normalized Transient Thermal Impedance vs. Pulse Width



Package Dimensions



### Ordering Information

Part Name	Quantity	Shipping Container
2SK1880L-E	3200 pcs	Box (Sack)
2SK1880STL-E	3000 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.



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