

No.3774

2SK1473

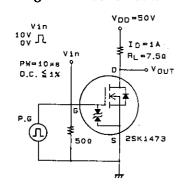
N-Channel MOS Silicon FET
Very High-Speed
Switching Applications

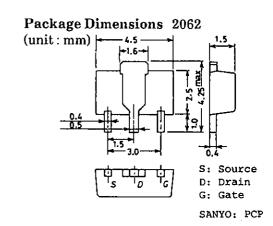
Features

- · Low ON resistance.
- · Very high-speed switching.
- · Low-voltage drive.

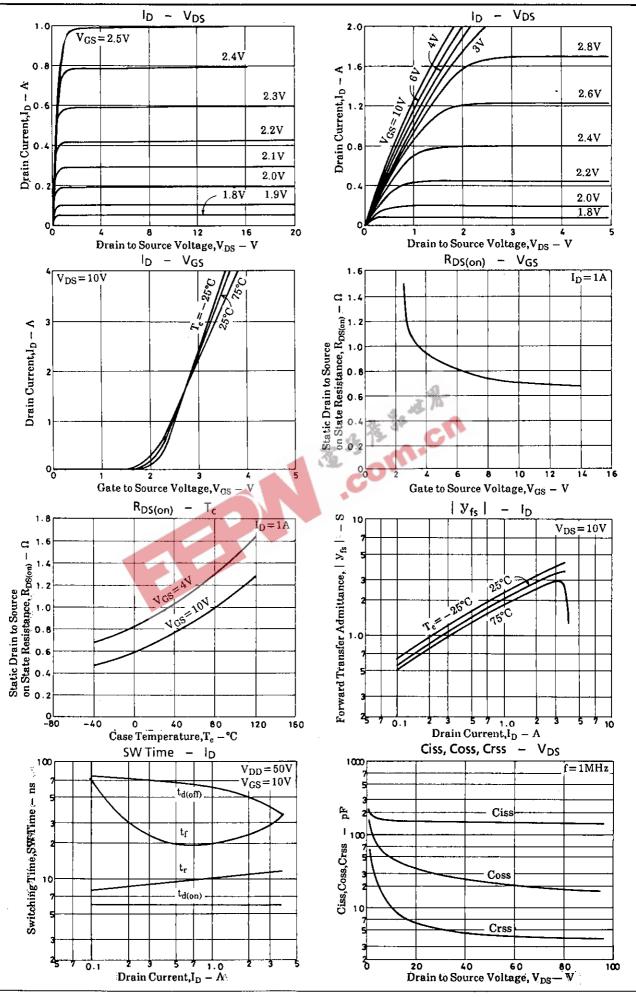
Absolute Maximum Ratings at Ta = 25°C unit						
Droin to Course Weller					unit	
Drain to Source Voltage	$v_{ m DSS}$			100	V	
Gate to Source Voltage	v_{GSS}			± 15	V	
Drain Current(DC)	I_{D}			2	Α	
Drain Current(Pulse)	I_{DP}	$PW \le 10 \mu s$, duty cycle $\le 1\%$		8	Α	
Allowable Power Dissipation	P_{D}	$T_c = 25$ °C		3.5	W	
		Mounted on ceramic board		1.5	W	
<u></u>		$(250 \text{mm}^2 \times 0.8 \text{mm})$				
Channel Temperature	Tch	- %	ppin-	150	$^{\circ}\mathrm{C}$	
Storage Temperature	Tstg	4, 45, 11	_55 to +	150	$^{\circ}\mathrm{C}$	
		$(250 \text{mm}^2 \times 0.8 \text{mm})$ $I_D = 1 \text{mA}, V_{GS} = 0$ $V_{DS} = 100 \text{V}, V_{GS} = 0$	(a)			,
Electrical Characteristics at Ta=	:25°C	20 %	min	typ	max	unit
D-S Breakdown Voltage	$V_{(BR)DSS}$	$I_D = 1 \text{mA}, V_{GS} = 0$	100	• •		v
Zero Gate Voltage	I_{DSS}	$V_{DS} = 100V, V_{GS} = 0$			100	μA
Drain Current						,
date to bounce beakage Current	I_{GSS}	$V_{GS} = \pm 12V, V_{DS} = 0$			± 10	μ A
Cutoff Voltage	V _{GS(off)}	$V_{DS} = 10V, I_D = 1mA$	1.0		2.0	v
Forward Transfer Admittance		$V_{DS} = 10V, I_{D} = 1A$	1.2	2.0	_,,	S
Static Drain to Source	R _{DS(on)}	$I_D = 1A, V_{GS} = 10V$		0.7	0.95	$\widetilde{\Omega}$
on State Resistance	R _{DS(on)}	$I_D = 1A$, $V_{GS} = 4V$		0.95	1.3	Ω
Input Capacitance	Ciss	$V_{DS} = 20V, f = 1MHz$		150	2.0	рF
Output Capacitance	Coss	$V_{DS} = 20V, f = 1MHz$		35		рF
Reverse Transfer Capacitance	Crss	$V_{DS} = 20V, f = 1MHz$		6		рF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit.		6		-
Rise Time	t_r	"		10		ns
Turn-OFF Delay Time	$\mathbf{t}_{\mathbf{d(off)}}$	"		60		ns
Fall Time	tf	,		20		ns
Diode Forward Voltage	v_{SD}	$I_S = 2A_1 V_{GS} = 0$				ns
	י טט	72 - 711, 1 G2 - 0		1.0		\mathbf{v}

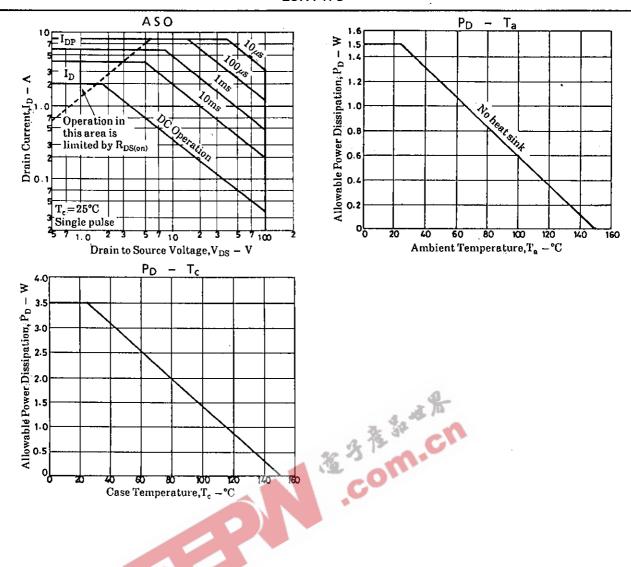
Switching Time Test Circuit





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