Silicon N-Channel MOS FET

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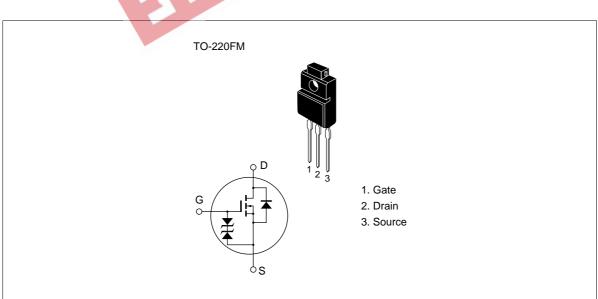
### Application

High speed power switching

#### Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for Switching regulator

### Outline





### **Absolute Maximum Ratings** ( $Ta = 25^{\circ}C$ )

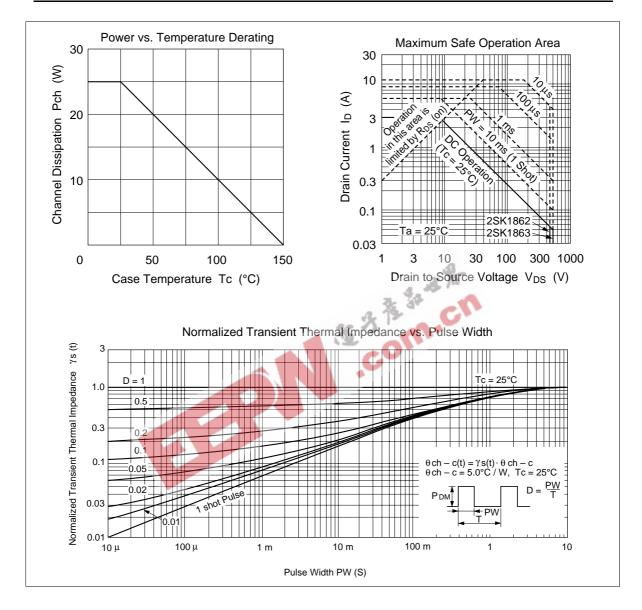
Item		Symbol	Ratings	Unit	
Drain to source voltage	2SK1862	V <sub>DSS</sub>	450	V	
	2SK1863	V <sub>DSS</sub>	500		
Gate to source voltage		$V_{GSS}$	±30	V	
Drain current		I <sub>D</sub>	3	А	
Drain peak current		I D(pulse) *1	12	А	
Body to drain diode reverse drain cur	I <sub>DR</sub>	3	А		
Channel dissipation	Pch*2	25	W		
Channel temperature	Tch	150	°C		
Storage temperature		Tstg	-55 to +150	°C	
2. Value at Tc = 25 °C		<b>逐</b> 方	-55 to +150		

## **Electrical Characteristics** (Ta = $25^{\circ}$ C)

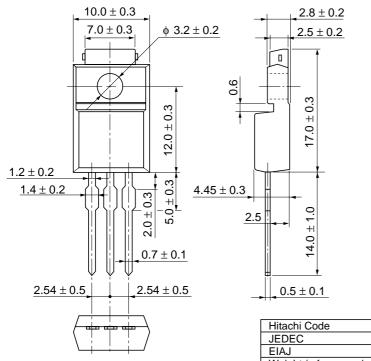
Item		Symbol	Min	Тур	Мах	Unit	Test conditions
Drain to source	2SK1862	V <sub>(BR)DSS</sub>	450	_	—	V	$I_{\rm D} = 10 \text{ mA}, V_{\rm GS} = 0$
breakdown voltage	2SK1863		500				
Gate to source b voltage	reakdown	$V_{(\text{BR})\text{GSS}}$	±30	_	_	V	$I_{g} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source le	eak current	I <sub>GSS</sub>	—		±10	μΑ	$V_{\text{GS}} = \pm 25 \text{ V},  V_{\text{DS}} = 0$
Zero gate	2SK1862	I <sub>DSS</sub>	_	_	250	μΑ	$V_{\rm DS} = 360 \text{ V}, V_{\rm GS} = 0$
voltage drain current	2SK1863						$V_{\rm DS} = 400 \ V, \ V_{\rm GS} = 0$
Gate to source c	utoff voltage	$V_{GS(off)}$	2.0		3.0	V	$I_{\rm D} = 1 \text{ mA}, V_{\rm DS} = 10 \text{ V}$
Static drain to	2SK1862	$R_{DS(on)}$	_	2.0	2.8	Ω	$I_{\rm D} = 2$ A, $V_{\rm GS} = 10$ V <sup>*1</sup>
source on state resistance	2SK1863		_	2.2	3.0	312	cn
Forward transfer	admittance	y <sub>fs</sub>	1.5	2.5	-	S	$I_{\rm D} = 2 \text{ A}$ $V_{\rm DS} = 10 \text{ V}^{*1}$
Input capacitance	е	Ciss	4	330	-	pF	V <sub>DS</sub> = 10 V
Output capacitan	ice	Coss	)))	90	_	pF	$V_{GS} = 0$
Reverse transfer	capacitance	Crss		15	—	pF	f = 1 MHz
Turn-on delay tin	ne	t <sub>d(on)</sub>	_	7	_	ns	I <sub>D</sub> = 2 A
Rise time		t,	_	20	—	ns	V <sub>GS</sub> = 10 V
Turn-off delay tin	ne	t <sub>d(off)</sub>	—	30	—	ns	$R_{L} = 15 \Omega$
Fall time		t <sub>f</sub>	_	20	_	ns	
Body to drain dio voltage	de forward	$V_{DF}$	—	0.9	_	V	$I_{\rm F} = 3 \text{ A}, V_{\rm GS} = 0$
Body to drain dio recovery time	de reverse	t <sub>rr</sub>	—	300		ns	$I_{F} = 3 \text{ A}, V_{GS} = 0,$ $di_{F} / dt = 100 \text{ A} / \mu \text{s}$

Note 1. Pulse Test

See characteristic curves of 2SK1153, 2SK1154







Unit: mm



Hitachi Code	TO-220FM
JEDEC	_
EIAJ	Conforms
Weight (reference value)	1.8 g

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#### Hitachi, Ltd.

URI

Nippon Bldg., 2-6-2, Otte-machi, Chiyoda-ku, Tokyo 100-0004, Japan Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

Hitachi Europe GmbH

NorthAmerica http:semiconductor.hitachi.com/ http://www.hitachi-eu.com/hel/ecg Europe http://www.has.hitachi.com.sg/grp3/sicd/index.htm http://www.has.hitachi.com.sg/grp3/sicd/index.htm Asia (Singapore) Asia (Taiwan) Asia (HongKong) http://www.hitachi.com.hk/eng/bo/grp3/index.htm http://www.hitachi.co.jp/Sicd/indx.htm

#### Japan

#### For further information write to:

Hitachi Semiconductor (America) Inc. 179 East Tasman Drive, San Jose,CA 95134 Tel: <1> (408) 433-1990 Fax: <1>(408) 433-0223

Electronic components Group Dornacher Stra§e 3 D-85622 Feldkirchen, Munich Germany Tel: <49> (89) 9 9180-0 Fax: <49> (89) 9 29 30 00 Hitachi Europe Ltd. Electronic Components Group. Whitebrook Park Lower Cookham Road Maidenhead Berkshire SL6 8YA, United Kingdom Tel: <44> (1628) 585000 Fax: <44> (1628) 778322

Hitachi Asia Pte. Ltd. 16 Collyer Quay #20-00 Hitachi Tower Singapore 049318 Tel: 535-2100 Fax: 535-1533

Hitachi Asia Ltd Taipei Branch Office 3F, Hung Kuo Building. No.167 Tun-Hwa North Road, Taipei (105) Tel: <886> (2) 2718-3666 Fax: <886> (2) 2718-8180

Hitachi Asia (Hong Kong) Ltd. Group III (Electronic Components) 7/F., North Tower, World Finance Centre, Harbour City, Canton Road, Tsim Sha Tsui, Kowloon, Hong Kong Tel: <852> (2) 735 9218 Fax: <852> (2) 730 0281 Telex: 40815 HITEC HX

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