



No.4602A

2SK2108

N-Channel MOS Silicon FET

Very High-Speed Switching Applications

Features

- Low ON resistance.
 - Very high-speed switching.
 - Low-voltage drive.
 - Micaless package facilitating mounting.

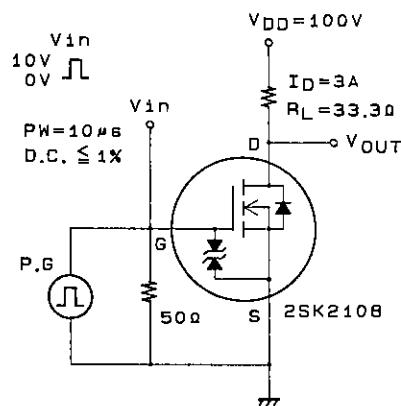
Absolute Maximum Ratings at $T_a = 25^\circ\text{C}$

Absolute Maximum Ratings at $T_A = 25^\circ\text{C}$		unit
Drain-to-Source Voltage	V_{DSS}	250 V
Gate-to-Source Voltage	V_{GSS}	± 30 V
Drain Current(DC)	I_D	6 A
Drain Current(Pulse)	I_{DP}	PW $\leq 10\ \mu\text{s}$, duty cycle $\leq 1\%$ 24 A
Allowable Power Dissipation	P_D	2.0 W
		$T_C = 25^\circ\text{C}$ 25 W
Channel Temperature	T_{ch}	150 $^\circ\text{C}$
Storage Temperature	T_{stg}	-55 to +150 $^\circ\text{C}$

Electrical Characteristics at Ta = 25°C

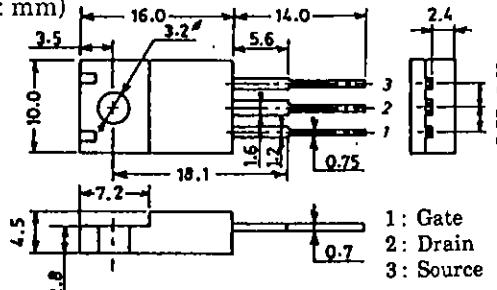
Electrical Characteristics at $T_A = 25^\circ C$		Unit
D-S Breakdown Voltage	$V_{(BR)DSS}$	$I_D = 1\text{mA}, V_{GS} = 0$
G-S Breakdown Voltage	$V_{(BR)GSS}$	$I_G = \pm 100\mu\text{A}, V_{DS} = 0$
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 250\text{V}, V_{GS} = 0$
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS} = \pm 25\text{V}, V_{DS} = 0$
Cutoff Voltage	$V_{GS(\text{off})}$	$V_{DS} = 10\text{V}, I_D = 1\text{mA}$
Forward Transfer Admittance	$ Y_{fs} $	$V_{DS} = 10\text{V}, I_D = 3\text{A}$
Static Drain-to-Source ON-State Resistance	$R_{DS(\text{on})}$	$I_D = 3\text{A}, V_{GS} = 10\text{V}$
Input Capacitance	C_{iss}	$V_{DS} = 20\text{V}, f = 1\text{MHz}$
Output Capacitance	C_{oss}	$V_{DS} = 20\text{V}, f = 1\text{MHz}$
Reverse Transfer Capacitance	C_{rss}	$V_{DS} = 20\text{V}, f = 1\text{MHz}$
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.
Rise Time	t_r	"
Turn-OFF Delay Time	$t_{d(off)}$	"
Fall Time	t_f	"
Diode Forward Voltage	V_{SD}	$I_S = 6\text{A}, V_{GS} = 0$

Switching Time Test Circuit



Package Dimensions 2063A

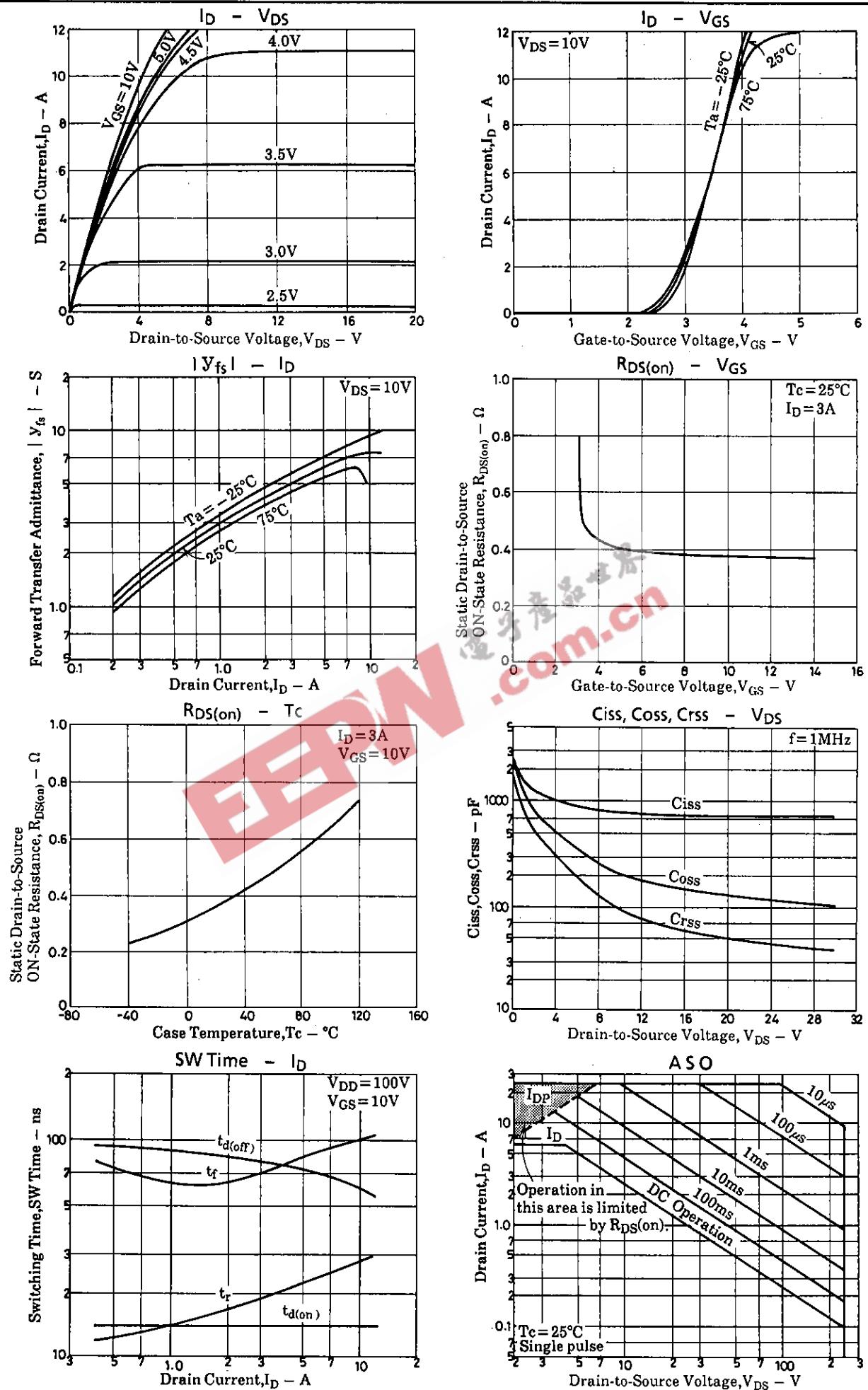
(unit : mm)

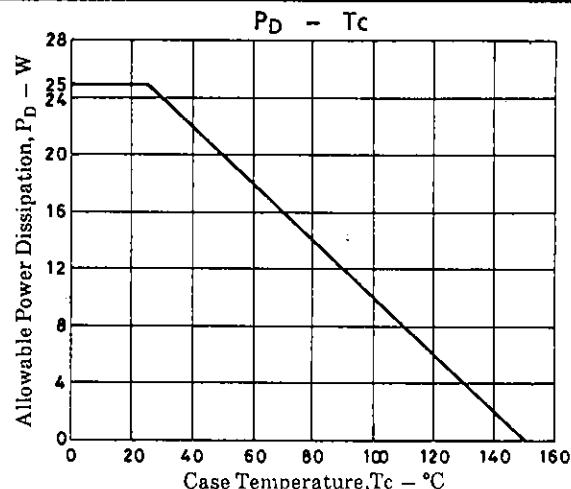
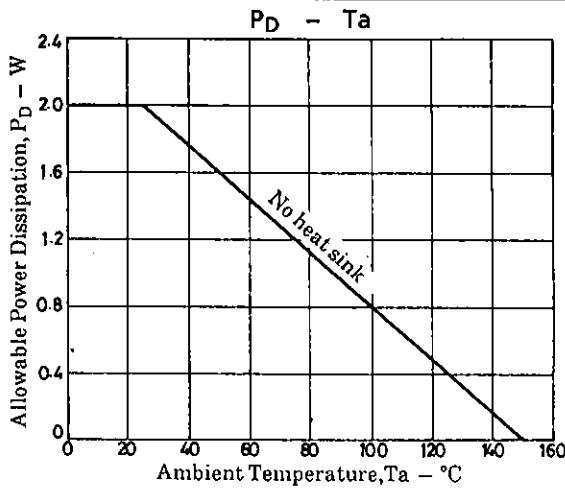


1: Gate
2: Drain
3: Source

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