

2SK973(L), 2SK973(S)

Silicon N-Channel MOS FET

Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- 4 V gate drive device
 - Can be driven from 5 V source
- Suitable for motor drive, DC-DC converter, power switch and solenoid drive

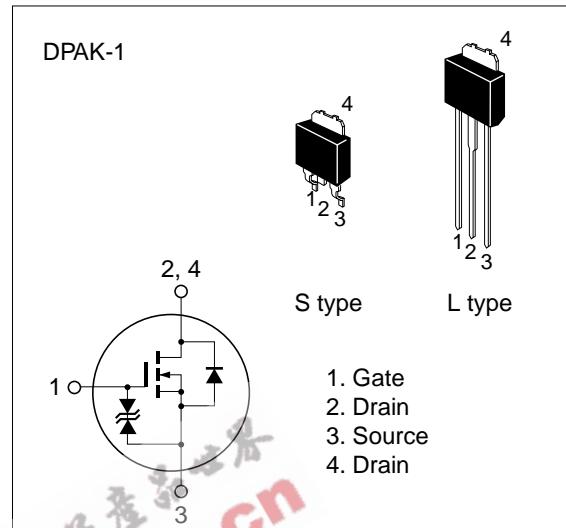


Table 1 Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	60	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	2	A
Drain peak current	I _{D(peak)*}	8	A
Body to drain diode reverse drain current	I _{DR}	2	A
Channel dissipation	P _{ch**}	10	W
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

* PW ≤ 10 µs, duty cycle ≤ 1 %

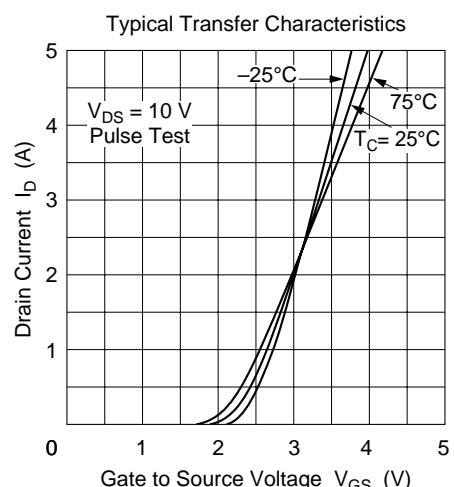
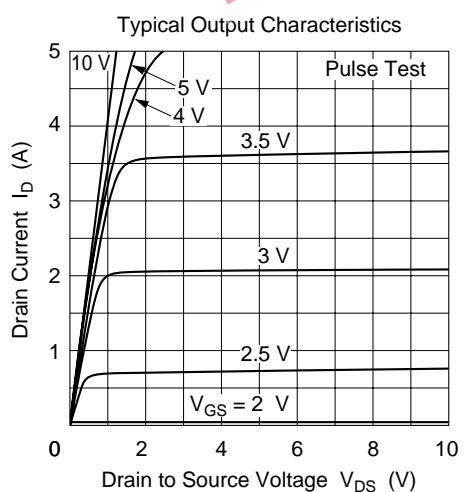
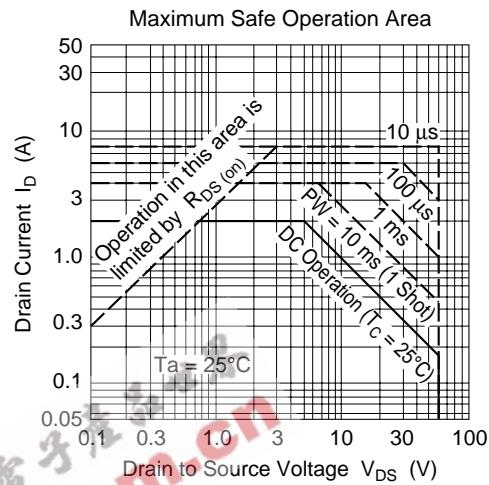
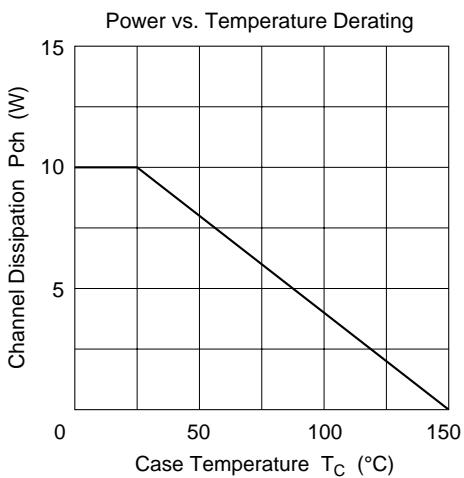
** Value at T_C = 25 °C

2SK973 (L) 2SK973 (S)

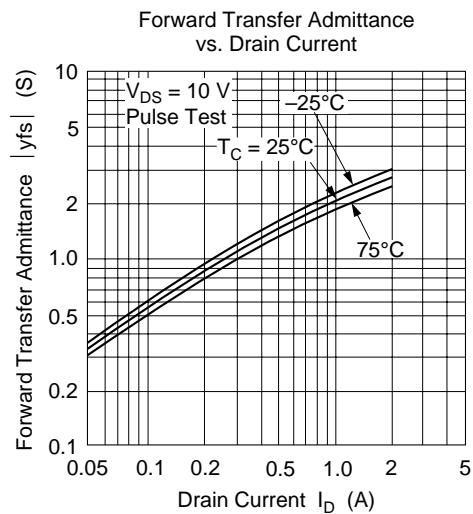
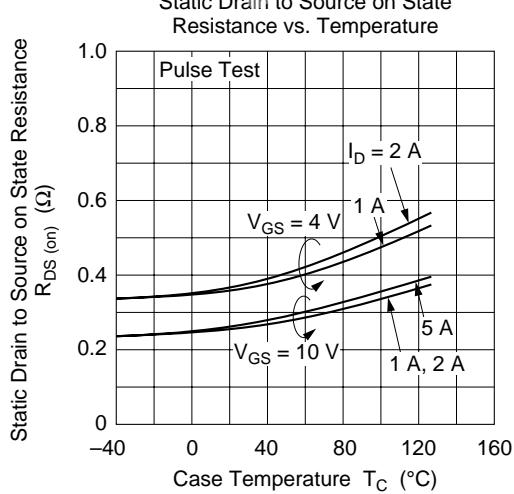
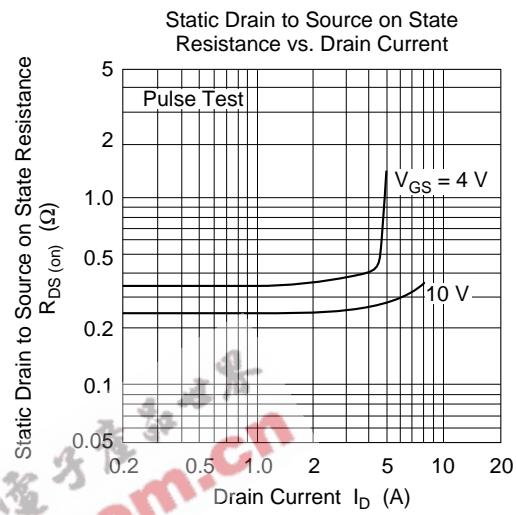
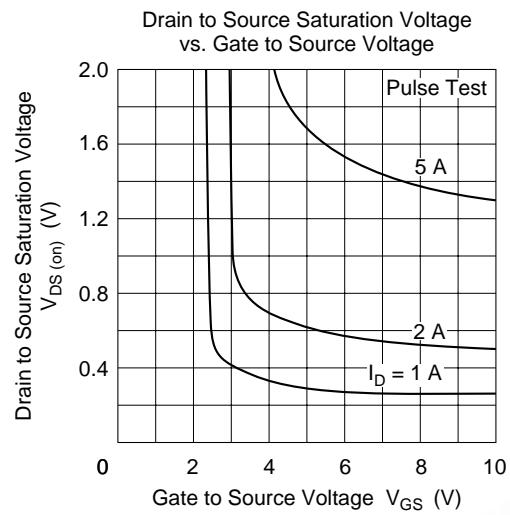
Table 2 Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	V _{(BR)DSS}	60	—	—	V	I _D = 10 mA, V _{GS} = 0
Gate to source breakdown voltage	V _{(BR)GSS}	±20	—	—	V	I _G = ±100 μA, V _{DS} = 0
Gate to source leak current	I _{GSS}	—	—	±10	μA	V _{GS} = ±16 V, V _{DS} = 0
Zero gate voltage drain current	I _{DSS}	—	—	100	μA	V _{DS} = 50 V, V _{GS} = 0
Gate to source cutoff voltage	V _{GS(off)}	1.0	—	2.0	V	I _D = 1 mA, V _{DS} = 10 V
Static drain to source on state resistance	R _{DS(on)}	—	0.25	0.35	Ω	I _D = 1 A, V _{GS} = 10 V *
			0.40	0.50		I _D = 1 A, V _{GS} = 4 V *
Forward transfer admittance	y _{fs}	1.2	2.0	—	S	I _D = 1 A, V _{DS} = 10 V *
Input capacitance	C _{iss}	—	240	—	pF	V _{DS} = 10 V, V _{GS} = 0,
Output capacitance	C _{oss}	—	115	—	pF	f = 1 MHz
Reverse transfer capacitance	C _{rss}	—	35	—	pF	
Turn-on delay time	t _{d(on)}	—	4	—	ns	I _D = 1 A, V _{GS} = 10 V,
Rise time	t _r	—	15	—	ns	R _L = 30 Ω
Turn-off delay time	t _{d(off)}	—	80	—	ns	
Fall time	t _f	—	40	—	ns	
Body to drain diode forward voltage	V _{DF}	—	1.0	—	V	I _F = 2 A, V _{GS} = 0
Body to drain diode reverse recovery time	t _{rr}	—	70	—	ns	I _F = 2 A, V _{GS} = 0, dI _F /dt = 50 A/μs

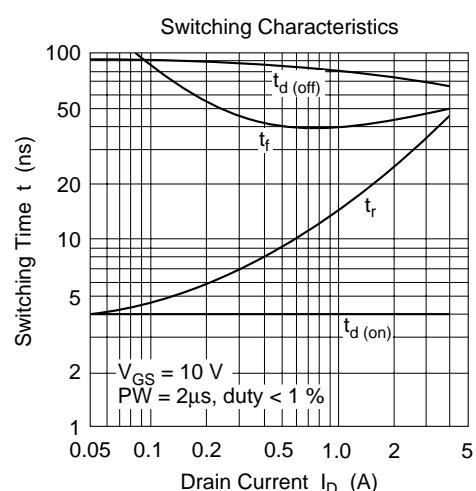
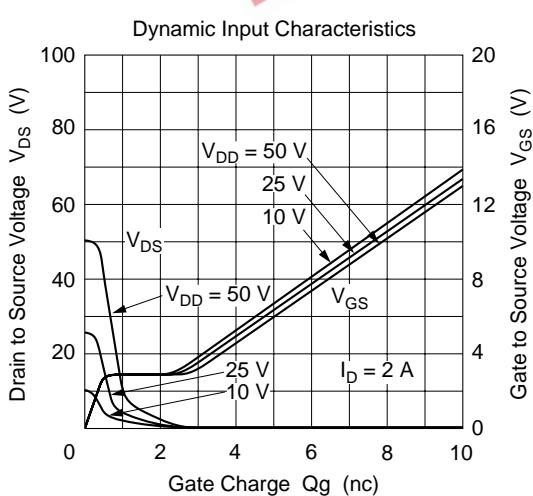
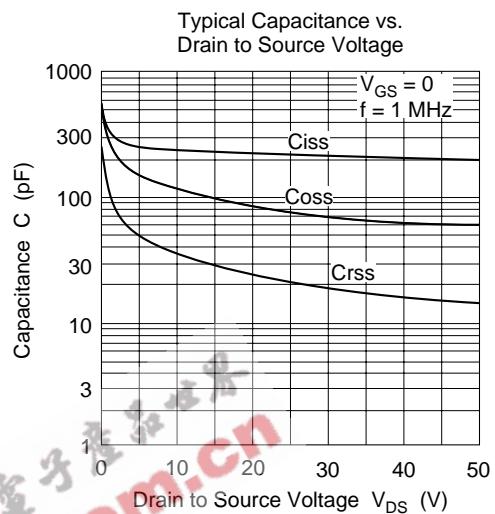
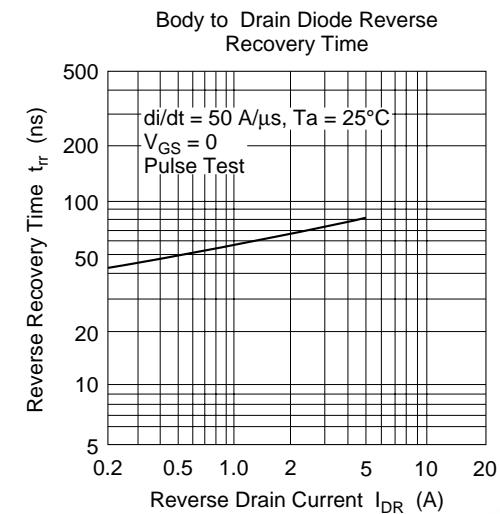
* Pulse Test



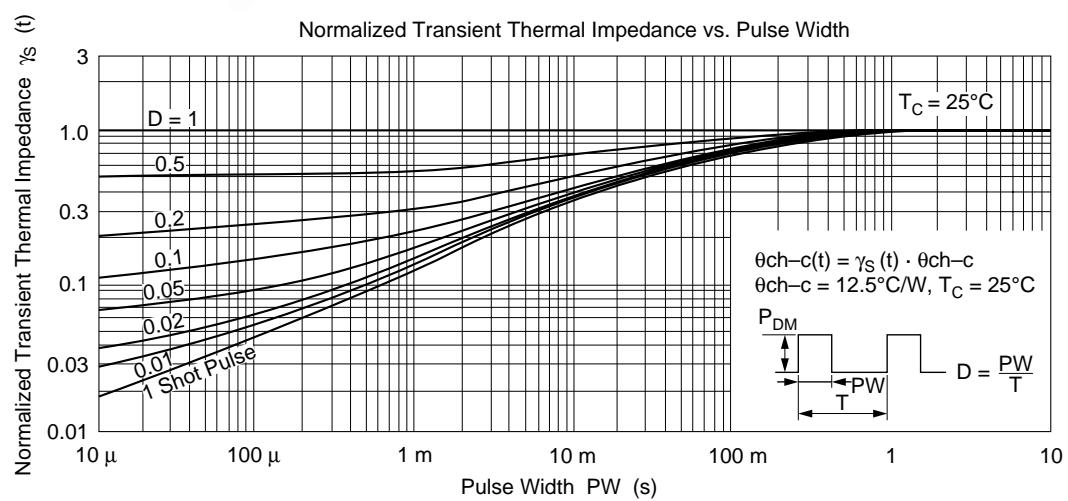
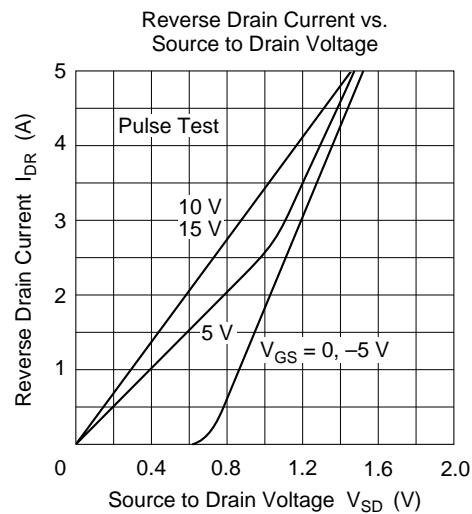
2SK973 L, 2SK973 S

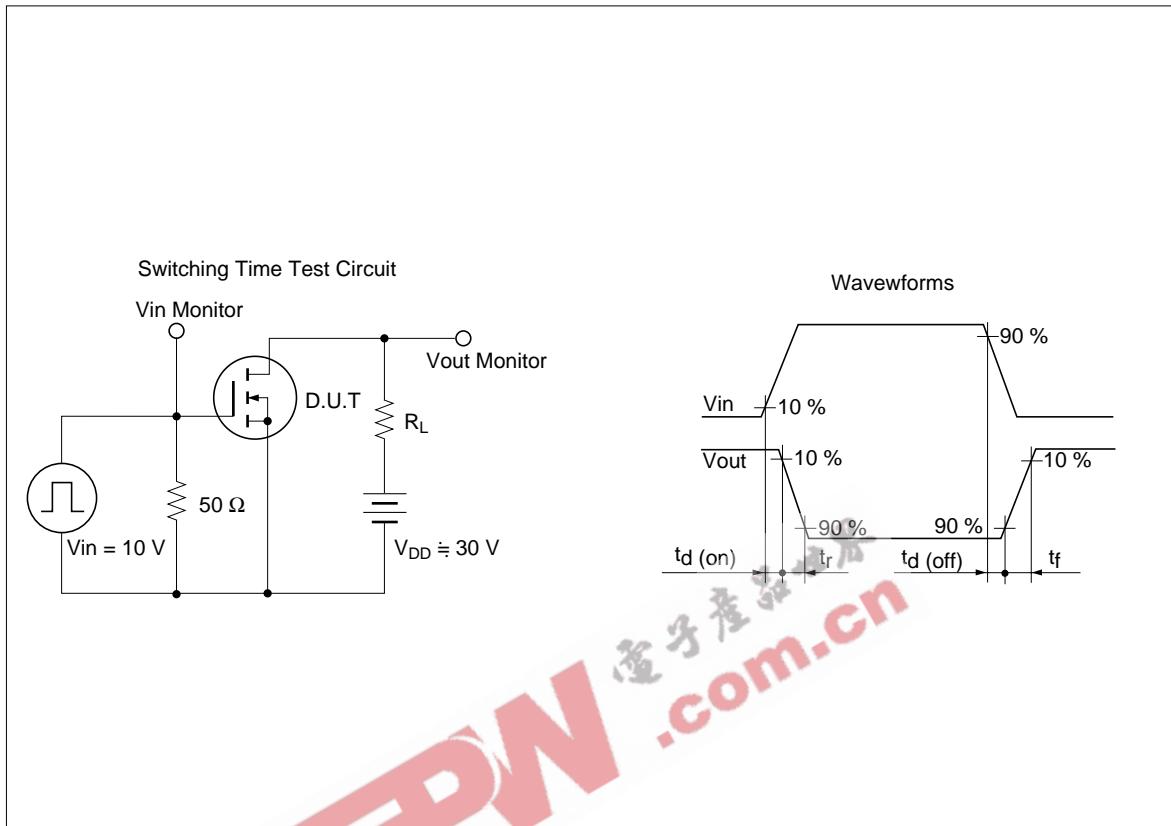


2SK973 L, 2SK973 S



2SK973 (L), 2SK973 (S)





EPRN 爱普生.com.cn