

No.3454

**2SK1451****SANYO**

N-Channel MOS Silicon FET

Very High-Speed Switching Applications

**Features**

- Low ON-state resistance.
- Very high-speed switching.
- Converters.
- Micaless package facilitating mounting.

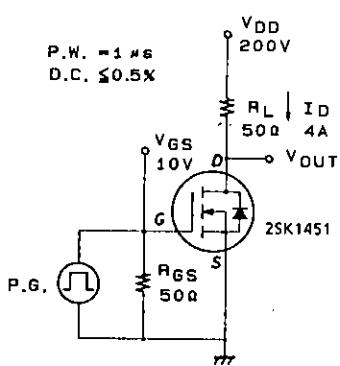
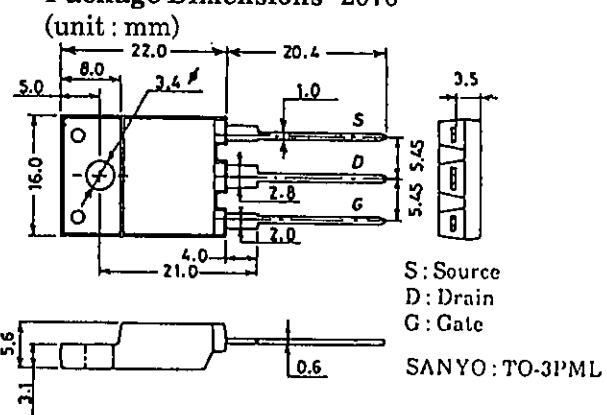
**Absolute Maximum Ratings at Ta=25°C**

		unit
Drain to Source Voltage	V <sub>DSS</sub>	450 V
Gate to Source Voltage	V <sub>GSS</sub>	±30 V
Drain Current(DC)	I <sub>D</sub>	8 A
Drain Current(Pulse)	I <sub>DP</sub>	PW≤10μs, duty cycle≤1% 32 A
Allowable Power Dissipation	P <sub>D</sub>	T <sub>c</sub> =25°C 50 W
		3.0 W
Channel Temperature	T <sub>ch</sub>	150 °C
Storage Temperature	T <sub>stg</sub>	-55 to +150 °C

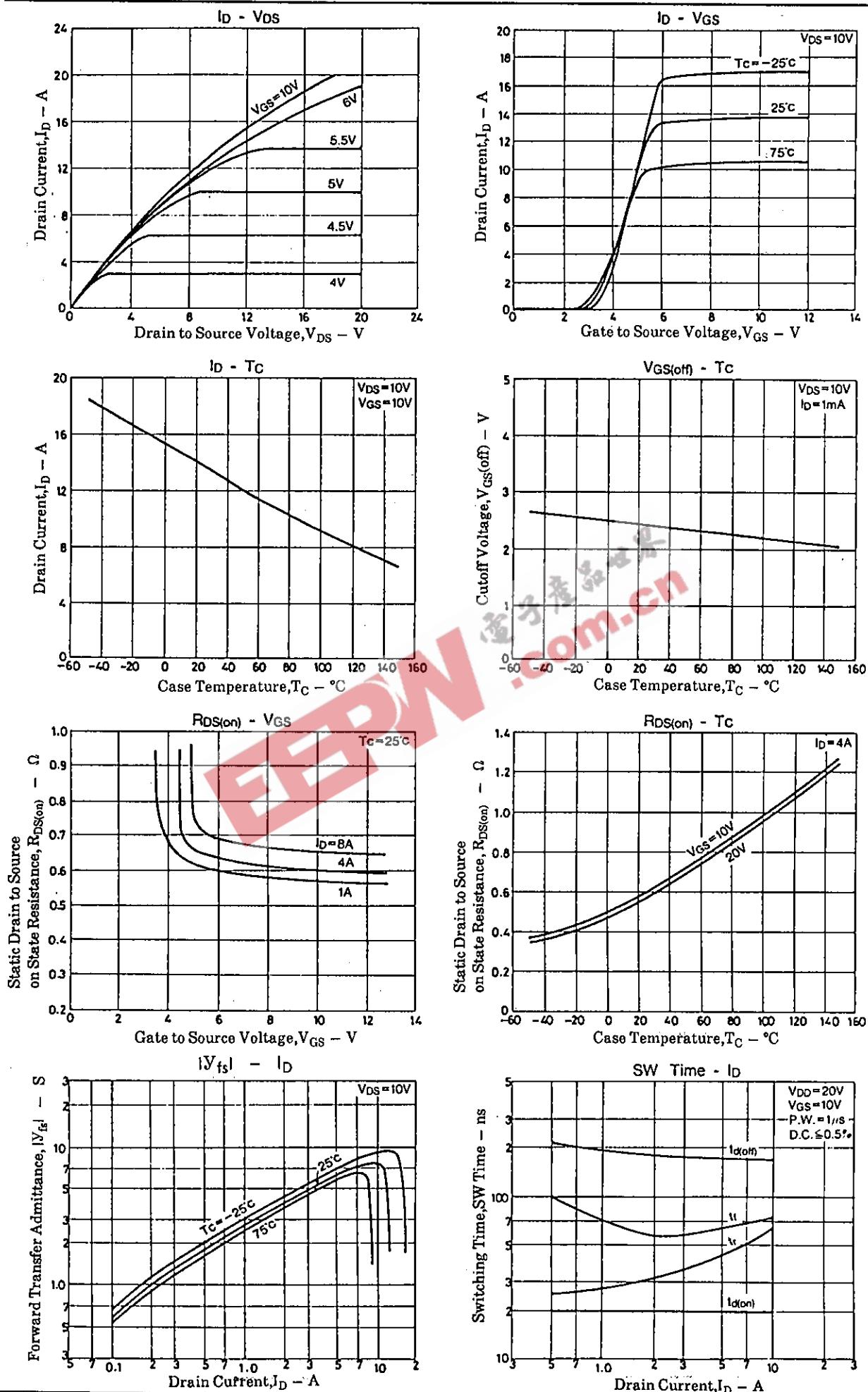
**Electrical Characteristics at Ta=25°C**

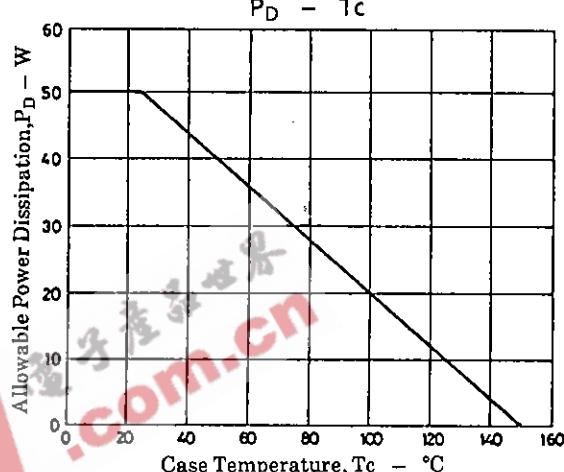
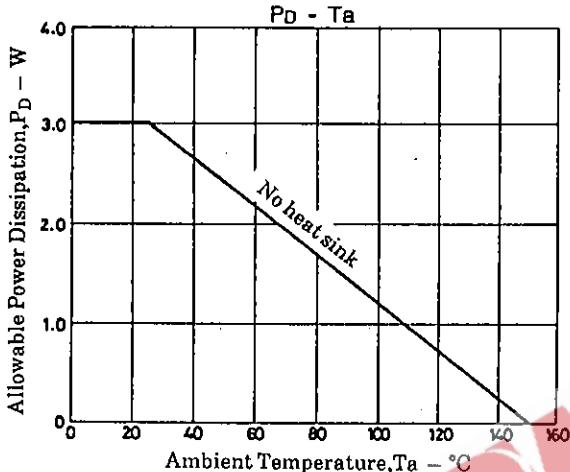
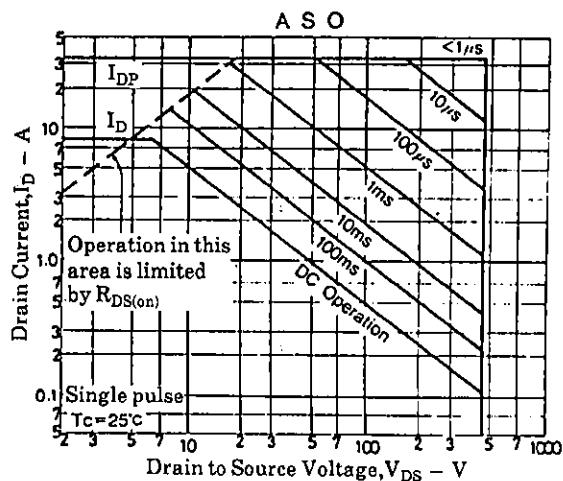
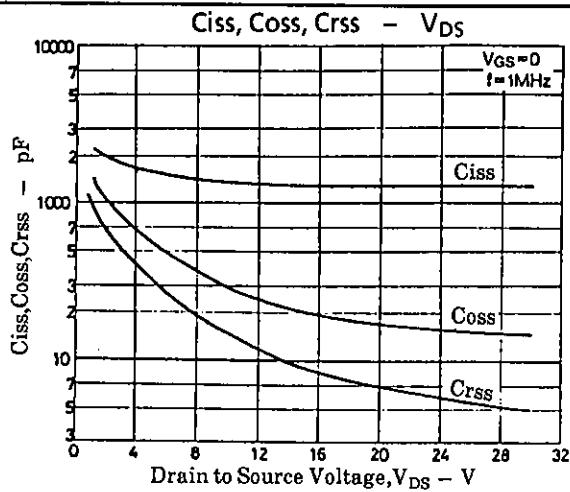
		min	typ	max	unit
D-S Breakdown Voltage	V <sub>(BR)DSS</sub>	450			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			1.0	mA
Gate to Source Leakage Current	I <sub>GSS</sub>			±100	nA
Cutoff Voltage	V <sub>GS(off)</sub>			2.0	V
Forward Transfer Admittance	Y <sub>fs</sub>			3.0	S
Static Drain to Source on State Resistance	R <sub>DS(on)</sub>			0.6	Ω
Input Capacitance	C <sub>iss</sub>			1200	pF
Output Capacitance	C <sub>oss</sub>			180	pF
Reverse Transfer Capacitance	C <sub>rss</sub>			70	pF
Turn-ON Delay Time	t <sub>d(on)</sub>			20	ns
Rise Time	t <sub>r</sub>			40	ns
Turn-OFF Delay Time	t <sub>d(off)</sub>			160	ns
Fall Time	t <sub>f</sub>			60	ns
Diode Forward Voltage	V <sub>SD</sub>			1.8	V

(Note) Be careful in handling the 2SK1451 because it has no protection diode between gate and source.

**Switching Time Test Circuit****Package Dimensions 2076**

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