SANYO

No.3827

N-Channel MOS Silicon FET Very High-Speed Switching Applications

Features

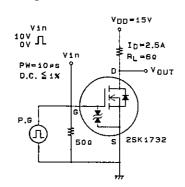
- · Low ON resistance.
- · Very high-speed switching.
- · Low-voltage drive.
- · Its height onboard is 9.5mm.
- · Meets radial taping.

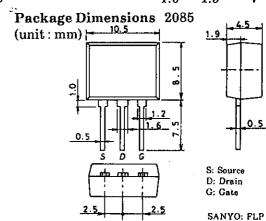
Absolute Maximum Ratings at	$Ta = 25^{\circ}C$		unit
Drain to Source Voltage	V_{DSS}	30	V
Gate to Source Voltage	${ m V_{GSS}}$	± 15	V
Drain Current(DC)	$I_{\mathbf{D}}$	4.5	Α
Drain Current(Pulse)	I_{DP}^-	$PW \le 10 \mu s$, duty cycle $\le 1\%$	Α
Allowable Power Dissipation	$P_{\mathbf{D}}$	1.5	W
Channel Temperature	Tch	4 150	$^{\circ}\mathrm{C}$
Storage Temperature	Tstg	-55 to +150	$^{\circ}\mathrm{C}$
		3. 3° - 1	

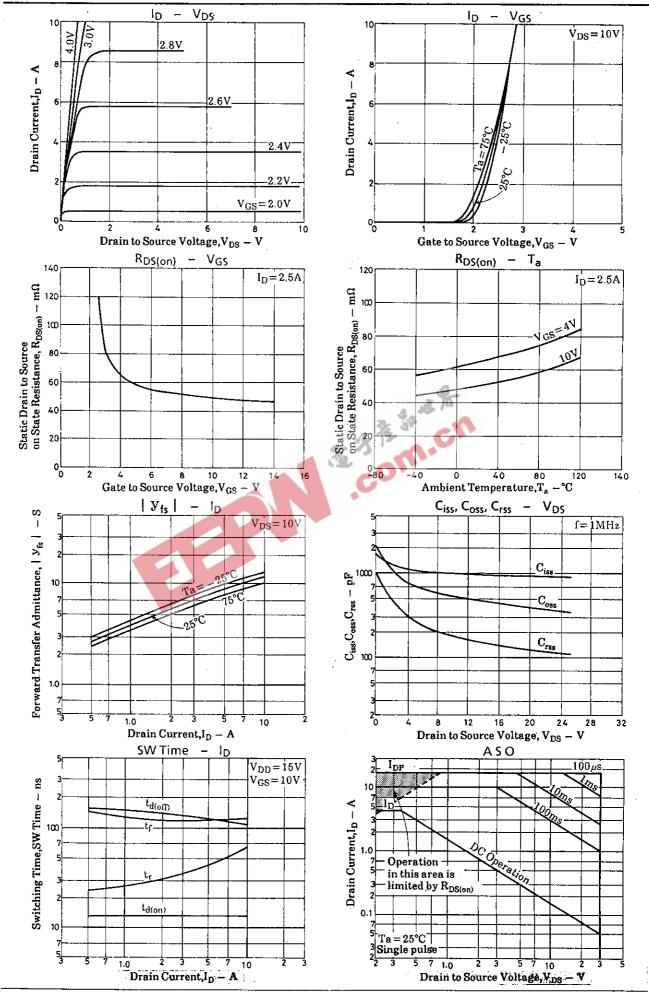
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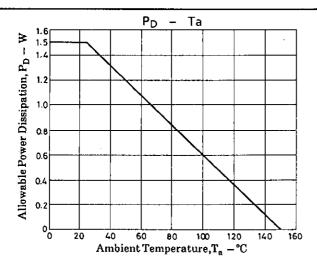
Electrical Characteristics at Ta=	25°C	2 13 6	min	typ	max	unit
D-S Breakdown Voltage	$V_{(BR)DSS}$	$I_D = 1 \text{mA}, V_{GS} = 0$ $I_D = \pm 100 \text{ mA}, V_{DS} = 0$	30	•		V
G-S Breakdown Voltage	V _{(BR)GSS}	$I_{G} = \pm 100 \mu A, V_{DS} = 0$	± 15			V
Zero Gate Voltage	I _{DSS}	$V_{DS} = 30V, V_{GS} = 0$			100	μ A
Drain Current						
Gate to Source Leakage Current	I _{GSS}	$V_{GS} = \pm 12V, V_{DS} = 0$			±10	μA
Cutoff Voltage	V _{GS(off)}	$V_{DS} = 10V, I_D = 1mA$	1.0		2.0	V
Forward Transfer Admittance	$ \mathbf{y}_{\mathrm{fs}} $	$V_{DS} = 10V, I_{D} = 2.5A$	4	6.5		S
Static Drain to Source	R _{DS(on)}	$I_{\rm D} = 2.5 {\rm A, V_{GS}} = 10 {\rm V}$		50	65	$\mathbf{m}\Omega$
on State Resistance	R _{DS(on)}	$I_D = 2.5A, V_{GS} = 4V$		65	85	${ m m}\Omega$
Input Capacitance	Ciss	$V_{DS} = 10V, f = 1MHz$		1000		рF
Output Capacitance	C_{oss}	$V_{DS} = 10V, f = 1MHz$		550		рF
Reverse Transfer Capacitance	\mathbf{C}_{rss}	$V_{DS} = 10V, f = 1MHz$		180		pF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.		13		ns
Rise Time	t_r	4		35		ns
Turn-OFF Delay Time	${ m t_{d(off)}}$	4		140		ns
Fall Time	t_f	4		120		ns
Diode Forward Voltage	V_{SD}	$I_S = 4.5A, V_{GS} = 0$		1.0	1.5	V

Switching Time Test Circuit











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