

2SK1620(L), 2SK1620(S)

Silicon N Channel MOS FET

REJ03G0957-0200
(Previous: ADE-208-1298)
Rev.2.00
Sep 07, 2005

Application

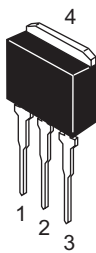
High speed power switching

Features

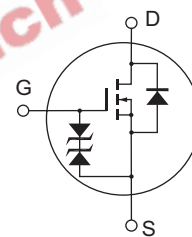
- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator, DC-DC converter and motor driver

Outline

RENESAS Package code: PRSS0004AE-A
(Package name: LDKPAK(L))



RENESAS Package code: PRSS0004AE-B
(Package name: LDKPAK(S)-(1))



1. Gate
2. Drain
3. Source
4. Drain

Absolute Maximum Ratings

(Ta = 25°C)

| Item | Symbol | Ratings | Unit |
|---|-------------------------------------|-------------|------|
| Drain to source voltage | V _{DSS} | 150 | V |
| Gate to source voltage | V _{GSS} | ±20 | V |
| Drain current | I _D | 10 | A |
| Drain peak current | I _{D(pulse)} ^{*1} | 40 | A |
| Body to drain diode reverse drain current | I _{DR} | 10 | A |
| Channel dissipation | P _{ch} ^{*2} | 50 | W |
| Channel temperature | T _{ch} | 150 | °C |
| Storage temperature | T _{stg} | -55 to +150 | °C |

Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1%
 2. Value at T_C = 25°C

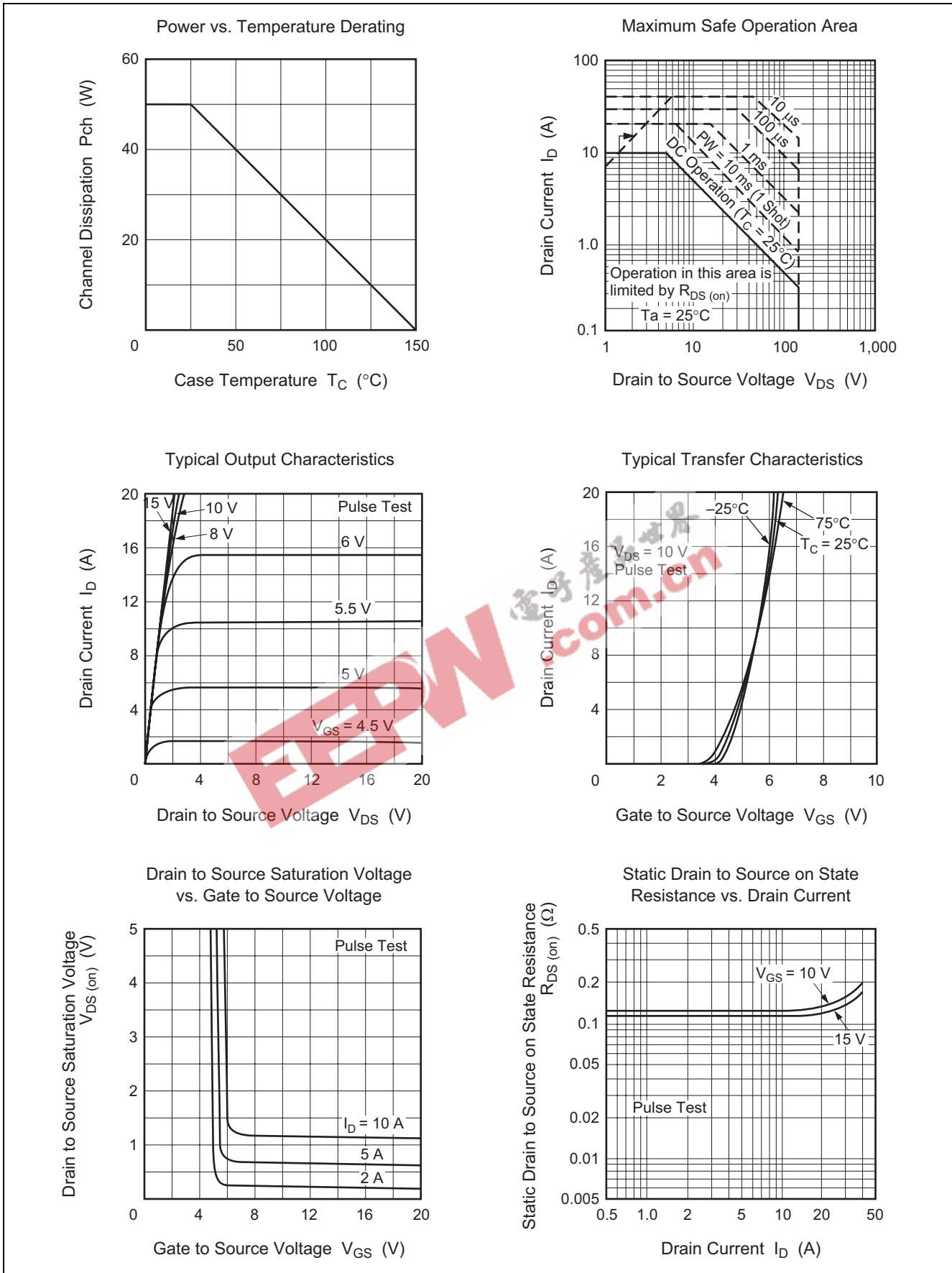
Electrical Characteristics

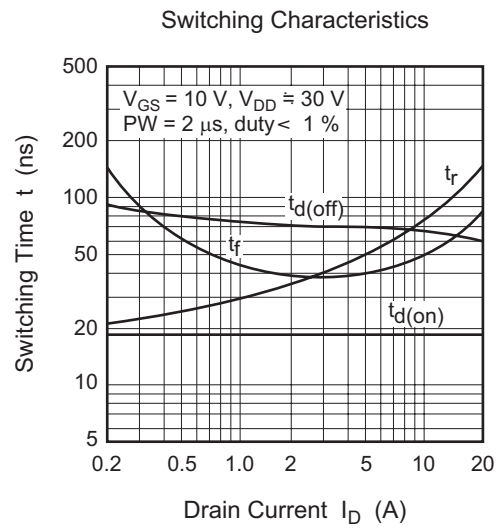
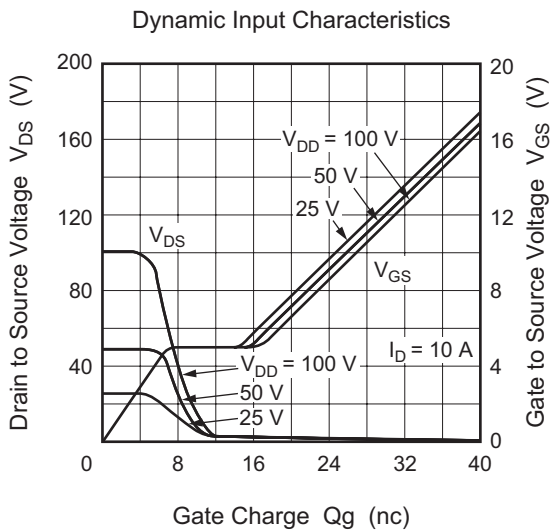
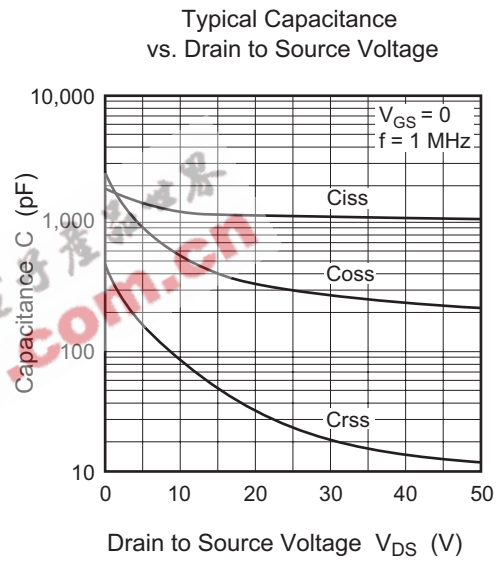
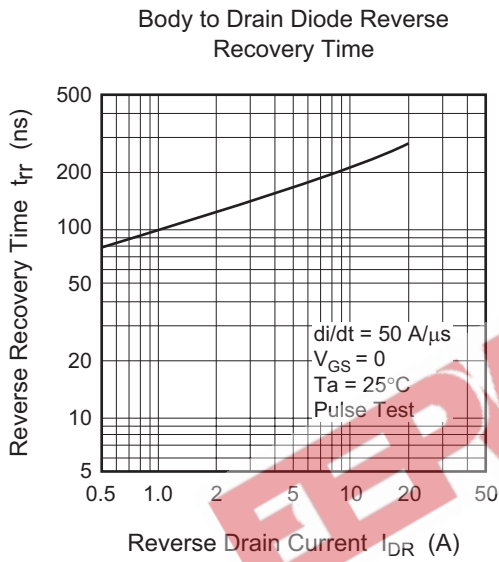
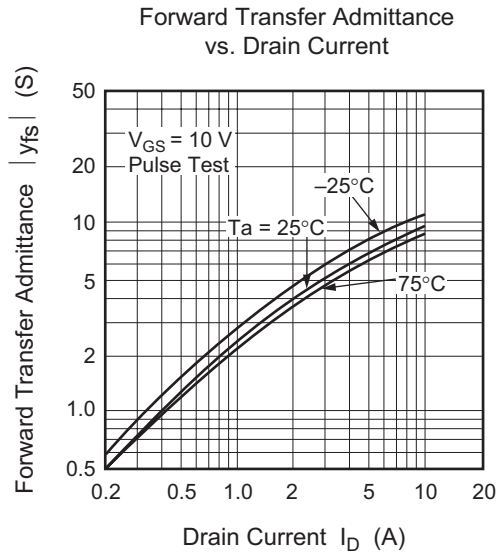
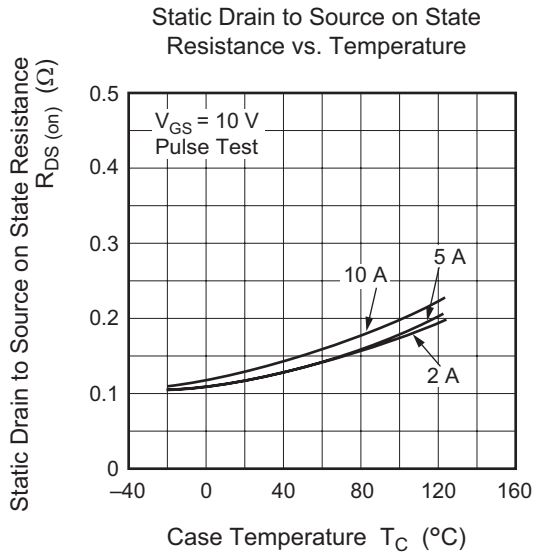
(Ta = 25°C)

| Item | Symbol | Min | Typ | Max | Unit | Test conditions |
|--|----------------------|-----|------|------|------|--|
| Drain to source breakdown voltage | V _{(BR)DSS} | 150 | — | — | V | I _D = 10 mA, V _{GS} = 0 |
| Gate to source breakdown voltage | V _{(BR)GSS} | ±20 | — | — | V | I _G = ±100 μA, V _{DS} = 0 |
| Gate to source leak current | I _{GSS} | — | — | ±10 | μA | V _{GS} = ±16 V, V _{DS} = 0 |
| Zero gate voltage drain current | I _{DSS} | — | — | 250 | μA | V _{DS} = 120 V, V _{GS} = 0 |
| Gate to source cutoff voltage | V _{GS(off)} | 2.0 | — | 4.0 | V | I _D = 1 mA, V _{DS} = 10 V |
| Static drain to source on state resistance | R _{DS(on)} | — | 0.12 | 0.15 | Ω | I _D = 5 A, V _{GS} = 10 V ^{*3} |
| Forward transfer admittance | y _{fs} | 4.0 | 7.0 | — | S | I _D = 5 A, V _{DS} = 10 V ^{*3} |
| Input capacitance | C _{iss} | — | 1200 | — | pF | V _{DS} = 10 V, V _{GS} = 0, f = 1 MHz |
| Output capacitance | C _{oss} | — | 550 | — | pF | |
| Reverse transfer capacitance | C _{rss} | — | 85 | — | pF | |
| Turn-on delay time | t _{d(on)} | — | 20 | — | ns | I _D = 5 A, V _{GS} = 10 V, R _L = 6 Ω |
| Rise time | t _r | — | 50 | — | ns | |
| Turn-off delay time | t _{d(off)} | — | 70 | — | ns | |
| Fall time | t _f | — | 40 | — | ns | |
| Body to drain diode forward voltage | V _{DF} | — | 1.2 | — | V | I _F = 10 A, V _{GS} = 0 |
| Body to drain diode reverse recovery time | t _{rr} | — | 220 | — | ns | I _F = 10 A, V _{GS} = 0, di _F /dt = 50 A/μs |

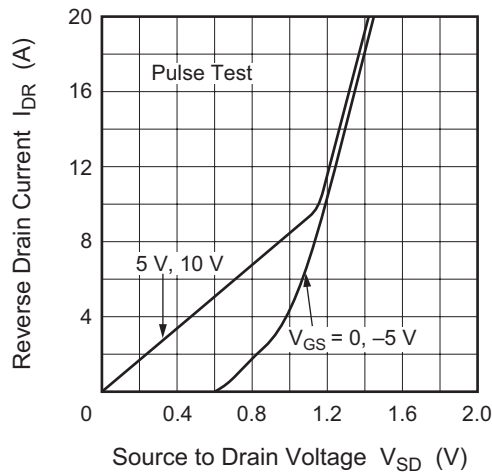
Note: 3. Pulse test

Main Characteristics

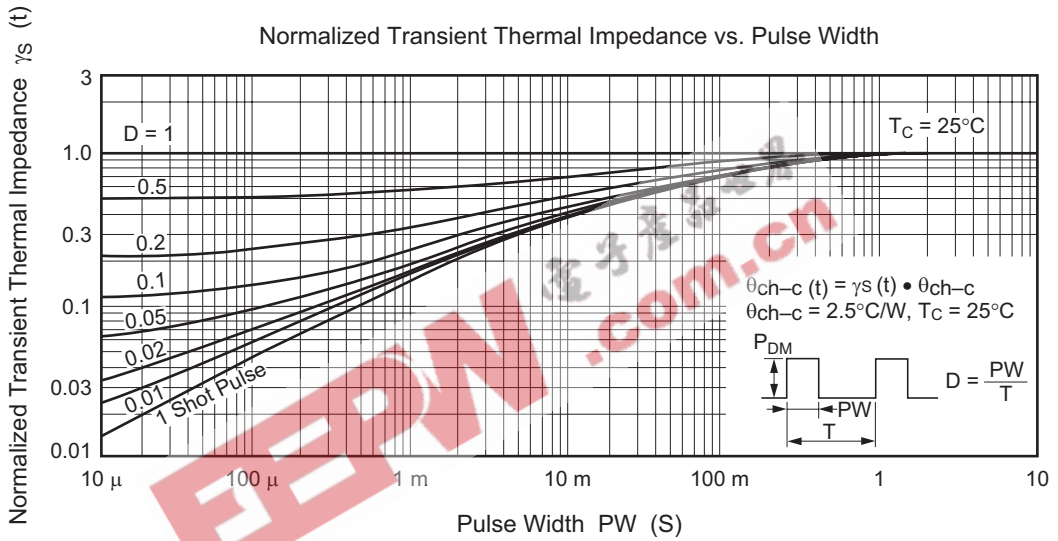




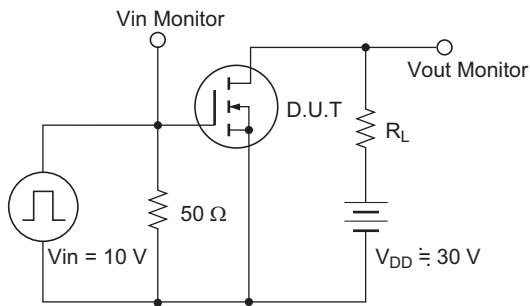
Reverse Drain Current vs. Source to Drain Voltage



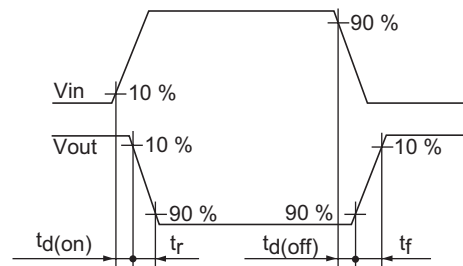
Normalized Transient Thermal Impedance vs. Pulse Width



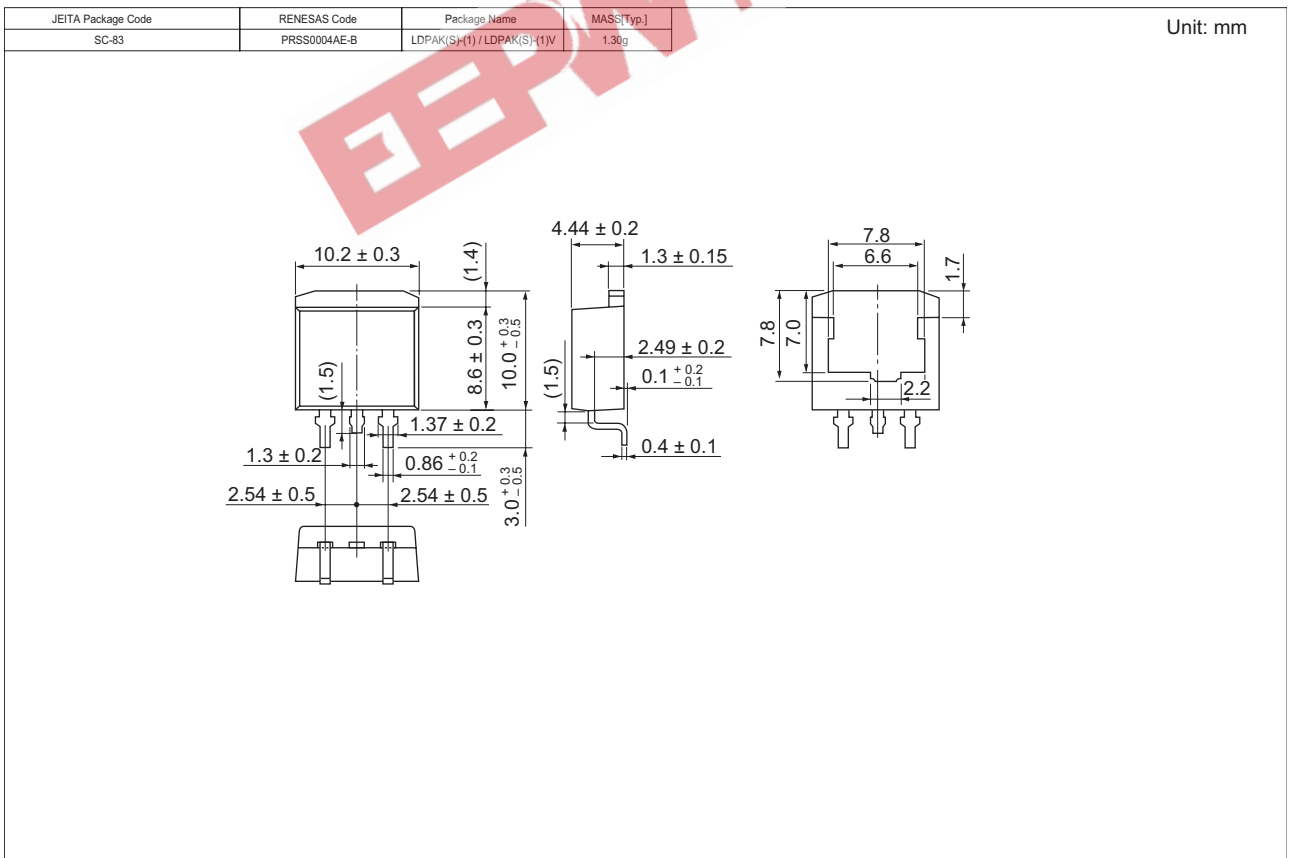
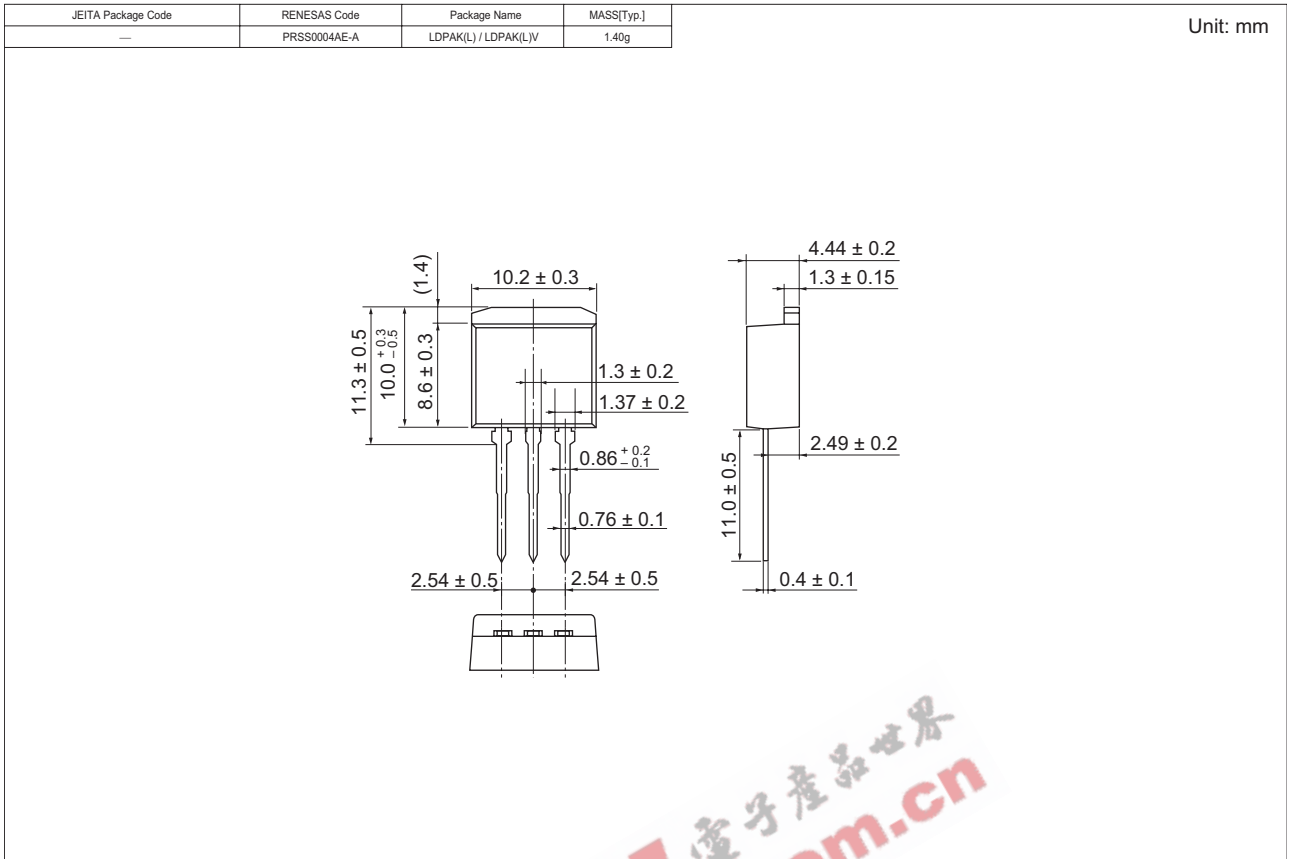
Switching Time Test Circuit



Waveforms



Package Dimensions



Ordering Information

| Part Name | Quantity | Shipping Container |
|--------------|----------|--------------------|
| 2SK1620L-E | 500 pcs | Box (Sack) |
| 2SK1620STL-E | 1000 pcs | Taping |

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.



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