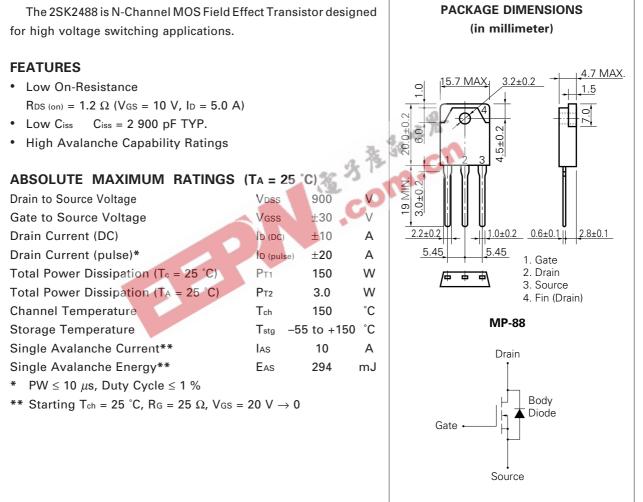
DATA SHEET



MOS FIELD EFFECT TRANSISTOR **2SK2488**

SWITCHING N-CHANNEL POWER MOS FET INDUSTRIAL USE

DESCRIPTION

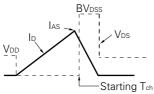


ELECTRICAL	CHARACTERISTICS	(Ta = 25	°C)	

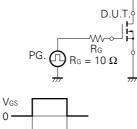
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain to Source On-Resistance	RDS (on)		1.0	1.2	Ω	$V_{GS} = 10 \text{ V}, \text{ Id} = 5.0 \text{ A}$
Gate to Source Cutoff Voltage	VGS (off)	2.5		3.5	V	$V_{DS} = 10 V, I_{D} = 1 mA$
Forward Transfer Admittance	y _{fs}	3.5			S	$V_{DS} = 20 V, I_{D} = 5.0 A$
Drain Leakage Current	IDSS			100	μΑ	$V_{DS} = V_{DSS}, V_{GS} = 0$
Gate to Source Leakage Current	lgss			±100	nA	$V_{GS} = \pm 30 \text{ V}, \text{ V}_{DS} = 0$
Input Capacitance	Ciss		2 900		pF	$V_{DS} = 10 V$
Output Capacitance	Coss		400		pF	Vgs = 0
Reverse Transfer Capacitance	Crss		70		pF	f = 1 MHz
Turn-On Delay Time	td (on)		35		ns	ID = 5.0 A
Rise Time	tr		30		ns	$V_{GS} = 10 V$
Turn-Off Delay Time	td (off)		160		ns	Vdd = 150 V
Fall Time	tr		32		ns	$R_{G} = 10 \Omega$
Total Gate Charge	Q _G		90		nC	ID = 10 A
Gate to Source Charge	Q _{GS}		16		nC	V _{DD} = 450 V
Gate to Drain Charge	Qgd		40	れち	nC	Vgs = 10 V
Body Diode Forward Voltage	VF (S-D)		1.0	3	V	IF = 10 A, VGS = 0
Reverse Recovery Time	trr		9 90	00	ns	IF = 10 A, VGS = 0
Reverse Recovery Charge	Qrr	<u> </u>	7.0		μC	di/dt = 50 A/µs

Test Circuit 1 Avalanche Capability

D.U.T. $R_G = 25 \Omega$ PG Vgs = 20 - 0 V $\mathop{\gtrless}_{|} 50\ \Omega$ (J) Vdd



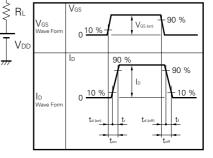
Test Circuit 2 Switching Time



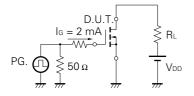
t

t = 1 us Duty Cycle \leq 1 %

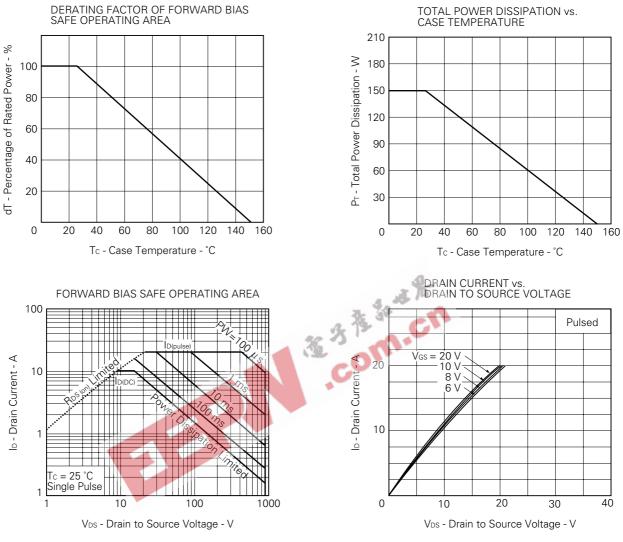
0-



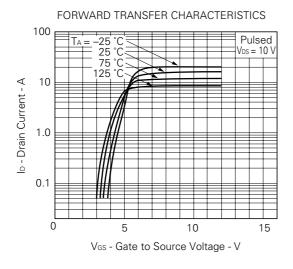
Test Circuit 3 Gate Charge

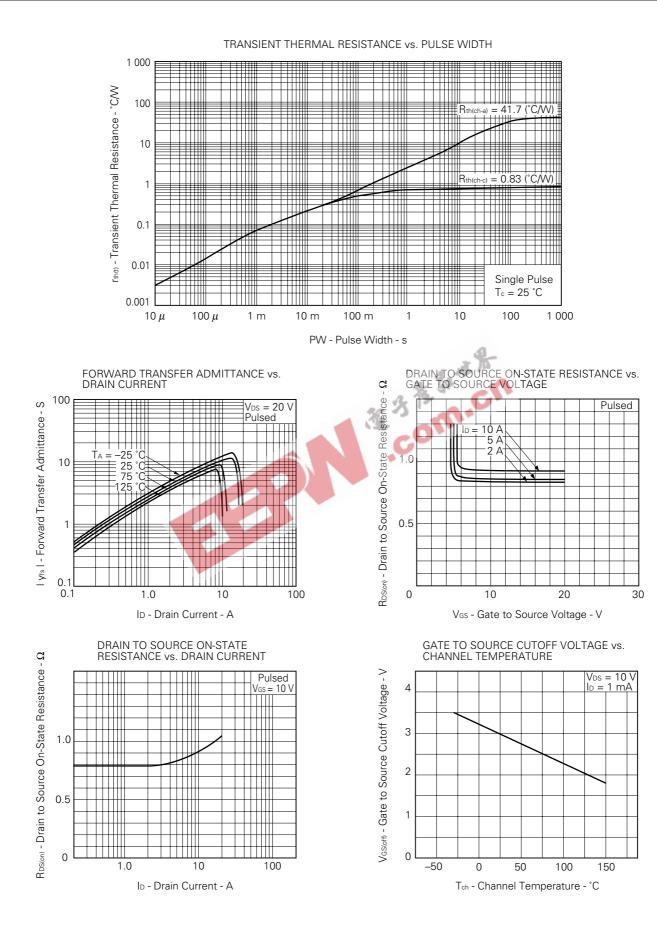


The application circuits and their parameters are for references only and are not intended for use in actual design-in's.



TYPICAL CHARACTERISTICS (TA = 25 °C)



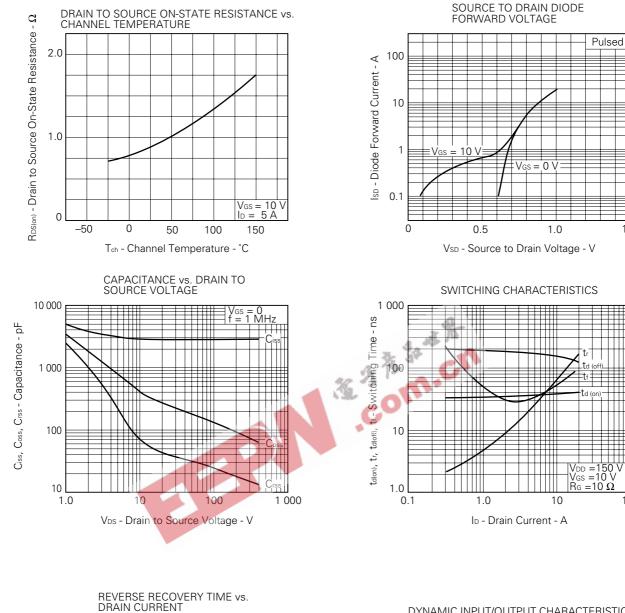


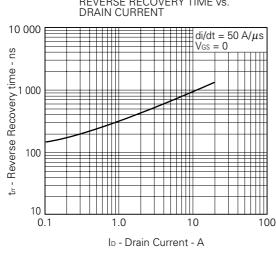
NEC

1.5

50

100

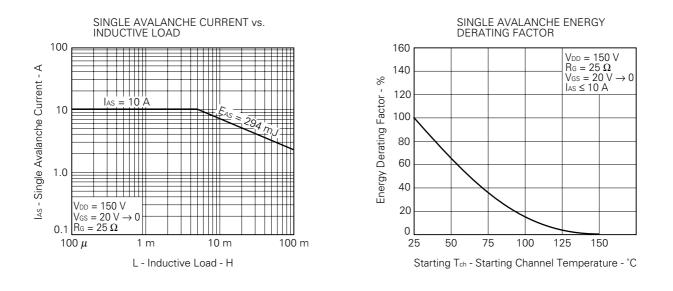




NEC

DYNAMIC INPUT/OUTPUT CHARACTERISTICS 800 16 ID = 10 A V_{Ds} - Drain to Source Voltage - V 14 V_{GS} - Gate to Source Voltage - V 600 12 $V_{DD} = 450 \text{ V}$ VGS 300 V 10 150 V 400 8 6 200 4 2 γ̈́DS _____0 120 0 30 60 90 Qg - Gate Charge - nC

5





REFERENCE

Document Name	Document No.
NEC semiconductor device reliability/quality control system.	TEI-1202
Quality grade on NEC semiconductor devices.	IEI-1209
Semiconductor device mounting technology manual.	IEI-1207
Semiconductor device package manual.	IEI-1213
Guide to quality assurance for semiconductor devices.	MEI-1202
Semiconductor selection guide.	MF-1134
Power MOS FET features and application switching power supply.	TEA-1034
Application circuits using Power MOS FET.	TEA-1035
Safe operating area of Power MOS FET.	TEA-1037



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Specific: Aircrafts, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems or medical equipment for life support, etc.

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Anti-radioactive design is not implemented in this product.

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