

54F/74F10 Triple 3-Input NAND Gate

General Description

This device contains three independent gates, each of which performs the logic NAND function.

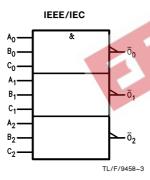
Commercial	Military	Package Number	Package Description		
74F10PC		N14A	14-Lead (0.300" Wide) Molded Dual-In-Line		
	54F10DM (Note 2)	J14A	14-Lead Ceramic Dual-In-Line		
74F10SC (Note 1)		M14A	14-Lead (0.150" Wide) Molded Small Outline, JEDEC		
74F10SJ (Note 1)		M14D	14-Lead (0.300" Wide) Molded Small Outline, EIAJ		
	54F10FM (Note 2)	W14B	14-Lead Cerpack		
	54F10LM (Note 2)	E20A	20-Lead Ceramic Leadless Chip Carrier, Type C		

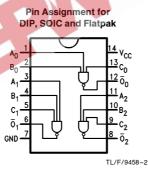
Note 1: Devices also available in 13" reel. Use suffix = SCX and SJX.

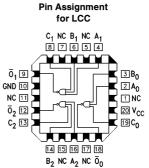
Note 2: Military grade device with environmental and burn-in processing. Use suffix = DMQB, FMQB and LMQB

Logic Symbol

Connection Diagrams







TL/F/9458-1

Unit Loading/Fan Out

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		54F/74F			
Pin Names	Description	U.L. HIGH/LOW	Input I _{IH} /I _{IL} Output I _{OH} /I _{OL}		
A_n, B_n, C_n \overline{O}_n	Inputs Outputs	1.0/1.0 50/33.3	20 μA/ - 0.6 mA - 1 mA/20 mA		

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Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Storage Temperature -65°C to +150°C Ambient Temperature under Bias -55°C to $+125^{\circ}\text{C}$ -55°C to +175°C Junction Temperature under Bias -55°C to $+150^{\circ}\text{C}$ Plastic

V_{CC} Pin Potential to Ground Pin -0.5V to +7.0VInput Voltage (Note 2) -0.5V to +7.0VInput Current (Note 2) $-30\ \text{mA}$ to $+5.0\ \text{mA}$

Voltage Applied to Output in HIGH State (with V_{CC} = 0V)

 $-0.5\mbox{V to V}_{CC} \\ -0.5\mbox{V to } +5.5\mbox{V}$ Standard Output TRI-STATE® Output

Current Applied to Output

in LOW State (Max) twice the rated I_{OL} (mA)

Note 1: Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Note 2: Either voltage limit or current limit is sufficient to protect inputs.

Recommended Operating Conditions

Free Air Ambient Temperature

Military -55°C to +125°C Commercial $0^{\circ}C$ to $\,+\,70^{\circ}C$

Supply Voltage

 $+\,4.5V$ to $+\,5.5V$ Military Commercial +4.5V to +5.5V

DC Electrical Characteristics

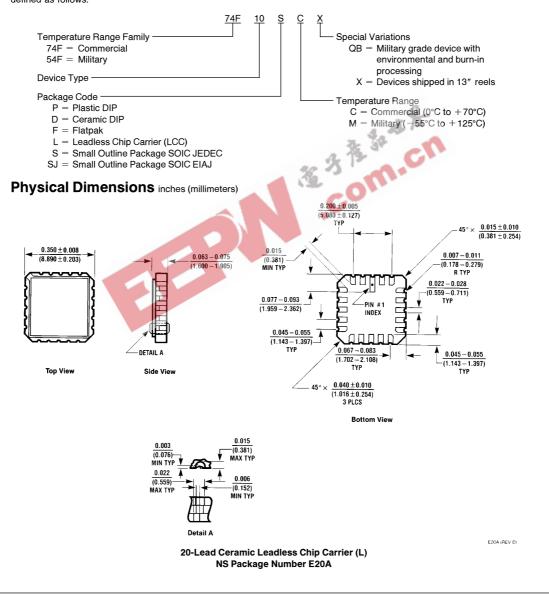
	Electrical Characteristics		54F/74F				10000	A The same of the	
Symbol	Parame	rameter		Min Typ		Units	Vcc	Conditions	
V _{IH}	Input HIGH Voltage		2.0			V		Recognized as a HIGH Signal	
V _{IL}	Input LOW Voltage				0.8	V		Recognized as a LOW Signal	
V_{CD}	Input Clamp Diode Voltage			₹ <u>1</u>	-1.2	V	Min	$I_{\text{IN}} = -18 \text{ mA}$	
V _{OH}	Output HIGH Voltage	54F 10% V _{CC} 74F 10% V _{CC} 74F 5% V _{CC}	2.5 2.5 2.7			٧	Min	$I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$	
V _{OL}	Output LOW Voltage	54F 10% V _{CC} 74F 10% V _{CC}			0.5 0.5	٧	Min	$I_{OL} = 20 \text{ mA}$ $I_{OL} = 20 \text{ mA}$	
I _{IH}	Input HIGH Current	54F 74F			20.0 5.0	μΑ	Max	V _{IN} = 2.7V	
I _{BVI}	Input HIGH Current Breakdown Test	54F 74F			100 7.0	μΑ	Max	V _{IN} = 7.0V	
I _{CEX}	Output HIGH Leakage Current	54F 74F			250 50	μΑ	Max	$V_{OUT} = V_{CC}$	
V _{ID}	Input Leakage Test	74F	4.75			V	0.0	$I_{\text{ID}} = 1.9 \mu\text{A}$ All other pins grounded	
I _{OD}	Output Leakage Circuit Current	74F			3.75	μΑ	0.0	V _{IOD} = 150 mV All other pins grounded	
I _{IL}	Input LOW Current				-0.6	mA	Max	$V_{IN} = 0.5V$	
los	Output Short-Circuit Current				-150	mA	Max	$V_{OUT} = 0V$	
I _{CCH}	Power Supply Current			1.4	2.1	mA	Max	V _O = HIGH	
I _{CCL}	Power Supply Current	t		5.1	7.7	mA	Max	V _O = LOW	

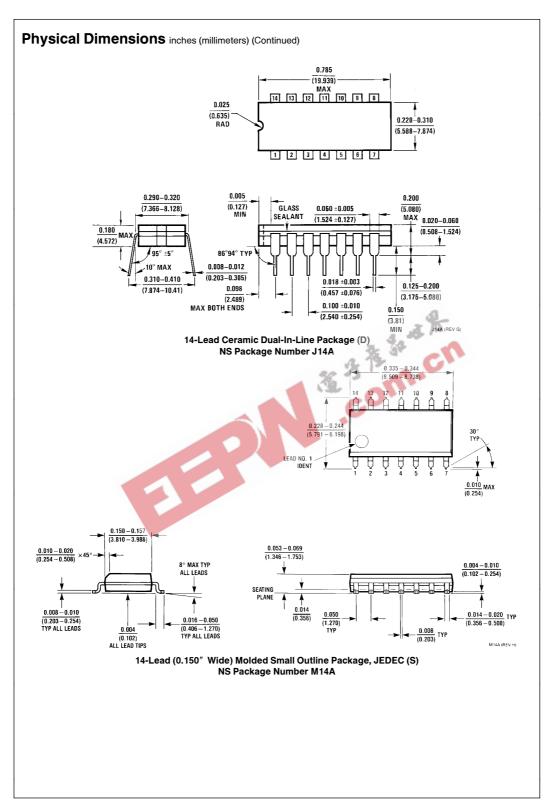
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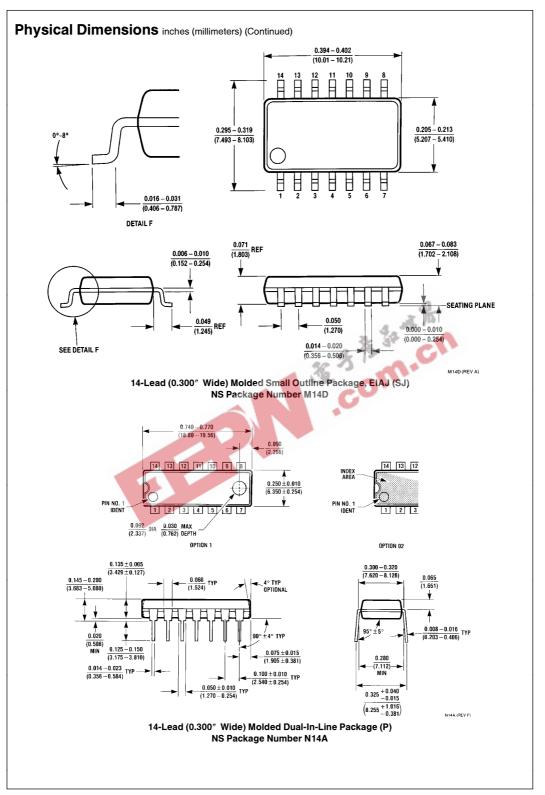
		74F			5-	4F	74F		
Symbol Parameter		$\begin{aligned} \textbf{T}_{\textbf{A}} &= +25^{\circ}\textbf{C} \\ \textbf{V}_{\textbf{CC}} &= +5.0\textbf{V} \\ \textbf{C}_{\textbf{L}} &= 50~\textbf{pF} \end{aligned}$			$ extsf{T}_{ extsf{A}}, extsf{V}_{ extsf{CC}} = extsf{Mil} \ extsf{C}_{ extsf{L}} = extsf{50 pF}$		extstyle ext		Units
		Min	Тур	Max	Min	Max	Min	Max	
t _{PLH}	Propagation Delay	2.4	3.7	5.0	2.0	7.0	2.4	6.0	no
t _{PHL}	A_n , B_n , C_n to \overline{O}_n	1.5	3.2	4.3	1.5	6.5	1.5	5.3	ns

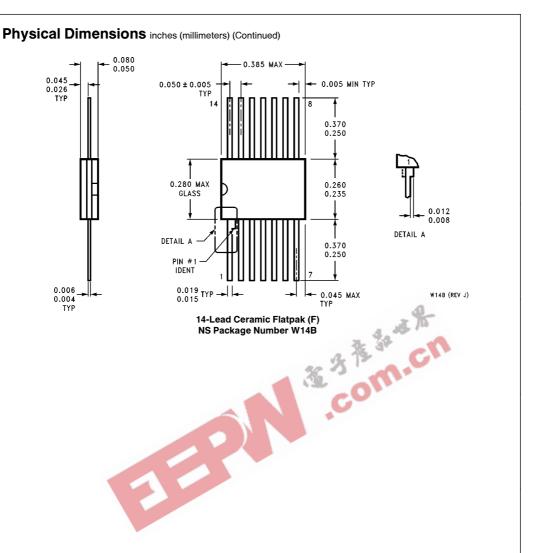
Ordering Information

The device number is used to form part of a simplified purchasing code where the package type and temperature range are defined as follows:









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