



3.3V HCMOS CERAMIC SMD OSCILLATOR WITH STANDBY MODEL: F4200 SERIES



FEATURES

- 3.3V Operation
- HCMOS Output
- Standby Function
- Tape and Reel (2,000 pcs. STD)

OPTIONS

- 5.0V (F3400) Version Available

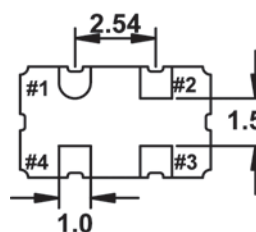
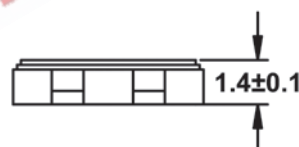
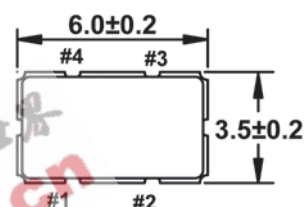


Learn more about:
[Part Marking Identification](#)
[Tape and Reel Specification](#)
[Mechanical Specification](#)

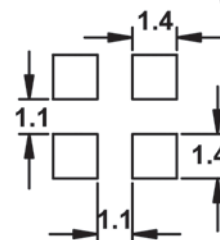
Internet required

• PART NUMBER SELECTION Learn More - Internet Required

Part Number	Model Number	Frequency Stability ¹	Operating Temperature (°C)	Frequency Range (MHz)
129-Frequency-xxxxx	F4200	±100PPM	-10 ~ +70	1.544 ~ 50.000
130-Frequency-xxxxx	F4200R	±100PPM	-40 ~ +85	1.544 ~ 50.000
131-Frequency-xxxxx	F4205	±50PPM	-10 ~ +70	1.544 ~ 50.000
132-Frequency-xxxxx	F4205R	±50PPM	-40 ~ +85	1.544 ~ 50.000
133-Frequency-xxxxx	F4206	±25PPM	-10 ~ +70	1.544 ~ 50.000
506-Frequency-xxxxx	F4206R	±25PPM*	-40 ~ +85	1.544 ~ 50.000
134-Frequency-xxxxx	F4208	±20PPM*	-10 ~ +70	1.544 ~ 50.000



Recommended Solder Pad Layout



Pin Connections

- #1 E/D #3 Output
#2 GND #4 V_{DD}

All dimensions are in millimeters.

• ELECTRICAL CHARACTERISTICS

PARAMETERS	MAX (unless otherwise noted)
Frequency Range (F _o)	1.544 ~ 50.000 MHz
Storage Temperature Range (T _{STG})	-55°C ~ +125°C
Supply Voltage (V _{DD})	3.3V ± 10%
Input Current (I _{DD})	20mA
Output Symmetry (50% V _{DD})	40% ~ 60%
Rise Time (10% ~ 90% V _{DD}) (T _R)	6nS
Fall Time (90% ~ 10% V _{DD}) (T _F)	6nS
Output Voltage (V _{OL})	10% V _{DD}
(V _{OH})	90% V _{DD} Min
Output Current (I _{OL})	2mA Min
(I _{OH})	-2mA Min
Output Load (HCMOS)	15pF
Standby Current	10μA
Start-up Time (T _s)	10mS
Output Disable Time ²	150nS
Output Enable Time ²	10mS

¹ Inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock, and vibration. *Excludes Shock/Vibration

² An internal pullup resistor from pin 1 to pin 4 allows active output if pin 1 is left open.

All specifications subject to change without notice. Rev. 6/1/04

• ENABLE / DISABLE FUNCTION

INH (Pin 1)	OUTPUT (Pin 3)
OPEN ²	ACTIVE
'1' Level V _{IH} ≥ 70% V _{DD}	ACTIVE
'0' Level V _{IL} ≤ 30% V _{DD}	High Z