



 SUMITOMO ELECTRIC

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#### ◆ **Features**

- Up to 1.3 Gb/s high speed operation
- 3.3 V single power supply
- Up to 35 mA p-p modulation current
- Up to 35 mA bias current
- Maximum bias current preset control

#### ◆ **Applications**

- Laser diode driver of an optical transmitter circuit for SDH (STM4) / SONET (OC-12)

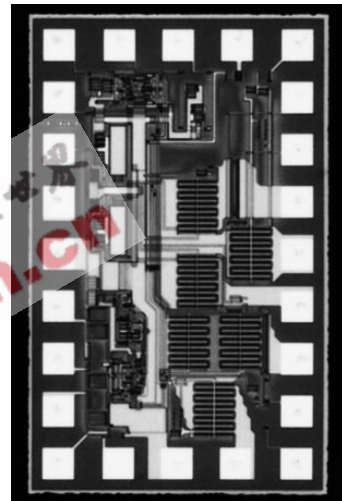
#### ◆ **Functional Description**

The F0530601B is a high performance GaAs integrated laser diode driver for use in an optical transmitter circuit up to 1.3 Gb/s NRZ data rate. The F0530601B typically specifies rise time and fall time of 300 psec (20 % - 80 %, 25  $\Omega$  load). It features a low power 3.3 V supply operation, 1 to 35 mA presettable bias current and up to 35 mA modulation current.

**F0530602B**

**3.3V Operation**

**Laser Diode Driver**



### ◆ Absolute Maximum Ratings

$T_a=25\text{ }^\circ\text{C}$ , unless specified

Parameter	Symbol	Value	Units
Supply Voltage	$V_{DD}, V_{SS}$	- 0.2 to 4.0	V
Supply Current	$I_{ckt}$	150	mA
Modulation Current	$I_{out}$	70	mA
Bias Current	$I_{outbi}$	70	mA
Input Voltage	$V_{in}$	$V_{SS}$ to $V_{DD}+0.5$	V
Junction Operating Temperature	$T_j$	0 to +140	$^\circ\text{C}$
Storage Temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

### ◆ Recommended Operating Conditions

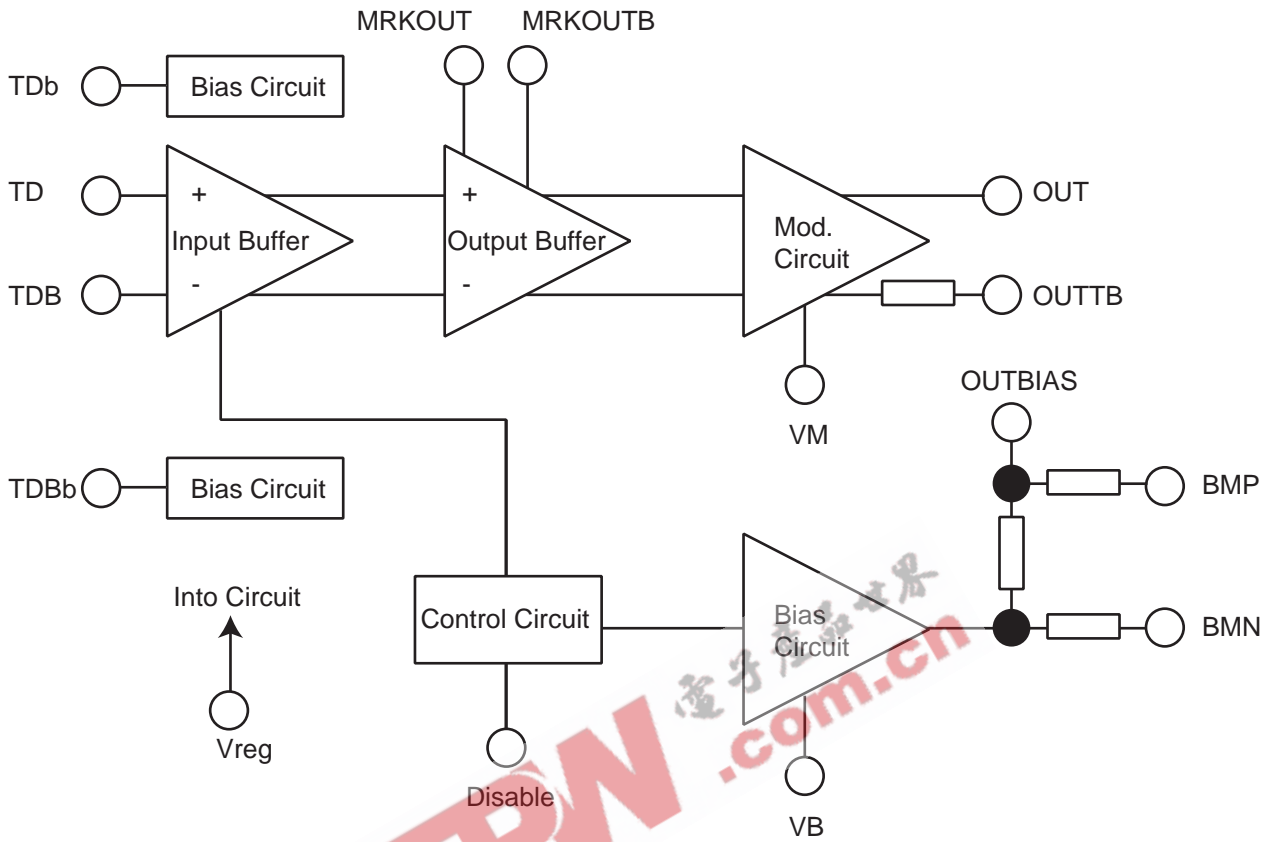
$T_a=25\text{ }^\circ\text{C}$ ,  $V_{DD}=0\text{ V}$ ,  $V_{SS}=-3.3\text{ V}$ , unless specified

Parameter	Symbol	Value			Units
		Min.	Typ.	Max.	
Supply Voltage	$V_{DD} - V_{SS}$	3.135	3.3	3.465	V
Output Voltage	$V_{out}$	$V_{DD} - 1.6$	$V_{DD} - 1.0$	$V_{DD}$	V
Input Voltage $V_{reg}$	$V_{reg}$	$V_{SS} + 1.86$	OPEN	$V_{SS} + 2.12$	V
Junction Operating Temperature	$T_j$	0	25	125	$^\circ\text{C}$

◆ **Electrical Characteristics**
 $T_a=25\text{ }^\circ\text{C}$ ,  $V_{DD}-V_{SS}=3.135\sim 3.465\text{V}$ , unless specified

Parameter	Symbol	Test Conditions	Value			Units
			Min.	Typ.	Max.	
Supply Current	I <sub>ckt</sub>	IMOD, IBIAS are excluded	-	35	50	mA
Input Voltage (for TD, TDB)	V <sub>IH</sub>	Differential Input	V <sub>DD</sub> -1.17	V <sub>DD</sub> -0.8	V <sub>DD</sub> -0.73	V
	V <sub>IL</sub>		V <sub>DD</sub> -1.95	V <sub>DD</sub> -1.8	V <sub>DD</sub> -1.45	V
Input Current	I <sub>IH</sub>	V <sub>IH</sub> =V <sub>DD</sub> -0.7V	-100	-	100	μA
	I <sub>IL</sub>	V <sub>IL</sub> =V <sub>DD</sub> -1.9V	-100	-	100	μA
Input Resistance	R <sub>i</sub>	DC, V <sub>DD</sub> =V <sub>SS</sub> =GND	1	1.3	-	kΩ
Input Bias Voltage	V <sub>IB</sub>	V <sub>DD</sub> -V <sub>SS</sub> =3.3V	V <sub>DD</sub> -1.17	V <sub>DD</sub> -1.3	V <sub>DD</sub> -1.43	V
Modulation Current	I <sub>MAX</sub>	V <sub>DIS</sub> =OPEN	35	-	-	mA
	I <sub>MIN</sub>	V <sub>DIS</sub> =OPEN	-	-	5	mA
	I <sub>DIS</sub>	V <sub>DIS</sub> =V <sub>DD</sub> -0.2V	-	-	0.5	mA
Bias Current	I <sub>BMAX</sub>	V <sub>DIS</sub> =OPEN	35	-	-	mA
	I <sub>BMIN</sub>	V <sub>DIS</sub> =OPEN	-	-	5	mA
	I <sub>BDIS</sub>	V <sub>DIS</sub> =V <sub>DD</sub> -0.2V	-	-	0.5	mA
Input Voltage for Disable	V <sub>DISIH</sub>	Disable Operation	V <sub>DD</sub> -2	-	V <sub>DD</sub>	V
	V <sub>DISIL</sub>	Enable Operation	V <sub>SS</sub>	OPEN	V <sub>SS</sub> +0.2	V
Resistance for Bias Monitor	R <sub>BM</sub>		-	10	-	Ω
Monitor Voltage of Mark Ratio	V <sub>MRK</sub>	Differential Output	-	0.9	-	V
Rise time	t <sub>r</sub>	R <sub>L</sub> =25Ω, 20%-80%	-	300	-	ps
Fall time	t <sub>f</sub>	R <sub>L</sub> =25Ω, 20%-80%	-	300	-	ps

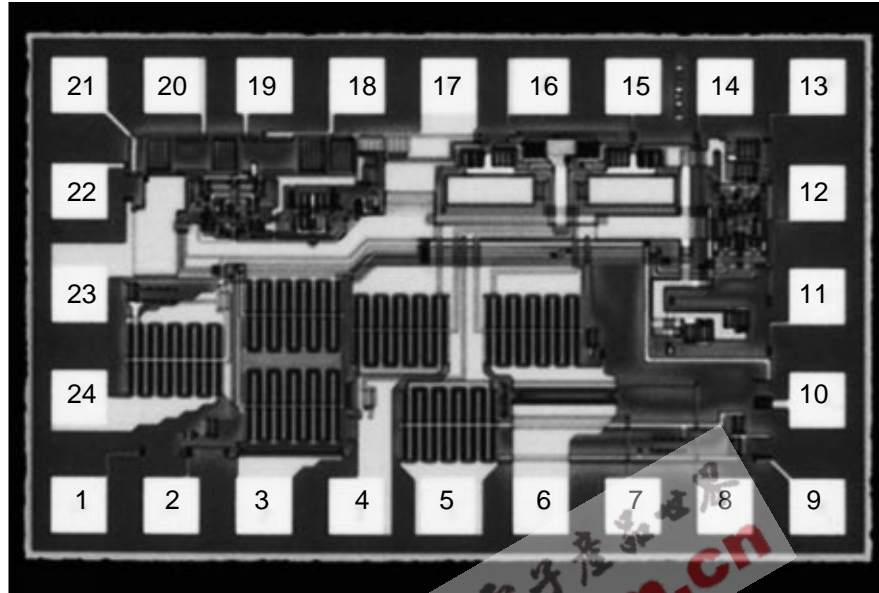
◆ **Block Diagram**



◆ **Die Pad Description**

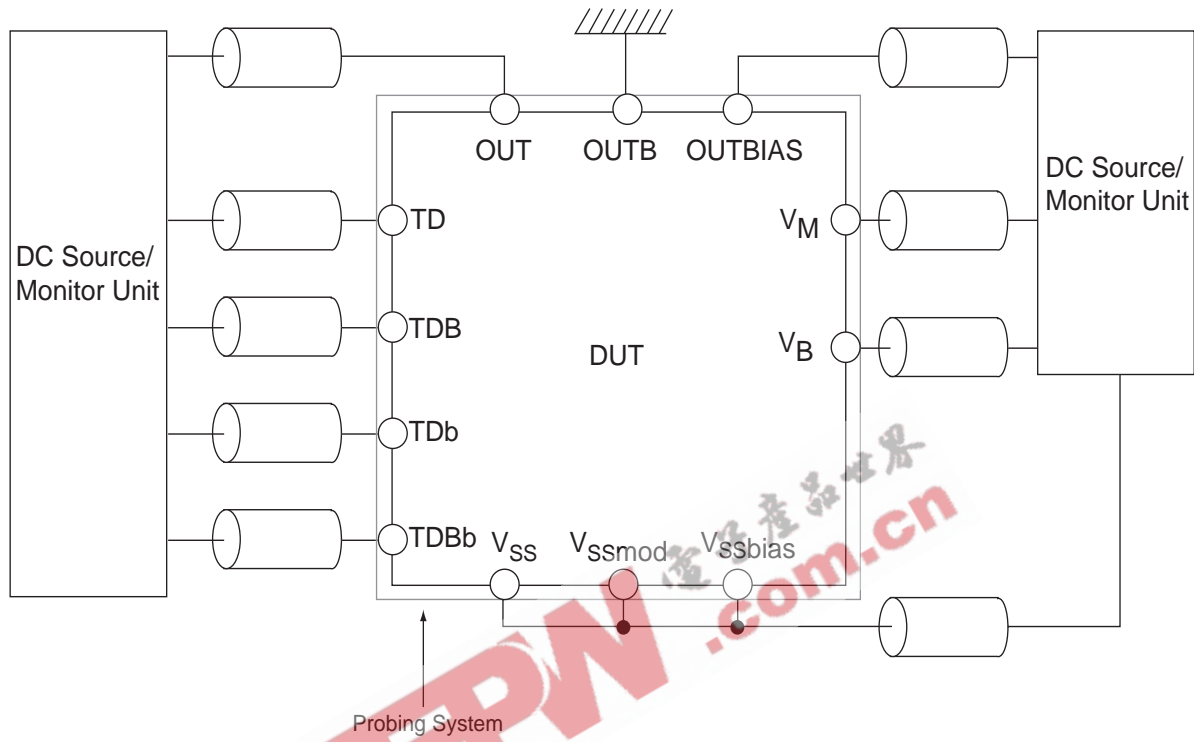
TD	Data Input (pos.)
TDB	Data Input (neg.)
TDb	Input Bias (pos.)
TDBb	Input Bias (neg.)
OUT	Modulation Current Output (pos.)
OUTB	Modulation Current Output (neg.)
OUTBIAS	Bias current Output
VM	Modulation Current Control
VB	Bias Current control
Disable	Current Shutdown Control
Vreg	Reference Voltage
BMP	Bias Current Monitor (pos.)
BMN	Bias Current Monitor (neg.)
MRKOUT	Mark ratio Monitor (pos.)
MRKOUTB	Mark ratio Monitor (neg.)

### ◆ Die Pad Assignments



No.	Symbol	Center Coordinates ( $\mu\text{m}$ )	No.	Symbol	Center Coordinates ( $\mu\text{m}$ )
1	BMN	(80,80)	15	ALMINB	(1040,810)
2	BMP	(240,80)	16	Vreg	(880,810)
3	OUTBIAS	(400,80)	17	V <sub>SS</sub>	(720,810)
4	OUT	(560,80)	18	TD	(560,810)
5	V <sub>SSmod</sub>	(720,80)	19	TD (bias)	(400,810)
6	OUTB	(880,80)	20	TDB	(240,810)
7	V <sub>DD</sub> TEMP	(1040,80)	21	TDB (bias)	(80,810)
8	V <sub>SS</sub> TEMP	(1200,80)	22	TD	(80,625)
9	VM	(1360,80)	23	V <sub>DD</sub>	(80,445)
10	VB	(1360,265)	24	V <sub>SS</sub> bias	(180,265)
11	MARKOUTB	(1360,445)			
12	MARKOUT	(1360,625)			
13	Disable	(1360,810)	O		(0,0)
14	ALMIN	(1200,810)	A		(1440,890)

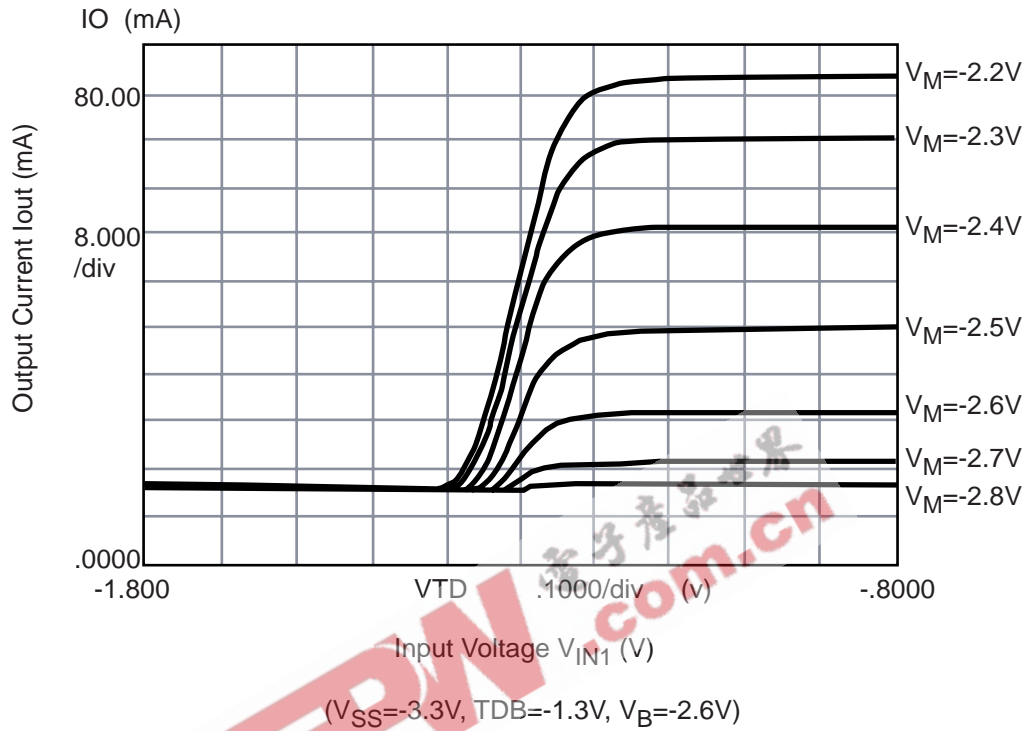
◆ Test Circuits



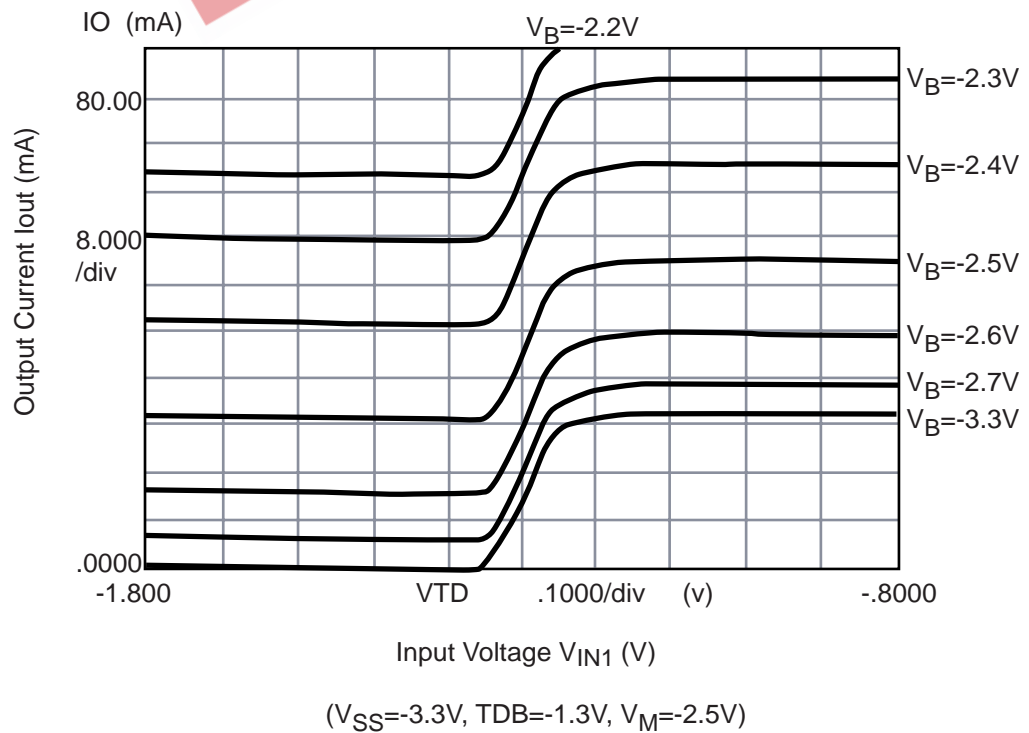
◆ Typical DC Characteristics

(1) Switching Characteristics

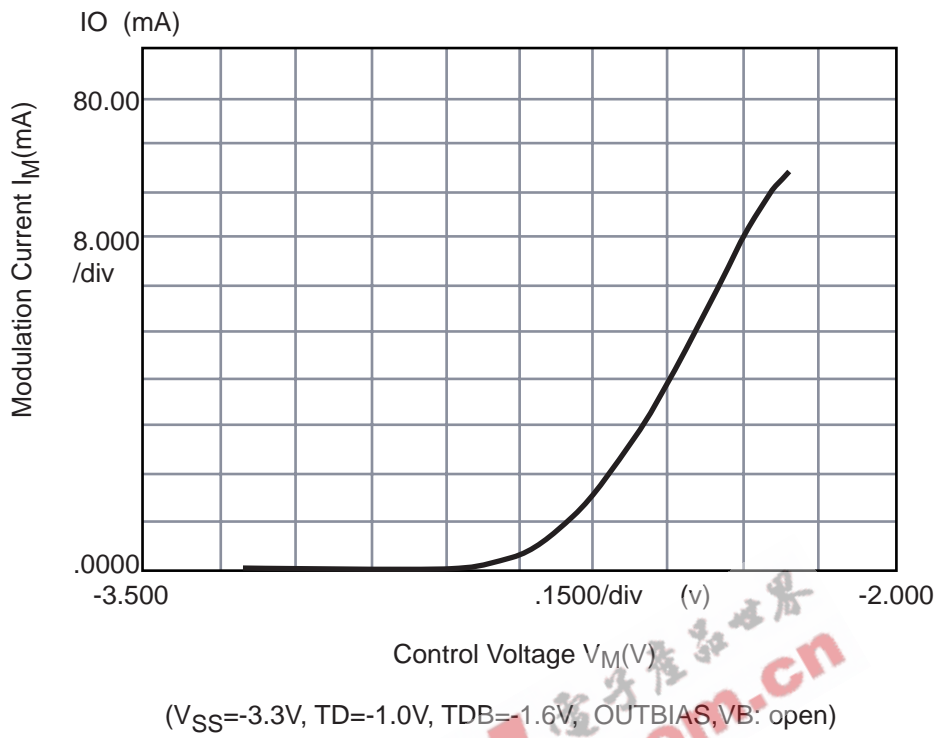
(a) Modulation Current Switching



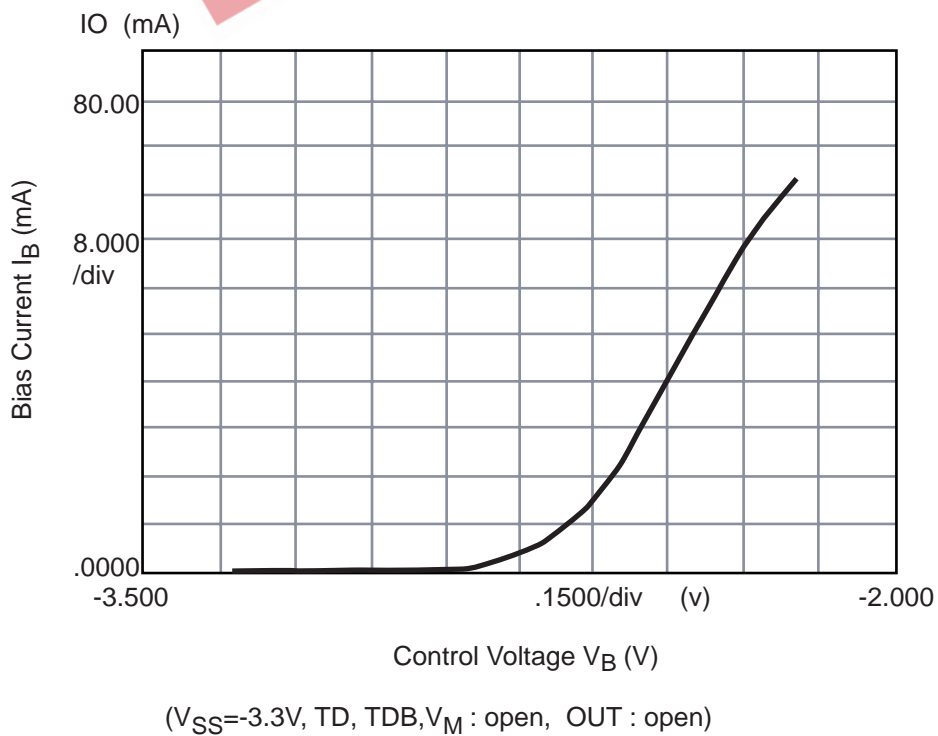
(b) Output Current Switching



(2) Modulation Current Control

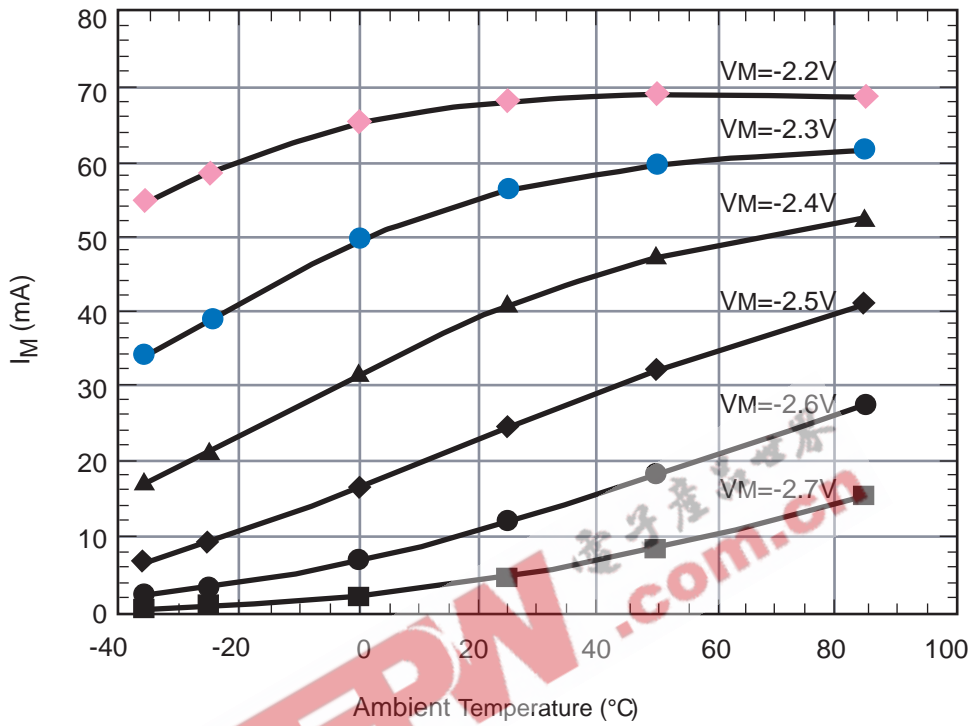


(3) Bias Current Control





(4) The Dependence of Modulation Current on the ambient temperature



( $V_{SS} = -3.3\text{V}$ ,  $T_D = -1.0\text{V}$ ,  $T_{DB} = -1.6\text{V}$ ,  $V_B$ ,  $\text{OUTBIAS}$ : open)