

**PART NUMBERING GUIDE**

Environmental/Mechanical Specifications on page F5

<b>OC-3.3- 100 48 A T - 30.000MHz</b>	
<b>Package</b> OC-3.3 = 5X7X1.6mm / 3.3Vdc / HCMOS-TTL OC-2.5 = 5X7X1.6mm / 2.5Vdc / HCMOS-TTL	<b>Pin One Connection</b> T = Tri State Enable High
<b>Inclusive Stability</b> 100= +/-100ppm, 50= +/-50ppm, 30= +/-30ppm, 25= +/-25ppm, 20= +/-20ppm, 15= +/-15ppm, 10= +/-10ppm (25,20,15,10= 0°C-70°C Only)	<b>Output Symmetry</b> Blank = 40/60%, A = 45/55%
	<b>Operating Temperature Range</b> Blank = -10°C to 70°C, 27 = -20°C to 70°C, 48 = -40°C to 85°C

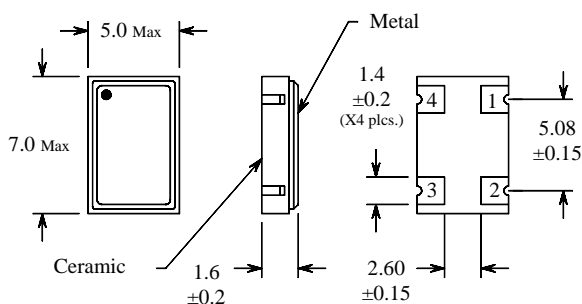
**ELECTRICAL SPECIFICATIONS**

Revision: 1996-G

<b>Frequency Range</b>	1.544MHz to 185.000MHz
<b>Operating Temperature Range</b>	-10°C to 70°C / -20°C to 70°C / -40°C to 85°C
<b>Storage Temperature Range</b>	-55°C to 125°C
<b>Supply Voltage</b>	2.5Vdc ±10% or 3.3Vdc ±10%
<b>Input Current</b>	1.544MHz to 36.000MHz: 10mA Maximum 36.001MHz to 70.000MHz: 18mA Maximum 70.001MHz to 185.000MHz: 40mA Maximum
<b>Frequency Tolerance / Stability</b>	Inclusive of Operating Temperature Range, Supply Voltage and Load: ±100ppm, ±50ppm, ±30ppm, ±25ppm, ±20ppm, ±15ppm or ±10ppm (25, 20, 15, 10 = 0°C to 70°C)
<b>Output Voltage Logic High (Voh)</b>	w/HCMOS or TTL Load: 90% of Vdd Min. / Ioh=-8mA
<b>Output Voltage Logic Low (Vol)</b>	w/HCMOS or TTL Load: 10% of Vdd Max. / Iol=8mA
<b>Rise / Fall Time</b>	10% to 90% of Waveform w/HCMOS Load; 0.4Vdc to 2.4V w/TTL Load / 5nSec Max.
<b>Duty Cycle</b>	@1.4Vdc w/TTL Load; @50% w/HCMOS Load: 50 ±10% (Standard) @1.4Vdc w/TTL Load or w/HCMOS Load: 50±5% (Optional)
<b>Load Drive Capability</b>	1.544MHz - 36.000MHz: 10TTL Load or 30pF / HCMOS Load Max. 36.0001MHz - 70.000MHz: 10TTL Load or 15pF / HCMOS Load Max. 70.0001MHz - 185.000MHz: 15pF / HCMOS Load Max.
<b>Pin 1 Tristate Input Voltage</b>	No Connection VIH: Enables Output VIL: +0.7Vdc Minimum to Enable Output +0.3Vdc Maximum to Disable Output
<b>Aging (@ 25°C)</b>	±5ppm / year Maximum
<b>Start Up Time</b>	10mSeconds Maximum
<b>Stand By Current</b>	10uA Maximum (Disabled Output, High Imp)
<b>RMS Phase Jitter</b>	1pSec Maximum / 12kHz to 20.000MHz offset

**MECHANICAL DIMENSIONS**

**Marking Guide**



All Dimensions in mm.

Line 1: 3T- Frequency  
Line 2: CEI YM

3 = 3.3V designator  
T = Tristate  
CEI = Caliber Electronics Inc.  
YM = Date Code (Year / Month)

Pin 1: Tri-State  
Pin 2: Case Ground

Pin 3: Output  
Pin 4: Supply Voltage