

ST3243B ST3243C

3 to 5.5 V, 400 Kbps, RS-232 transceiver with auto-power-down

Features

- 1 μA supply current achieved when in autopower-down
- 250 kbps minimum guaranteed data rate
- Guaranteed 6 V/µs slew rate range
- Guaranteed mouse drive ability
- 0.1 µF external capacitors
- Meet EIA/TIA-232 specifications down to 3 V
- Available in SSOP-28 and TSSOP28

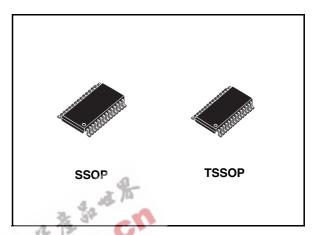


The ST3243 device consists of 3 drivers, 5 receivers and a dual charge-pump circuit. The device meets the requirements of EIA/TIA and V.28/V.24 communication standards providing high data rate capability.

The receiver R2 is always active to implement a wake-up feature for serial port.

The ST3243 has a proprietary low-dropout transmitter output stage enabling true RS-232 performance from a 3.0 V to 5.5 V supply with a dual charge pump. The device is guaranteed to run at data rates of 250 kbps while maintaining RS-232 output levels.

The Auto-power-down feature functions when FORCEON is low and FORCEOFF is high. During this mode of operation, if the device does not sense a valid RS-232 signal, the driver outputs are disabled. If FORCEOFF is set low, both drivers and receivers (expert R2B) are shut off, and supply current is reduced to 1 mA. Disconnecting the serial port or turning off the



peripheral drives causes the auto-power-down condition to occur.

Auto-power-down can be disabled when FORCEON and FORCEOFF are high, and should be done when driving a serial mouse. With Auto-power-down enabled, the device is activated automatically when a valid signal is applied to any receiver input.

Typical application are in notebook, subnotebook, palmtop computers, battery-powered equipment, hand-held equipment, peripherals and printers.

Table 1. Device summary

Order code	Temperature range	Package	Packaging
ST3243CPR	0 to 70 °C	SSOP-28 (tape and reel)	1350 parts per reel
ST3243BTR	-40 to 85 °C	TSSOP28 (tape and reel)	2500 parts per reel

Contents ST3243B - ST3243C

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ST3243B - ST3243C Pin configuration

1 Pin configuration

Figure 1. Pin configuration

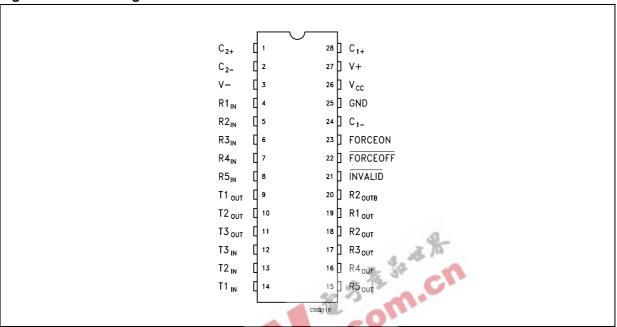


Table 2. Pin description

Pin n°	Symbol	Name and function
1	C ₂ +	Positive terminal of inverting charge pump capacitor
2	C ₂ -	Negative terminal of inverting charge pump capacitor
3	V-	-5.5V Generated by the charge pump
4	R1 _{IN}	First receiver input voltage
5	R2 _{IN}	Second receiver input voltage
6	R3 _{IN}	Third receiver input voltage
7	R4 _{IN}	Fourth receiver input voltage
8	R5 _{IN}	Fifth receiver input voltage
9	T1 _{OUT}	First transmitter output voltage
10	T2 _{OUT}	Second transmitter output voltage
11	T3 _{OUT}	Third transmitter output voltage
12	T3 _{IN}	Third transmitter input voltage
13	T2 _{IN}	Second transmitter input voltage
14	T1 _{IN}	First transmitter input voltage
15	R5 _{OUT}	Fifth receiver output voltage
16	R4 _{OUT}	Fourth receiver output voltage

Pin configuration ST3243B - ST3243C

Table 2. Pin description (continued)

Pin n°	Symbol	Name and function
17	R3 _{OUT}	Third receiver output voltage
18	R2 _{OUT}	Second receiver output voltage
19	R1 _{OUT}	First receiver output voltage
20	R2 _{OUTB}	Non-inverting complementary receiver output, always active for wake-up
21	INVALID	Output of the valid signal detector. Indicates if a valid RS-232 level is present on receiver inputs logic "1"
22	FORCEOFF	Drive low to shut down transmitters and on-board power supply. This over-rides all automatic circuitry and FORCEON
23	FORCEON	Drive high to override automatic circuitry keeping transmitters on (FORCEOFF must be high)
24	C ₁ -	Negative terminal of voltage-charge pump capacitor
25	GND	Ground
26	V _{CC}	Supply voltage
27	V+	5.5V Generated by the charge pump
28	C ₁ +	Positive terminal of voltage-charge pump capacitor

Table 3. Truth table

FORCE OFF	Тоит	R _{OUT}	R _{2OUTB}
0	HIGH Z	HIGH Z	ACTIVE (1)
1	ACTIVE (1)	ACTIVE (1)	ACTIVE (1)

If the part is in auto-power-down mode (FORCE OFF = V_{CC}, FORCE ON = GND) it is shutdown, if no valid RS-232 levels are present on all receiver input

ST3243B - ST3243C Maximum ratings

2 Maximum ratings

Table 4. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{CC}	Supply voltage	-0.3 to 6	V
V+	Doubled voltage terminal	(V _{CC} -0.3) to 7	٧
V-	Inverted voltage terminal	0.3 to -7	٧
V+ + V-		13	٧
FORCEON, FORCEOFF, T _{IN}	Input voltage	-0.3 to 6	V
R _{IN}	Receiver input voltage range	±25	V
T _{OUT}	Transmitter output voltage range	± 13.2	٧
R _{OUT} R _{OUTB} INVALID	Receiver output voltage range	-0.3 to (V _{CC} + 0.3)	V
t _{SHORT}	Short circuit duration on T _{OUT} (one at a time)	Continuous	
T _{stg}	Storage temperature range	-65 to 150	°C

Note: 1 Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional operation under these condition is not implied. V+ and V- can have a maximum magnitude of +7 V, but their absolute addition can not exceed 13 V.

2 The device doesn't meet 1 kV ESD HBM

3 Electrical characteristics

Table 5. Electrical characteristics

(C $_1$ - C $_4$ = 0.1 $\mu F,~V_{CC}$ = 3 V to 5.5 V, T $_A$ = -40 to 85 °C, unless otherwise specified. Typical values are referred to T $_A$ = 25 °C

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{ASHDN}	Supply current auto-power-down	V_{CC} = 3.3 or 5.0V, T_A = 25°C All R_IN open, FORCEOFF = V_{CC}		1	10	μΑ
I _{SHDN}	Shutdown supply current	V _{CC} = 3.3 or 5.0V, T _A = 25°C All R_IN open, FORCEOFF = GND		1	10	μΑ
I _{SHDN}	Supply current auto-power-down disabled	$V_{CC} = 3.3 \text{ or } 5.0 \text{V}$ $T_A = 25^{\circ}\text{C}$ FORCEON = FORCEOFF = V_{CC} No Load		0.3	1	mA

Table 6. Logic input electrical characteristics

(C1 - C4 = 0.1 μ A, VCC = 3 V to 5.5 V, TA = -40 to 85 °C, unless otherwise specified. Typical values are referred to TA = 25 °C

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V_{TIL}	Input logic threshold low	T-IN, FORCEON, FORCEOFF			0.8	V
V _{TIH}	Input logic threshold high	T-IN, FORCEON, FORCEOFF $V_{CC} = 3.3V$ $V_{CC} = 5V$	2 2.4			V V
V _{THYS}	Transmitter input hysteresis			0.5		V
I _{IL}	Input leakage current	T-IN, FORCEON, FORCEOFF		±0.01	±1.0	μΑ
I _{OL}	Output leakage current	Receiver Disabled		± 0.05	±10	μΑ
V _{OL}	Output voltage low	I _{OUT} = 1.6mA			0.4	V
V _{OH}	Output voltage high	I _{OUT} = -1mA	V _{CC} -0.6	V _{CC} -0.1		V

Table 7. Auto-power-down electrical characteristics

(C₁ - C₄ = 0.1 μ F, V_{CC} = 3 V to 5.5 V, T_A = -40 to 85 °C, unless otherwise specified. Typical values are referred to T_A = 25 °C

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{RITE}	Receiver input threshold to transmitter enabled	Positive threshold Negative threshold	2.7		2.7	V V
V _{RITD}	Receiver input threshold to transmitter disabled	1 μA Supply Current	-0.3		0.3	V
V _{IOL}	INVALID Output voltage LOW				0.4	V
V _{IOH}	INVALID Output voltage HIGH		V _{CC} -0.6			V
t _{WU}	Receiver threshold to transmitter enabled	I _{OUT} = 1.6mA		250		ms

Table 7. Auto-power-down electrical characteristics

(C₁ - C₄ = 0.1 μ F, V_{CC} = 3 V to 5.5 V, T_A = -40 to 85 °C, unless otherwise specified. Typical values are referred to T_A = 25 °C

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _{invh}	Receiver positive or negative threshold to INVALID HIGH	I _{OUT} = -1mA		1		ms
t _{invL}	Receiver positive or negative threshold to INVALID LOW			30		ms

Table 8. Transmitter electrical characteristics

(C1 - C4 = 0.1 μ F, VCC = 3 V to 5.5 V, TA = -40 to 85°C, unless otherwise specified. Typical values are referred to TA = 25 °C

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{TOUT}	Output voltage swing	All transmitter outputs are loaded with 3KΩto GND	±5	±5.4		٧
R _{OUT}	Output Resistance	$V_{CC} = V + = V - = 0V, V_{OUT} = \pm 2V$	300	10M		Ω
I _{SC}	Output short circuit current	2 44		±35	±60	mA
V _{OT}	Transmitter output voltage	T1IN = T2IN = GND, T3IN = V_{CC} T3OUT loaded with 3K Ω to GND T1OUT and T2OUT loaded with 2.5mA each	±5			V

Table 9. Receiver electrical characteristics

(C₁ - C₄ = 0.1 μ F, V_{CC} = 3 V to 5.5 V, T_A = -40 to 85 °C, unless otherwise specified. Typical values are referred to T_A = 25 °C

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{RIN}	Receiver input voltage operating range		-25		25	V
V _{RIL}	RS-232 Input threshold low	$T_A = 25$ °C, $V_{CC} = 3.3V$ $T_A = 25$ °C, $V_{CC} = 5.0V$	0.6 0.8	1.2 1.2		V
V _{RIH}	RS-232 Input threshold high	T _A = 25°C, V _{CC} = 3.3V T _A = 25°C, V _{CC} = 5.0V		1.5 1.8	2.4 2.4	V
V _{RIHYS}	Input hysteresis			0.5		V
R _{RIN}	Input resistance	T _A = 25°C	3	5	7	kΩ

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Electrical characteristics ST3243B - ST3243C

Table 10. Timing characteristics

(C₁ - C₄ = 0.1 μ F, V_{CC} = 3 V to 5.5 V, T_A = -40 to 85 °C, unless otherwise specified. Typical values are referred to T_A = 25 °C

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
D _R	Maximum data rate	$R_L = 3K\Omega$ $C_L = 1000pF$ one transmitter switching	250	400		kbps
t _{PHL}	Receiver propagation delay	R _{IN} to R _{OUT} , C _L = 150pF		0.15		μs
t _{T_SKEW}	Transmitter skew			100		ns
t _{R_SKEW}	Receiver skew			50		ns
t _{INVH}	Receiver positive or negative threshold to INVALID HIGH			1		ms
t _{INVL}	Receiver positive or negative threshold to INVALID LOW			30		ms
S _{RT}	Transition slew rate	$T_A=25^{\circ}C R_L=3K \text{ to } 7K\Omega \qquad V_{CC}=3.3V$ measured from +3V to -3V or -3V to +3V $C_L=150 pF$ to $1000 pF$ $C_L=150 pF$ to $2500 pF$	6 4		30 30	V/µs V/µs
Θ[= 130β1 to 2300β1 4 00 V/μs						

ST3243B - ST3243C Application

4 Application

Figure 2. Application circuits

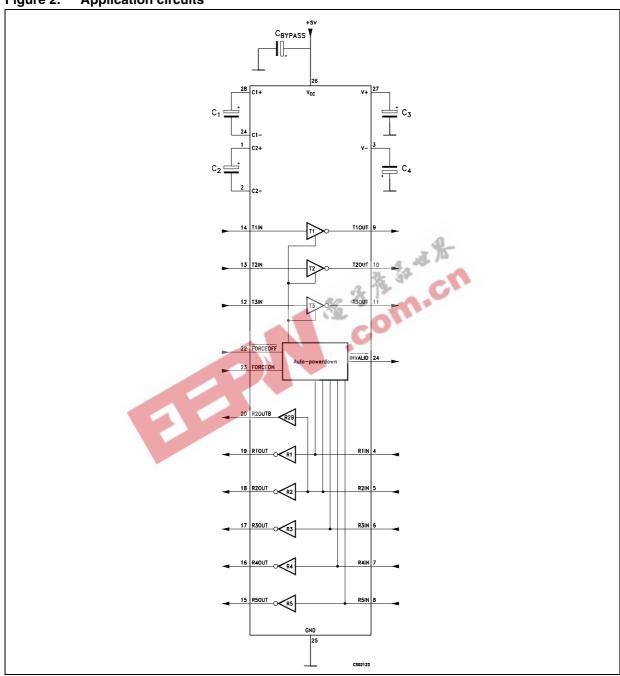


Table 11. Capacitance value (μ F)

V _{CC}	C1	C2	C3	C4	Cbypass
3.0 to 3.6	0.1	0.1	0.1	0.1	0.1



Application ST3243B - ST3243C

Table 11. Capacitance value (μ F)

v _{cc}	C1	C2	C3	C4	Cbypass
4.5 to 5.5	0.047	0.33	0.33	0.33	0.33
3.0 to 5.5	0.22	1.0	1.0	1.0	0.22



5 Typical performance characteristics

Unless otherwise specified $T_J = 25$ °C

Figure 3. Invalid high threshold time

t₀(μs)

80

60

40

20

-50 -25 0 25 50 75 100 T_A(°C)

Figure 4. Invalid low threshold time

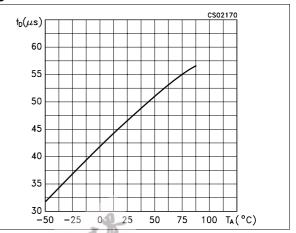
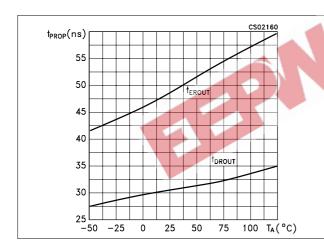


Figure 5. Receiver propagation delay

Figure 6. Receiver output enable & disable



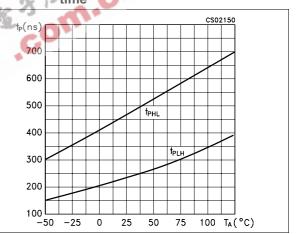


Figure 7. Output current vs output high voltage

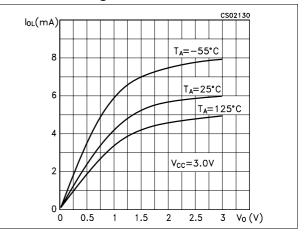
T_A=25°C

T_A=25°C

T_A=25°C

T_A=3.5 -3 -2.5 -2 -1.5 -1 -0.5V_{CC}-V_{OH}(V)

Figure 8. Output current vs output low voltage





6 Package mechanical data

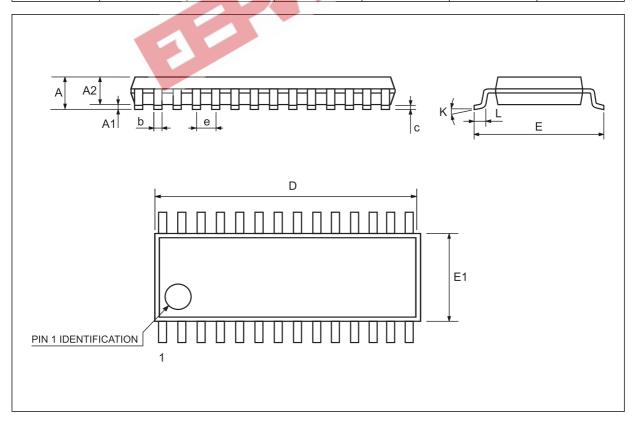
In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com





SSOP28 mechanical data

Dim.	mm.			inch.		
Dilli.	Min.	Тур.	Max.	Min.	Тур.	Max.
А			2			0.079
A1	0.050			0.002		
A2	1.65	1.75	1.85	0.065	0.069	0.073
b	0.22		0.38	0.009		0.015
С	0.09		0.25	0.004		0.010
D	9.9	10.2	10.5	0.390	0.402	0.413
E	7.4	7.8	8.2	0.291	0.307	0.323
E1	5	5.3	5.6	0.197	0.209	0.220
е		0.65 BSC	36	5	0.0256 BSC	
К	0°		10°	0°		10°
L	0.55	0.75	0.95	0.022	0.030	0.037

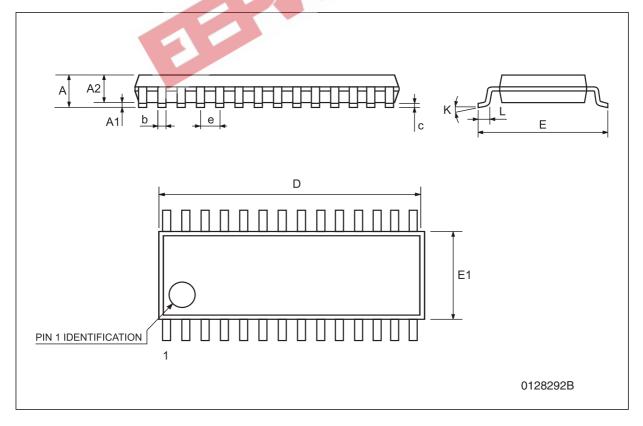


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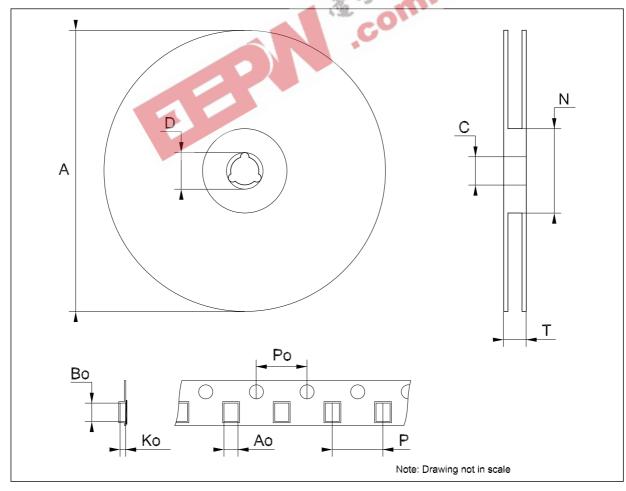
TSSOP28 mechanical data

Dim.	mm.			inch.		
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α			1.2			0.047
A1	0.05		0.15	0.002	0.004	0.006
A2	0.8	1	1.05	0.031	0.039	0.041
b	0.19		0.30	0.007		0.012
С	0.09		0.20	0.004		0.0079
D	9.6	9.7	9.8	0.378	0.382	0.386
E	6.2	6.4	6.6	0.244	0.252	0.260
E1	4.3	4.4	4.48	0.169	0.173	0.176
е		0.65 BSC	36	5 1000	0.0256 BSC	
К	0°		8°	0°		8°
L	0.45	0.60	0.75	0.018	0.024	0.030



Tape & reel SSOP28 mechanical data

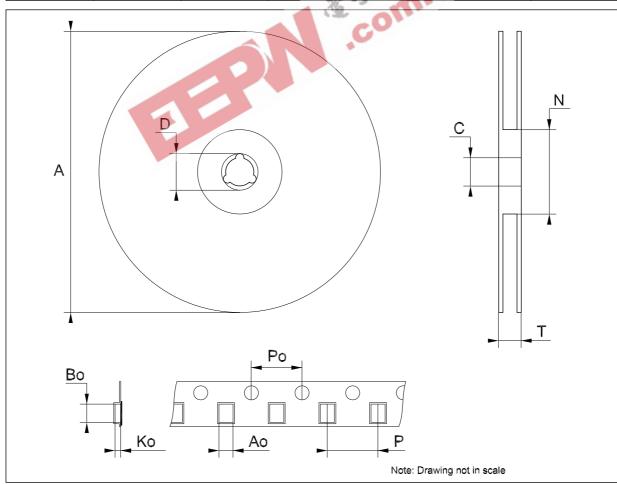
Dim.	mm.			inch.		
Dilli.	Min.	Тур.	Max.	Min.	Тур.	Max.
А			330			12.992
С	12.8		13.2	0.504		0.519
D	20.2			0.795		
N	60			2.362		
Т			22.4			0.882
Ao	8.4		8.6	0.331		0.339
Во	10.7		10.9	0.421		0.429
Ko	2.9		3.1	0.114		0.122
Po	3.9		4.1	0.153		0.161
Р	11.9		12.1	0.468	1.0	0.476





Tape & reel TSSOP28 mechanical data

Dim.	mm.			inch.		
Dilli.	Min.	Тур.	Max.	Min.	Тур.	Max.
А			330			12.992
С	12.8		13.2	0.504		0.519
D	20.2			0.795		
N	60			2.362		
Т			22.4			0.882
Ao	6.8		7	0.268		0.276
Во	10.1		10.3	0.398		0.406
Ko	1.7		1.9	0.067		0.075
Po	3.9		4.1	0.153		0.161
Р	11.9		12.1	0.468		0.476



ST3243B - ST3243C Revision history

7 Revision history

Table 12. Document revision history

Date	Revision	Changes			
19-Sep-2004	6	Document updating.			
31-Mar-2006	7	Order codes updated.			
12-Nov-2007	8	Added Table 1.			



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