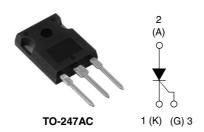


Vishay High Power Products

### Phase Control SCR, 35 A



PRODUCT SUMMARY					
V <sub>T</sub> at 40 A	< 1.45 V				
I <sub>TSM</sub>	500 A				
V <sub>RRM</sub>	800/1200 V				

#### **DESCRIPTION/FEATURES**

The 40TPS...APbF High Voltage Series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology used has reliable operation up to 125 °C junction temperature. Low lgt parts available.



Typical applications are in input rectification (soft start) and these products are designed to be used with Vishay HPP input diodes, switches and output rectifiers which are available in identical package outlines.

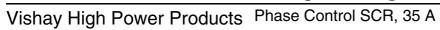
This product has been designed and qualified for industrial level and lead (Pb)-free ("PbF" suffix).

MAJOR RATINGS AND CHARACTERISTICS							
PARAMETER	TEST CONDITIONS	VALUES	UNITS				
I <sub>T(AV)</sub>	Sinusoidal waveform	35	А				
I <sub>RMS</sub>		55	^				
V <sub>RRM</sub> /V <sub>DRM</sub>		800/1200	V				
I <sub>TSM</sub>		500	Α				
V <sub>T</sub>	40 A, T <sub>J</sub> = 25 °C	1.45	V				
dV/dt		1000	V/μs				
dI/dt		100	A/μs				
TJ		- 40 to 125	°C				

VOLTAGE RATINGS								
PART NUMBER	V <sub>RRM</sub> /V <sub>DRM</sub> , MAXIMUM REPETITIVE PEAK AND OFF-STATE VOLTAGE V	V <sub>RSM</sub> , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I <sub>RRM</sub> /I <sub>DRM</sub> AT 125 °C mA					
40TPS08APbF	800	900						
40TPS12APbF	1200	1300	10					
40TPS08PbF	800	900	10					
40TPS12PbF	1200	1300						

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<sup>\*</sup> Pb containing terminations are not RoHS compliant, exemptions may apply





ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average on-state current	I <sub>T(AV)</sub>	T <sub>C</sub> = 79 °C, 180° conduction half sine w	vave	35		
Maximum continuous RMS on-state current as AC switch	I <sub>T(RMS)</sub>			55	Α	
Maximum peak, one-cycle	I <sub>TSM</sub>	10 ms sine pulse, rated V <sub>RRM</sub> applied		500		
non-repetitive surge current	TISM	10 ms sine pulse, no voltage reapplied		600		
Maximum I <sup>2</sup> t for fusing	l <sup>2</sup> t	10 ms sine pulse, rated $V_{\mbox{\scriptsize RRM}}$ applied	Initial $T_J = T_{-1}$ maximum	1250	A <sup>2</sup> s	
waxiinum i-t ioi iusiing	1-1	10 ms sine pulse, no voltage reapplied	<b>J</b>	1760		
Maximum I <sup>2</sup> √t for fusing	I <sup>2</sup> √t	t = 0.1 to 10 ms, no voltage reapplied		12 500	A²√s	
Low level value of threshold voltage	V <sub>T(TO)1</sub>			1.02	V	
High level value of threshold voltage	V <sub>T(TO)2</sub>	T <sub>J</sub> = 125 °C	1.23	v		
Low level value of on-state slope resistance	r <sub>t1</sub>	1j = 125 C		9.74	mΩ	
High level value of on-state slope resistance	r <sub>t2</sub>			7.50		
Maximum peak on-state voltage	$V_{TM}$	110 A, T <sub>J</sub> = 25 °C		1.85	V	
Maximum rate of rise of turned-on current	dl/dt	T <sub>J</sub> = 25 °C		100	A/μs	
Maximum holding current	I <sub>H</sub>	23		150		
Maximum latching current	ΙL	132		300	A	
Maximum reverse and direct leakage current	1 1	T <sub>J</sub> = 25 °C V <sub>B</sub> = Rated V <sub>BRM</sub> /V <sub>DBM</sub>		0.5	mA	
	I <sub>RRM</sub> /I <sub>DRM</sub>	T <sub>J</sub> = 125 °C	10			
Maximum rate of rise of off-state voltage 40TPS08	dV/dt $T_1 = T_1$ maximum, linear to 80 % $V_{DBM}$ , $R_0$ -k = Open		500	V/μs		
Maximum rate of rise of off-state voltage 40TPS12			= 13 maximum, ililear to 60 % v <sub>DRM</sub> , n <sub>g</sub> -k = Open			

TRIGGERING					
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS
Maximum peak gate power	P <sub>GM</sub>			10	w
Maximum average gate power	P <sub>G(AV)</sub>			2.5	, vv
Maximum peak gate current	I <sub>GM</sub>			2.5	Α
Maximum peak negative gate voltage	- V <sub>GM</sub>			10	V
	V <sub>GT</sub>	T <sub>J</sub> = - 40 °C	Anode supply = 6 V resistive load	4.0	V
Maximum required DC gate voltage to trigger		T <sub>J</sub> = 25 °C		2.5	
vollage to ingget		T <sub>J</sub> = 125 °C		1.7	
	I <sub>GT</sub>	T <sub>J</sub> = - 40 °C		270	mA
Maximum required DC gate current to trigger		T <sub>J</sub> = 25 °C		150	
		T <sub>J</sub> = 125 °C		80	
		$T_J = 25$ °C, for 40TPS08APbF and 40TPS12APbF		40	
Maximum DC gate voltage not to trigger V <sub>GD</sub>		T <sub>J</sub> = 125 °C, V <sub>DRM</sub> = Rated value		0.25	V
Maximum DC gate current not to trigger	I <sub>GD</sub>			6	mA

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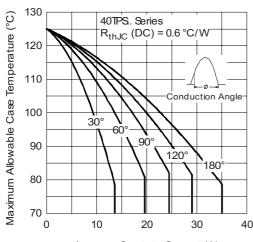
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PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and sto temperature range	rage	T <sub>J</sub> , T <sub>Stg</sub>		- 40 to 125	°C	
Maximum thermal resistance, junction to case		R <sub>thJC</sub> DC operation		0.6		
Maximum thermal resistan junction to ambient	ce,	R <sub>thJA</sub>	- DC operation	40		
Maximum thermal resistan case to heatsink	ce,	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.2	1	
Approximate weight				6	g	
Approximate weight				0.21	oz.	
Mounting torque	minimum			6 (5)	kgf · cm	
wounting torque	maximum			12 (10)	(lbf · in)	
				40TP	S08A	
Mandan and a day		Case style TO-247AC		40TPS12A		
Marking device				40TPS08		
				40TF	40TPS12	
			J.Co.			

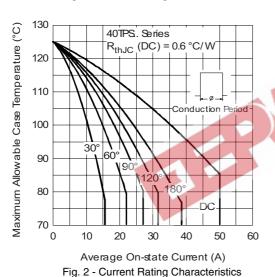
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# Vishay High Power Products Phase Control SCR, 35 A





Average On-state Current (A) Fig. 1 - Current Rating Characteristics



Maximum Average On-state Power Loss(W) 40 30 20 Conduction Angle 40TPS. Series 10 T<sub>J</sub>= 125°C

15 20

Average On-state Current (A) Fig. 3 - On-State Power Loss Characteristics

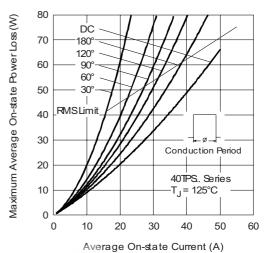


Fig. 4 - On-State Power Loss Characteristics



Number Of Equal Amplitude Half Cycle Current Pulses (N) Fig. 5 - Maximum Non-Repetitive Surge Current

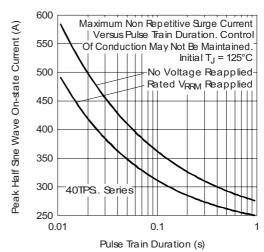


Fig. 6 - Maximum Non-Repetitive Surge Current

60

50

0

180

120°

909

60°

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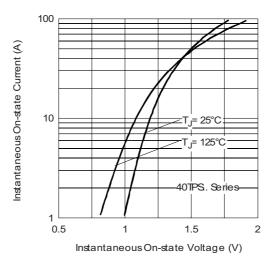
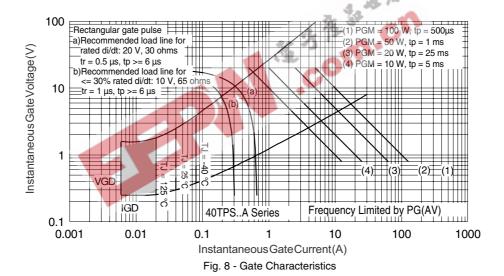


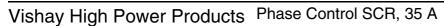
Fig. 7 - On-State Voltage Drop Characteristics



O.001 0.001 0.01 0.1 1

Square Wave Pulse Duration (s)

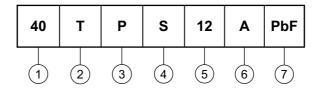
Fig. 9 - Thermal Impedance  $Z_{thJC}$  Characteristics





#### **ORDERING INFORMATION TABLE**

**Device code** 



- 1 Current rating (40 = 40 A)
- 2 Circuit configuration:

T = Thyristor

3 - Package:

P = TO-247

4 - Type of silicon:

S = Standard recovery rectifier

08 = 800 V 12 = 1200 V

- 5 Voltage ratings
  - • A = Low Igt selection 40 mA maximum
    - None = Standard lgt selection
- • None = Standard production
  - PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS					
Dimensions				http://www.vishay.com/doc?95223	
Part marking information				http://www.vishay.com/doc?95226	





Vishay

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